Shenzhen Leadtek Electronics Co.,Ltd

PRODUCT SPECIFICATION

TFT-LCD MODULE

Module No：LTK035HVCCT34-V0

☑ Preliminary Specification

☐ Approval Specification

Designed by	Checked by	Approved by
		

Final Approval by Customer

Approved by	Comment

※The specification of “TBD” should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

1.Document Revision History

Version	Contents	Date	Note
V0	Original	2023.10.06	

2.GENERAL INFORMATION

Item	Specification	Unit
LCD size	3.5	inch
Display Mode	Normally White	--
Resolution	320(RGB)x480	Pixel
Pixel pitch	0.153 (H) x 0.153 (V)	mm
Pixel Arrangement	RGB Vertical Stripe	
Viewing direction	12 O'clock	-
CTP+LCM Module outline dimension	69.50(H)* 116.50 (V)*4.17(D)	mm
LCD AA	48.96 (H)* 73.44 (V)	mm
Colors	262K	-
Weight	-	g
Driver IC	ILI9488	-
Interface	4_SPI	--
Backlight	White LED	--
Touch IC	GT911	--
Surface hardness	6H	--
Operating Temperature	-10℃~ +60℃	--
Storage Temperature	-20℃~ +70℃	--

3.ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit	Note
Power Supply voltage 1	VCI~GND	-0.3	+3.6	V	
Power Supply voltage 2	IOVCC~GND	-0.3	+3.6	V	
Logic Input Voltage Range	V_{IN}	-0.3	IOVCC+0.5	V	
Logic Output Voltage Range	V_O	-0.3	IOVCC+0.5	V	
Operating temperature	Topr	-10	+60	℃	
Storage temperature	Tstg	-20	+70	℃	

* The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

4.DC ELECTRICAL CHARACTERISTICS

4.1 Driving TFT LCD Panel

AGND = GND = 0V, Ta = 25℃

Parameter	Symbol	Min	Typ	Max	Unit
Supply voltage for analog circuit	VCI	2.5	2.8	3.3	V
Supply voltage for logic circuit	IOVCC	1.65	1.8	3.3	V
Input voltage 'H'level	V_{IH}	0.7*IOVCC	—	IOVCC	V
Input voltage 'L'level	V_{IL}	GND	—	0.3*IOVCC	V
Output voltage 'H'level	V_{OH}	0.8*IOVCC	—	IOVCC	V
Output voltage 'L'level	V_{OL}	GND	—	0.2*IOVCC	V

4.2 Backlight Characteristics

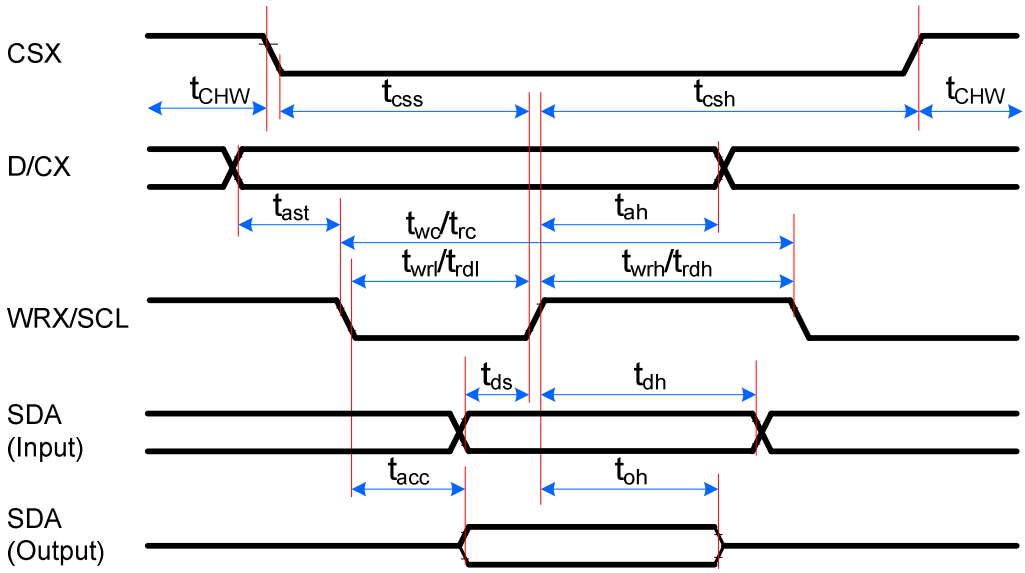
Ta = 25℃

Item	Symbol	Min	Typ	Max	Unit	Condition
Forward voltage	Vf	3.0	3.2	3.4	V	If=120 mA
Luminance	LV	-	320	—	cd/㎡	
Number of LED	-	1X2			Piece	-
Connection mode	S/P	1Serial/2Parallel			-	-

Using condition: constant current driving method If= 6×20mA (+/-10%)

5.TIMING CHARACTERISTICS

5.1 4-SPI Serial Data Transfer Interface Characteristics



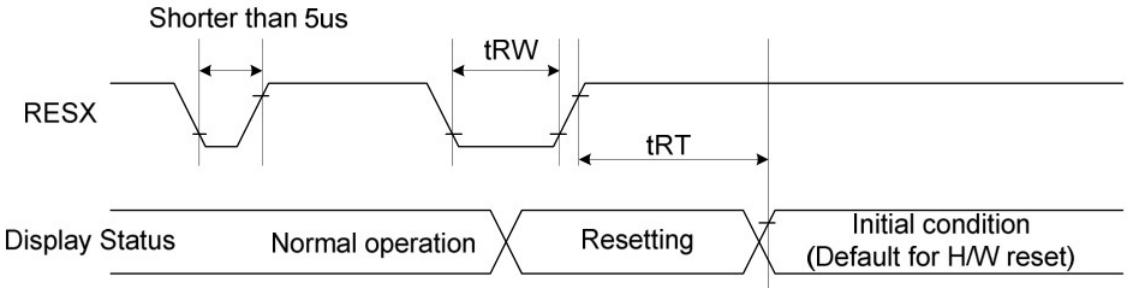
Signal	Symbol	Parameter	min	max	Unit	Description
CSX	tcss	Chip select time (Write)	15	-	ns	
	tcsh	Chip select hold time (Read)	15	-	ns	
	tCHW	CS H pulse width	40	-	ns	
SCL	twc	Serial clock cycle (Write)	50	-	ns	
	twrh	SCL H pulse width (Write)	10	-	ns	
	twrl	SCL L pulse width (Write)	10	-	ns	
	trc	Serial clock cycle (Read)	150	-	ns	
	trdh	SCL H pulse width (Read)	60	-	ns	
	trdl	SCL L pulse width (Read)	60	-	ns	
D/CX	tas	D/CX setup time	10	-	ns	
	tah	D/CX hold time (Write/Read)	10	-	ns	
SDA/SDI (Input)	tds	Data setup time (Write)	10	-	ns	
	tdh	Data hold time (Write)	10	-	ns	
SDA/SDO (Output)	tacc	Access time (Read)	10	50	ns	For maximum CL=30pF For minimum CL=8pF
	tod	Output disable time (Read)	15	50	ns	

Notes:

1. Ta = -30 to 70 ℃, IOVCC = 1.65V to 3.3V, VCI = 2.5V to 3.3V, AGND = DGND = 0V, T = 10+/-0.5ns.
2. Does not include signal rising and falling times.

Figure5.1.2 4-SPI Interface Characteristics

6. Reset Timing



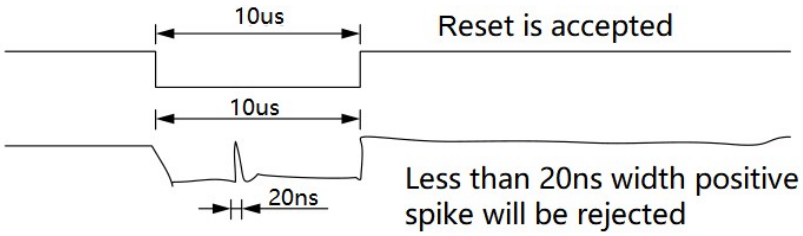
Signal	Symbol	Parameter	Min	Max	Unit	Description
RESX	tRW	Reset pulse duration	10	-	us	-
	tRT	Reset cancel	-	5	ms	Note 1,5
			-	120	ms	Note 1,6,7

Note:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NV memory to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts

3. During the Reset period, the display will be blanked (When Reset starts in the Sleep Out mode, the display will enter the blanking sequence in at least 120 ms. The display remains the blank state in the Sleep In mode.) and then return to the default condition for the Hardware Reset.
4. Spike Rejection can also be applied during a valid reset pulse, as shown below:

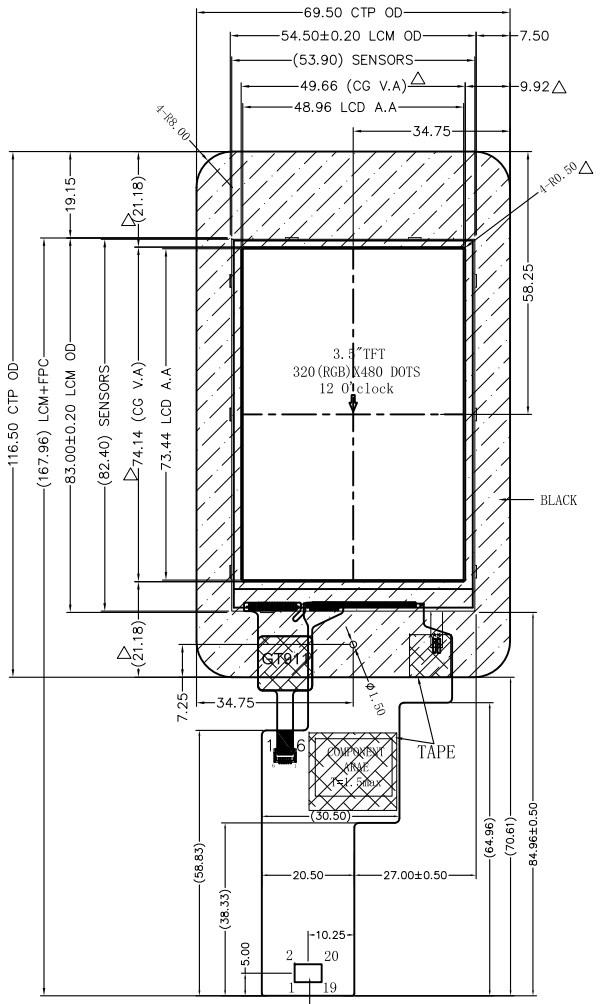


Positive Noise Pulse during Reset Low

5. When Reset is applied during the Sleep In Mode.
6. When Reset is applied during the Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. The Sleep Out command also cannot be sent in 120msec.

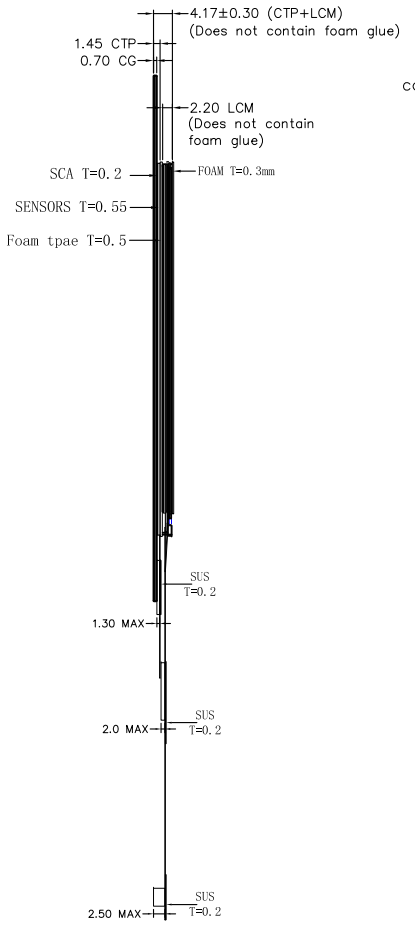
7. Mechanical Drawings

Front View

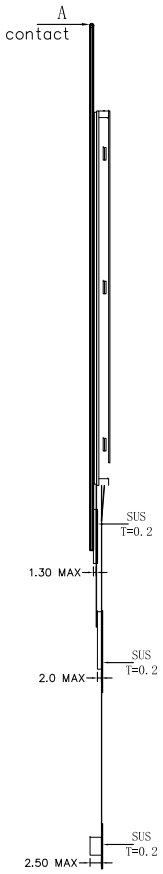


MOLEX SLIMSTACK BOARD TO BOARD RECEPTACLE
505066-2022

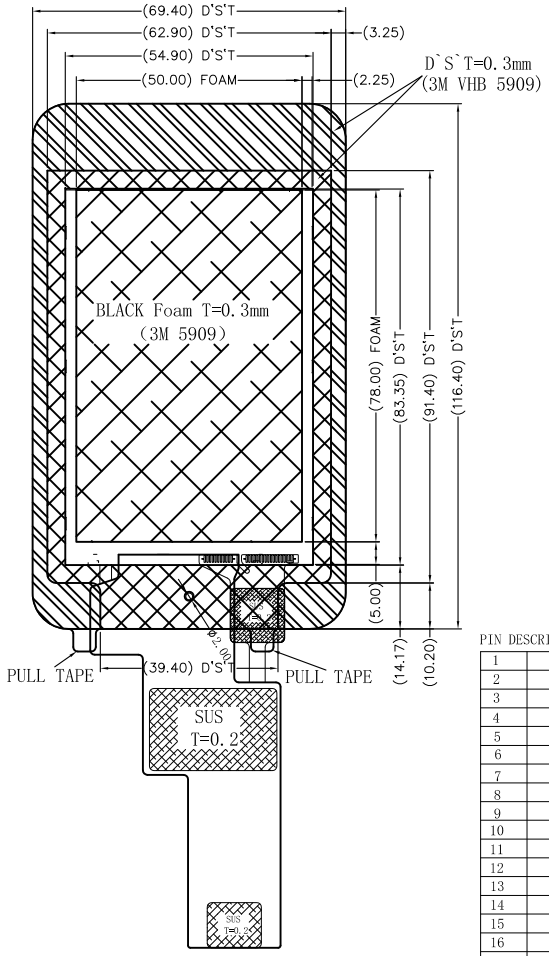
Side View



Side View



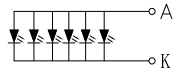
Back View



PIN DESCRIPTION

1	LED A+
2	LEDK 4
3	LEDK 3
4	LEDK 2
5	LEDK 1
6	REST
7	SDO
8	SDI
9	RS
10	SCL
11	CS
12	CTP_INT
13	CTP_SCL
14	CTP_SDA
15	CTP_RST
16	VCI
17	CTP_VDD
18	10VCC
19	GND
20	GND

CTP NOTE:
1. G+G
   LENS GLASS: 0.7mm
   SENSOR GLASS: 0.55mm
   IC: GT911
2. Operation Voltage: 2.8V-3.3 V
3. Transmittance: ≥85%
4. The cover hardness: 6H



IF=120mA, VF=3.0V(TYP)

0.10X45° BEVEL
BOTH SIDES

DETAIL A
SCALE 10:1

Notes:
1. Display : 3.5", TFT
2. Resolution: 320xRGBx480
3. LCD Viewing Direction: 12 O'clock,
4. Drive IC:ILI9488
5. Display Mode: Normally White/Transmissive
6. LCM Bringhtness: 380cd/m²(TYP),
   LCM+TP Brightness:320cd/m²(TYP),uniformity≥80%
7. unmark Tolerance:±0.2
8. OPERATING TEMP: -10° C~ +60° C
9. STORAGE TEMP: -20° C~ +70° C
10. Requirements on Environmental Protection: ROHS

REV	DESCRIPTION	DATE	NAME
3			
2			
1			
0	NEW	2023.09.01	JACK

LEADTEK COMPANY LIMITED

LEADTEK DISPLAY

SCALE:1/1	UNIT:mm	PAGE:1/1	Approve	Check	Drawn
Part No:	LTK035HVCCT34	VER:V0	IAN	JONA	JACK
Customer No:					

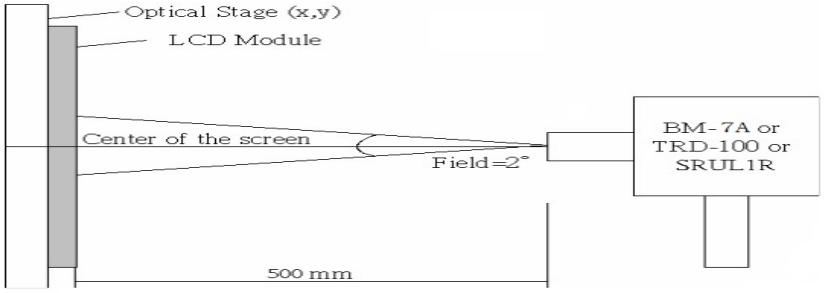
8.INTERFACE SIGNAL

NO	SYMBOL	FUNCTION
1	LEDA	LED Anode
2	LEDK4	LED Cathode
3	LEDK3	LED Cathode
4	LEDK2	LED Cathode
5	LEDK1	LED Cathode
6	RESET	Reset signal(low active)
7	SDO	SDO Serial data output
8	SDI	Serial data input pin in serial bus system interface
9	RS	Data or command select pin
10	SCL	Servers as SCL (Serial Clock)
11	CS	Chip select
12	TP_INT	INT pin for CTP
13	TP_SCL	SCL pin for CTP
14	TP_SDA	SDA pin for CTP
15	TP_RES	Reset pin for CTP
16	VCI_3.3V	Power Supply for analog, VCC=2.8V~3.6V.
17	TP_VCC	Power supply for CTP.
18	IOVCC_1.8V	Power Supply for I/O system. IOVCC=1.65V~3.3V
19	GND	Power Ground
20	GND	Power Ground

9.ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Specification			Unit	Remark
			Min.	Typ.	Max.		
Response time (By Quick)	Tr+Tf	θ= 0°	-	20	40	ms	Note 5
Contrast ratio	CR	θ= 0°	-	500	-		Note 2,6
Viewing angle	Top	CR≧10	-	60	-	deg.	Note 2,6,7
	Bottom	CR≧10	-	60	-		
	Left	CR≧10	-	70	-		
	Right	CR≧10	-	70	-		
Color chromaticity (CF only with ITO, light source is C light, CIE 1931)	Wx	θ= 0°	0.292	0.307	0.322		Note 3
	Wy		0.312	0.327	0.342		
	Rx		0.609	0.624	0.639		
	Ry		0.316	0.331	0.346		
	Gx		0.281	0.296	0.311		
	Gy		0.562	0.577	0.592		
	Bx		0.128	0.143	0.158		
	By		0.094	0.109	0.124		
NTSC			57%	60%	-		Note 3
Cross talk	Ct		-	-	2%		Note 9
Transmittance	Trans		-	5.50%	-		Note 4

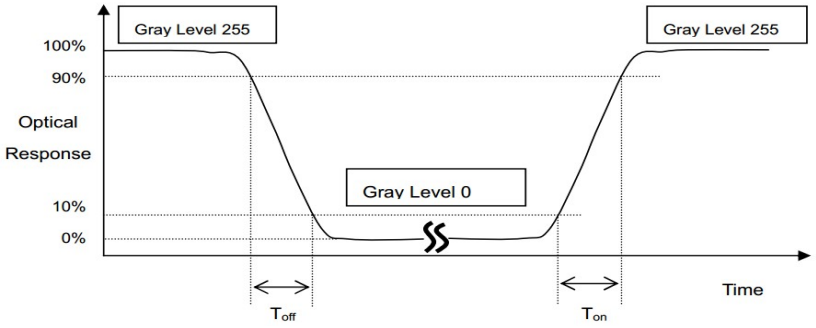
Note 1.Ambient condition：25℃±2℃，60±10%RH，under 10 Lunx in the darkroom。
Note 2.Measure device：BM-7A (TOPCON)，viewing cone=2°
Lv = Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)



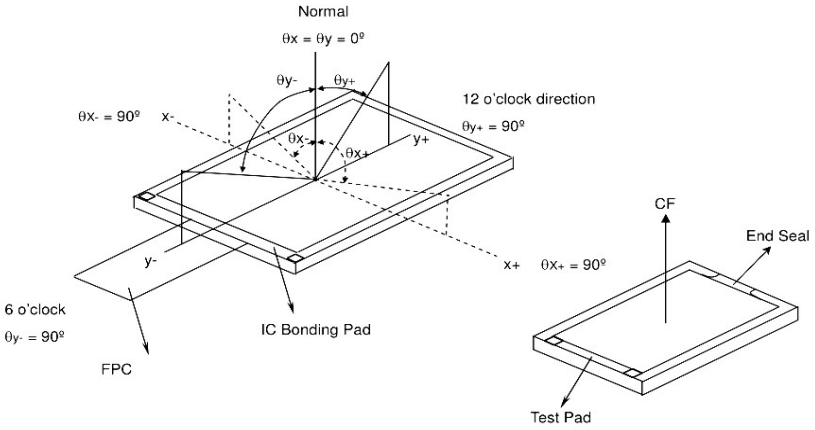
Note 3. Definition of Contrast Ratio：

$$CR = \frac{\text{Average Surface Luminance with all black pixels (P1,P2,P3,P4,P5)}}{\text{Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)}}$$

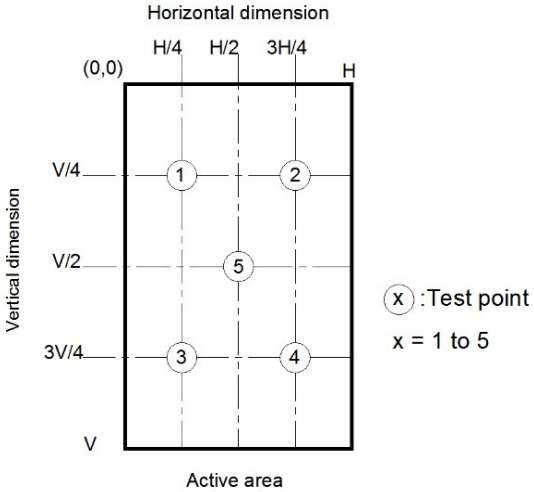
Note 4. Definition of Response Time (Ton, Toff), The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 5. Definition of view angle(θ，ψ)：



Note 6. The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room. Measouring method for Contrast ratio, surface luminance, Luminance uniformity,CIE(x,y) chromaticity.



Light spot size ⌀=7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-7

10.RELIABILITY TEST

Reliability test conditions (Polarizer characteristics null)

No.	Items	Condition	Inspection after test
1	High Temperature Storage	T = 70℃±2℃ for 48 hr	Inspection after 4 hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD 2.Sealleak; 3.Non-display; 4.missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value.
2	Low Temperature Storage	T = -20℃±2℃ for 48 hr	
3	High Temperature Operating	T = 60℃±2℃ for 48 hr	
4	Low Temperature Operating	T = -10℃±2℃ for 48 hr (But no condensation of dew)	
5	High Temp. and High Humidity Operating	T = 50℃±5℃ /90% for 48 hr (But no condensation dew)	
6	Thermal Shock	-20±2℃~25~70±2℃×10cycles (30min.) (5min.) (30min.)	
7	Dropping test (non-operation)	Drop to the ground from 76cm height, one time, every side of carton. (Packing condition)	
8	Packing Vibration (non-operation)	Frequency：10Hz~55Hz~10Hz Amplitude：1.0mm, X，Y，Z direction for total 3hours (Packing condition)	
9	ESD	Voltage:±6KV R: 330Ω C: 150pF Air discharge, 10time	

Note:

(1)The test samples should be applied to only one test item.

(2)Sample size for each test item is 5~10pcs.

(3)In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
Using ionizer(an antistatic blower) is recommended at working area in order to reduce electro-static voltage.
When removing protection film from LCM panel, peel off the tag slowly (recommended more than one second) while blowing with ionizer toward the peeling face to minimize ESD which may damage electrical circuit.

(4) Please use automatic switch testing mode when test operating mode.

深圳市丽台电子有限公司

Shenzhen Leadtek Electronics Co.,Ltd

Quality inspection standards

品质允收标准

MODEL No. / 产品型号： **Applies 0.95~5.0 TFT-LCD Panel**
UPDATED DATE / 生效日期： **2022-05-20**
VERSION / 版 本： **A0**
Customer Signature/客户签字： _____

RECORD OF REVISION/修订履历：

Version 版本	Revision Record 修订内容	Compile 修订者	Date of Revision 修订日期
V0	首发/Starting	Green	2022.05.20

1.Scope 1适用范围

This document shall be applied to 0.95~5.0 TFT-LCD Panel.

本文件适用于0.95~5.0 TFT-LCD Panel.

2.Inspection and Environment onditions/检查条件与环境

2. 1 Inspection Conditions 检查条件:

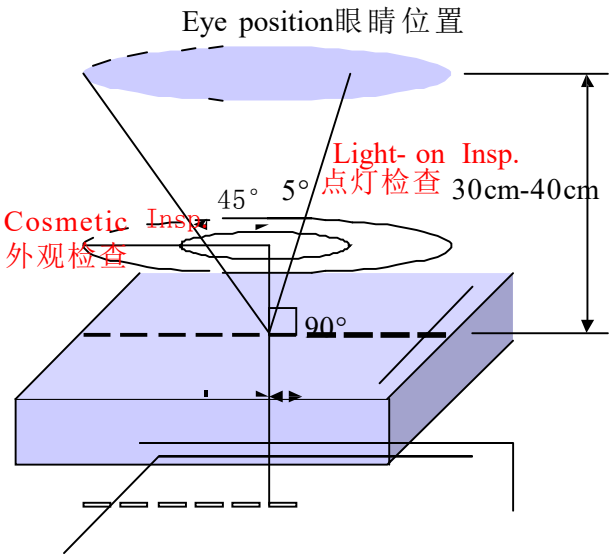
(1) Inspection Distance检测距离: 35 cm±5cm

(2) Each picture /每个画面：2~3 secs/秒，Cosmetic Inspection/外观10~12 secs/秒

(3) View Angle观看角度:

Light-on Inspection Angle点灯检验角度 ： ±45°

Cosmetic Inspection Angle外观检验角度 ： ±45°



(Perpendicular to LCD panel surface垂直于液晶显示表面)

2.2 Environment Conditions环境条件:

Ambient Temperature 温度		25℃±5℃
Ambient Humidity 湿度		55±5%RH
Ambient Illumination 亮度	Cosmetic Inspection 外观检验	800-1000 Lux
	Functional Inspection 点灯检验	200~300Lux

2.3 Sampling Conditions 抽样条件:

(1) Lot Size： Quantity of shipment lot per model/.

批量：单次运送单一机型数量

(2) Sampling Method ：
抽样方法：

Sampling Plan 抽样计划		GB2828/2003
		Normal Inspection, Single Sampling 正常检验、单次抽样
		Geneal II Inspection 普通二级
AQL	Major Defect 主要缺点	0.25
	Minor Defect 次要缺点	0.65

(3) The classification of Major(MA) and Minor(MI) defects is shown as 3. Inspection Criteria.
主缺(MA)及次缺(MI)定义于"3.检查标准"

3.Terms and Definitions/术语和定义

3.1 Classification of defects缺陷的分类:

Major defects: A major defect is a defect that is likely to result in failure, or to reduce materially the usability of the product for its intended purpose.

主要缺陷：会导致产品功能失效或减少产品可用性的缺陷。

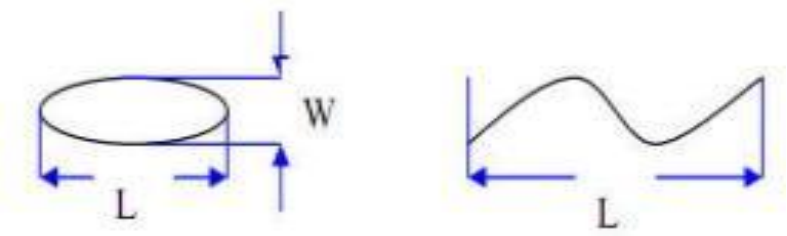
Minor defects: A minor defect either is a defect that is not likely to reduce materially the usability of the product for its intended purpose, or is a departure from an established having little bearing on the effective use or operation of the product.

次要缺陷：不会导致产品功能失效，不会减少产品的有效使用和操作。

3.2 Extraneous substances that can be wiped out ,like Finger point,Particles are not cnsidered as a defect . 可以被擦拭干净的表面物质不视为缺陷 (如手指印，尘粒) 。

3.3 Defects on the Black Matraix(outside ofActive Area) are not considered as a defect . BM 区域 (AA 区以外) 的缺陷不视为缺陷。

3.4 Size of circular defect,is defined by diameter"D" 。The defect average diameter D=1/2(W+L) 圆形缺陷的大小是由直径 D 定义的。缺陷的平均直径 D=1/2(W+L)



3.5 When defect size L≥2W,the defect count as liner type defect.Size of linear defect is defined by length(L) and the maximum width(W).

当缺陷尺寸 L≥2W 时，被视为线状缺陷。线状缺陷是由长度 (L) 和最大宽度 (W) 定义的。 3.6 3.6 Mura criteria :judged by ND filter6%,and can't be seen under at ND filter 6% .

3.6 MURA 判断标准：使用 ND6%判定，且透过 ND6% ，遮住不可见.

3.7 Dot defect is defind as the defective area of the dot is larger than 50% of the dot area and is visible through 6% ND filter

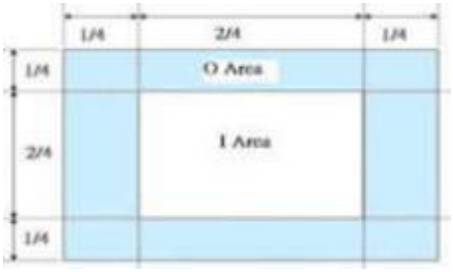
DOT 定义为点缺陷面积大于 50% DOT 面积, 且透过 ND6%遮住是可见的.

The drawing of 1/2 area sub-pixel definition: The 1/2 area sub-pixel can be defined as below one or more of specific shapes

1/2 面积的子像素定义绘图：1 / 2 面积的子像素可以定义为如下一个或多个特定形状图：

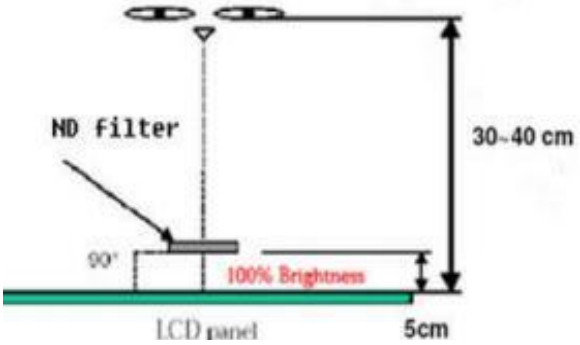
3.8 A dot defect that is smaller than the defined dot defect will be treated as small bright dot. 细碎亮点：小于"DOT 定义"的点缺陷视为细碎亮点。

I 区与 O 区比例：1：2：1



3.9 Inspection method of ND Filter - holding ND filter in front of the panel around 5cm and examine the panel from 35±5 cm in the front view for 2~3 second.

ND 卡的检查方法：在面板上方大约 5CM 处握住 ND 卡，眼睛距离面板 30-40CM ，通过 2~3 秒观察。



4. Inspection Criteria 检验标准

4.1 Appearance Inspection specification 外观检查规格:

Judge area 区域	Judge item 项目	Specification inspection 检查规格	Judge criterion	
			Major	Minor
Silicone 硅胶	Silicone spread 硅胶涂布	The height can't over C/F , color filter , or gomu 高度不能过超 C/F		MI
	Silicone residue 硅胶残余	Can't cover polarizer, FPC …etc. 不能覆盖 POL, FPC 等		MI
LCD 玻璃	Wire(on Array) 线路	No damage 不能损伤	MA	
	Edge 边缘	No extended crack 不可有延伸性裂纹	MA	
PCBA Connector FPC/ FFC	Appearance 外观	Scratch or damage result in copper expose is not allowed 划伤或损伤不允许导致出现露铜		MI
	Component 零件	No damage 不能损伤	MA	
	Connection status 连接状况	Need correct connection 需要正确连接	MA	
	Broken 破裂	Not allowable 不允许	MA	
	Folding sign 对位记号折叠	Not allowable 不允许	MA	
POL 偏光片	Scract on the polarizer 偏光片划伤	1.W≤0.05mm; L≤5mm, Ignore (忽略)		MI
		2.0.05mm<W≦0.10mm ; L≤5mm ; N≤3 ; DS≥10mm		
		3.0. 10mm＜W; 5mm＜ L , Not allowable不允许		

Judge area 区域	Judge item 项目	Specification inspection 检查规格	Judge criterion	
			Major	Minor
POL 偏光片	Dent on the polarizer 偏光片凹痕	1.D＜0. 15mm，Ignore（忽略）		MI
		2.0.15mm＜D≤0.30mm；N≤3；DS≥10mm		
		3.0.30mm＜D，Not allowable不允许		
	POL Linear bubble 线状气泡	1.W≤0.05mm；L≤5mm，Ignore（忽略）		MI
		2.0.05mm<W≦0.10mm ; L≤5mm ; N≤3 ; DS≥10mm		
		3.0. 10mm＜W；5mm＜ L，Not allowable不允许		
	POL dot bubble 点状气泡	1.D＜0. 15mm，Ignore（忽略）		MI
		2.0.15mm＜D≤0.30mm；N≤3；DS≥10mm		
		3.0.30mm＜D，Not allowable不允许		
	POL edge bubble 片边缘气泡	1. The display area is 1/2BM outside，Not allowable 显示区往外 1/2BM 区域内，不允许 2. The display area is outside the outer 1/2BM area，Not allowable 显示区往外1/2BM区域以外，不管控		MI

Judge area 区域	Judge item 项目	Specification inspection 检查规格	Judge criterion	
			Major	Minor
TP&CG	Foreign Material in spot shape 点状异物	1.D≤0.15mm；Ignored（忽略） 2.0.15mm＜D≤0.30mm；N≤3；DS≥10mm 3.D＞0.30mm；Not allowable不允许		MI
	Fisheye/bubbles 鱼眼/气泡	1.D≤0.15mm；Ignored（忽略） 2.0.15mm＜D≤0.30mm；N≤3；DS≥10mm 3.D＞0.30mm；Not allowable不允许		MI
	Scratches on the surface 表面划伤	1.W≤0.05mm；Ignored（忽略） 2.0.05mm<W≤0.10mm，L≤5mm；N≤3；DS≥10mm 3.W＞0.10mm，L＞5mm；Not allowable不允许		MI
	Collapse corner、Crash edge 崩角、崩边	Product front:/产品正面： collapse corners, collapsed edges are not allowed 崩角、崩边不允许； Product back/产品背面： X≤ 0.5 , Y≤0.5，Z≤1/2T；N≤2；DS≥10mm	MA	
	Printed fonts/LOGO 丝印/LOGO	Printed fonts/LOGO clarity、complete、content right 字体/LOGO丝印清晰、完整、内容正确		MI
	Broken 破损	Not allowable不允许	MA	
	Dirty surfaces 表面脏污	Dirt cannot be wiped， Not allowable 不可擦拭的脏污，不允许		MI
	IR hole IR孔	Black spots/黑点： W ≤0.15mm，N≤2，Not visible against a black background/黑色背景下不可见		MI
		IR hole Scratches： 1.W＜0.03mm，Ignored（忽略） （Dense points Not allowable 不允许密集）； 2.0.03mm＜W≤0.08mm；L≤2mm；N≤2； 3.W＞0.08mm，L＞2mm，Not allowable 不允许		MI

4.2 Electrical Inspection specification电性检查规格:

Item 项目	Judgment Criteria 判定标准	Judge criterion	
		Major	Minor
LCD Bright /Dark dot 玻璃亮点/暗点	1.D≦0. 15mm， Ignored（忽略），Not dense（不可密集） 2.0.15mm<D≦0.30mm ； N≦3 ； DS≥10mm 3.D＞0.30mm ，Not allowed/不允许		MI
Mura	Invisible through 6% ND filter，200~300Lux 透过ND6% 遮住 ， 目测不可见即为OK， 200~300Lux		MI
Small bright dot 细碎亮点	Not allowed if it can be observed through ND Filter6% 透过ND6%目测看得见，不允许		MI
ZBD Rate 玻璃亮点比率	90:10		MI
Light Leakage 漏光	Invisible through 6% ND filter，OK 透过ND6%遮住目测不可见即为OK If necessary,set up set up Limit Sample. 如果有必要，可制订限度样品		MI
Bubble in Cell (LC Bubble/Actice Area) CELL气泡 (AA区LCD气泡)	Eyes should not find it . 目视观察不可见，视为 OK	MA	

Item 项目	Judgment Criteria 判定标准	Judge criterion	
		Major	Minor
Foreign Material in spot shape 点状异物	1.D≦0.15mm， Ignored（忽略） 2.0.15mm<D≦0.30mm ； N≦3； DS≥10mm 3.D＞0.30mm ， Not allowable/不允许		MI
Foreign Material in line or spiral shape 线状异物	1.W≦0.05mm ， Ignored（忽略） 2. L≤5mm ； 0.05mm<W≦0.10mm ； N≦3 3.W＞0.10mm ； L＞5mm ， Not allowable/不允许		MI
White dot in back-light 白点	1.D≦0. 15mm，Ignored（忽略） 2.0.15mm<D≦0.30mm ； N≦3 ； DS≥10mm 3.D＞0.30mm ， Not allowed/不允许		MI
TP no touch 无触摸	Not allowable 不允许	MA	
Abnormal Disply 显示异常	Not Allowed 不允许	MA	
NO display 无显示	Not Allowed 不允许	MA	
Line Defect 缺线	Not Allowed 不允许	MA	
Angle of view error 视角错误	Not Allowed 不允许	MA	
Tect crostalk 不消失的残影	Not Allowed 不允许	MA	