Shenzhen Leadtek Electronics Co.,Ltd

PRODUCT SPECIFICATION

TFT-LCD-TP MODULE

Module No: LTK050FWHCT10-V0

- ☑ Preliminary Specification
- ☐ Approval Specification

Designed by	Checked by	Approved by
lan	Ridi	Steven

Final Approval by Customer

Approved by	Comment

**The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

1.Document Revision History

Version	Contents	Date	Note
V0	First Issued	2022.10.28	

2.GENERAL INFORMATION

Item	Specification	Unit
LCD size	5.0"TFT	inch
Display Mode	Normally Black	
Resolution	480(RGB)x854	Pixel
Pixel pitch	0.039(H) x 0.117(V)	mm
Pixel Arrangement	RGB Vertical Stripe	-
Viewing direction	ALL	-
CTP+LCM Module outline dimension	80.00(H)*128.80(V)*3.96(T)	mm
Active area	56.16 (H)* 93.60 (V)	mm
Colors	16.7M	mm
Driver IC	ST7701S	-
Interface	2 lines MIPI	-
Backlight	White LED	
Touch IC	GT1151QM	
Surface hardness	6H	
Touch structure	G+G	
Operating Temperature	-20℃~ +70℃	
Storage Temperature	-30℃~ +80℃	
Weight	TBD	

3.ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit	Note
Power Supply voltage 1	VCI~GND	-0.3	+4.6	V	
Power Supply voltage 2	IOVCC~GND	-0.3	+4.6	V	
Logic Input Voltage Range	V _{IN}	-0.3	IOVCC+0.3	V	
Logic Output Voltage Range	Vo	-0.3	IOVCC+0.3	V	
Operating temperature	Topr	-20	+70	$^{\circ}$	
Storage temperature	Tstg	-30	+80	$^{\circ}$	

^{*} The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

4.DC ELECTRICAL CHARACTERISTICS

4.1 Driving TFT LCD Panel

AGND = GND = 0V, Ta = 25° C

Parameter	Symbol	Min	Тур	Max	Unit
Supply voltage for analog circuit	VCI	2.5	2.8	3.3	V
Supply voltage for logic circuit	IOVCC	1.65	1.8	3.3	V
Input voltage 'H'level	V _{IH}	0.7*IOVCC	_	IOVCC	V
Input voltage 'L'level	V _{IL}	-0.3	_	0.3*IOVCC	V
Output voltage 'H'level	V _{OH}	0.8*IOVCCI	_	IOVCC	V
Output voltage 'L'level	V _{OL}	GND		0.2*IOVCC	٧

4.2 Back-light Unit:

Ta = 25℃

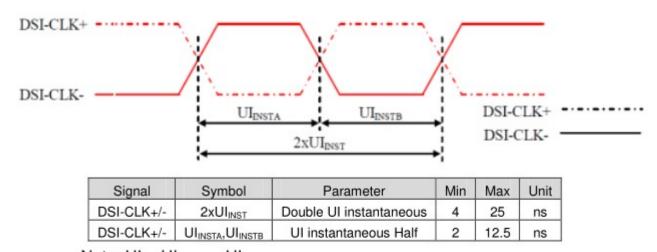
PARAMETER	Sym.	Min.	Тур.	Max.	Unit	Test Condition	Note
LED Current	IF	_	40	_	mA	_	_
Luminance		_	500	_	cd/m ²	I=40mA	
LED Voltage	VF	_	21	23.1	V	I=40mA	_
Life Time		_	30000	_	Hr.	I=40mA	
Color	White						

Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and IF =40mA. The LED lifetime could be decreased if operating IF is larger than=40mA.

5.IMING CHARACTERISTICS

5.1 High Speed Mode-Clock Channel Timing



Note: UI = UI_{INSTA} = UI_{INSTB}

Figure 5.1.1 DSI Clock Channel Timing

5.2 High Speed Mode-Data Clock Channel Timing

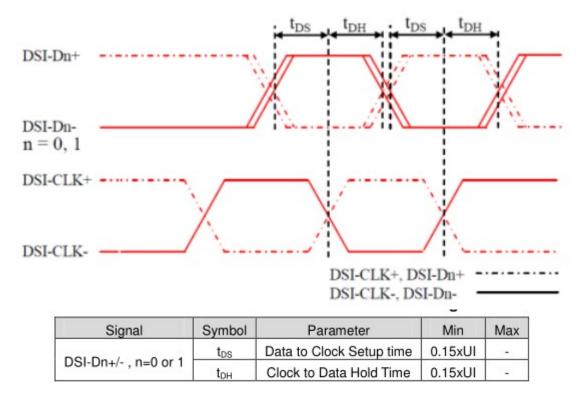
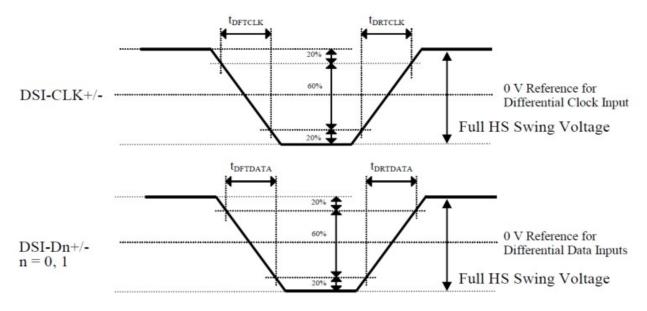


Figure 5.2.1 Data to Clock Channel Timing

5.3 High Speed Mode-Rising and Falling Timings



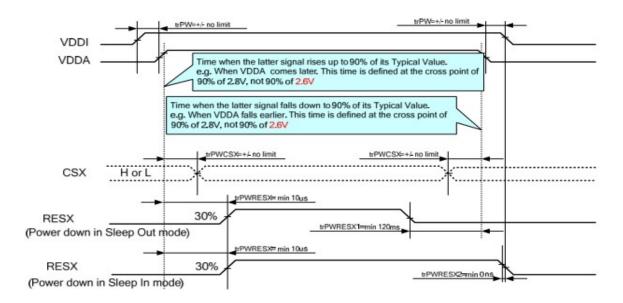
Decemeter	Combal	Condition	Specification			
Parameter	Symbol		Min	Тур	Max	Unit
Differential Rising Time for Clock	torrcuk	DSI-CLK+/-	-	-	150 (Note 1)	ps
Differential Rising Time for Data	t _{DRTDATA}	DSI-Dn+/- n=0 and 1	-	-	150 (Note 1)	ps
Differential Falling Time for Clock	t _{DFTCLK}	DSI-CLK+/-	•	-	150 (Note 1)	ps
Differential Falling Time for Data	t _{DFTDATA}	DSI-Dn+/- n=0 and 1	•	-	150 (Note 1)	ps

Note: The display module has to meet timing requirements, what are defined for the transmitter (MPU) on MIPI D-Phy standard.

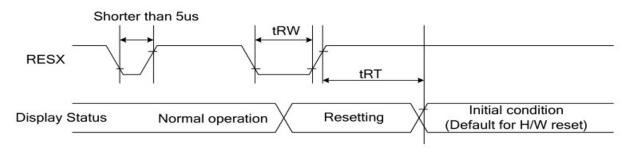
Figure 5.3.1 Rising and Falling Timings on Clock and Data channels

5.4 VCI VDDI(IOVCC) Input power sequence





6. Reset Timing



Signal	Symbol	Parameter	Min	Max	Unit
	tRW	Reset pulse duration	10		uS
RESX	·DT			5 (note 1,5)	mS
	tRT	Reset cancel		120 (note 1,6,7)	mS

Notes:

1. The reset cancel also includes required time for loading ID bytes, VCOM setting and other settings from EEPROM (or similar device) to registers.

This loading is done every time when there is HW reset cancel time (tRT) within 10 ms after a rising edge of RESX.

2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

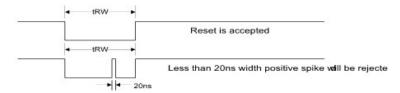
RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked(The display is entering blanking sequence,

which maximum time is 120 ms, when Reset

Starts at Sleep-Out status. The display remains the blank state in Sleep-In mode). Then return to Default condition for Hardware Reset

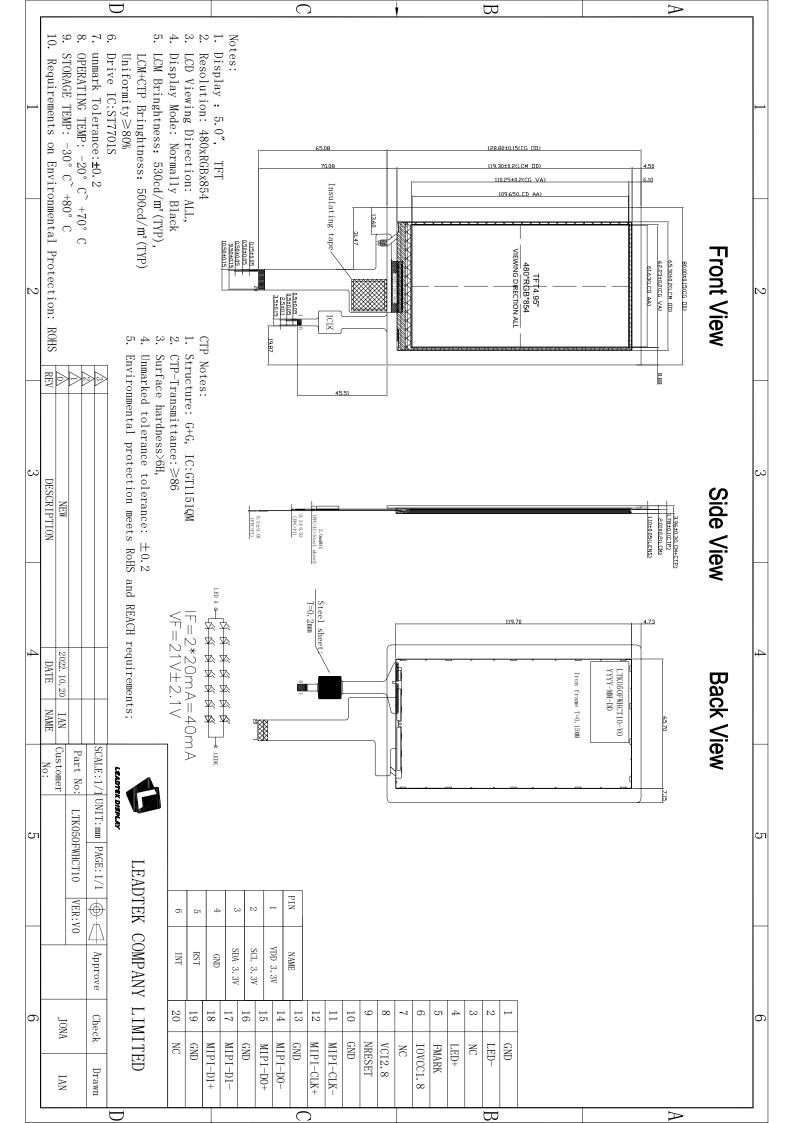
4. Spike Rejection also applies during a valid reset pulse as shown below:



- 5. When Reset applied during Sleep-In Mode.
- 6. When Reset applied during Sleep-Out Mode
- 7. It is necessary to wait 10ms after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120 ms.

7. Mechanical Drawing





8.INTERFACE SIGNAL

Pin No.	Symbol	Description
1	GND	Power ground
2	LEDK	LED backlight cathode
3	NC	NC
4	LEDA	LED backlight anode.
5	FMARK	Used to synchronize with the master
6	IOVCC	Power supply for the logic power and I/O circuit
7	NC	NC
8	VCI	Power supply for the analog power
9	RESET	Reset signal(low active)
10	GND	Power ground
11	MIPI_CLKN	MIPI-DSI data lane Nosivtve input pin
12	MIPI_CLKP	MIPI-DSI data lane Pegtive input pin
13	GND	Power ground
14	MIPI_D0N	MIPI-DSI data lane 0 Nosivtve input pin
15	MIPI_D0P	MIPI-DSI data lane 0 Pegtive input pin
16	GND	Power ground
17	MIPI_D1N	MIPI-DSI data lane 1 Nosivtve input pin
18	MIPI_D1P	MIPI-DSI data lane 1 Posivtve input pin
19	GND	Power ground
20	NC	NC

8.1 TP INTERFACE SIGNAL

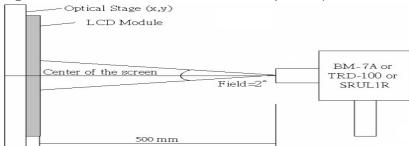
1	CTP-VDD 3.3V	Power Supply For CTP.
2	CTP-SCL	I2C clock input.
3	CTP-SDA	I2C data input and output.
4	CTP-GND	Power ground
5	CTP-RES	External Reset, Low is active.
6	CTP-INT	Interrupt request to the host, or Wakeup request from the host.

9.ELECTRO-OPTICAL CHARACTERISTICS

Item		S	ymbol	Condition	Min	Тур	Max	Unit	Note	
	Transmittance (w/o polarizer)		Т%		3.7	4.3	-	%	1	
Contrast	Contrast ratio		Cr	θ=0°	1000	1500	-	_	3	
Response	Response time		n+Toff	+Toff Ta=25℃		20	25	ms	4	
Surface Lum	ninance		LV			500	-	cd/m²	2	
		Н	Θ _{x+}		-	80	-	deg		
Viouing and	Viewing angle range		Θ _{x-}	Center	-	80	-	deg	5	
viewing angi			Θ _{Y+}	CR>10	-	80	-	deg		
		er	Θ _Y -		-	80	-	deg		
	Dad		Х		0.625	0.645	0.665	-		
	Red	у		0.313	0.333	0.353	-			
			Х	Viewing	0.304	0.324	0.344	-		
CIE(x,y)	Green		у	normal	0.550	0.570	0.590	-	6	
chromaticity	Dlug		Х	angle Θx=θy=0°	0.114	0.134	0.154	-	0	
	Blue		у	Ta=25℃	0.104 0.124 0.124 0.124 0.124	0.124	0.144	_		
	White	х			0.27	0.30	0.33	-		
	VVIIILE		у		0.29	0.32	0.35	-		

Note 1.Ambient condition: 25°C±2°C, 60±10%RH, under 10 Lunx in the darkroom. Note 2.Measure device: BM-7A (TOPCON), viewing cone=2°

Lv = Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)

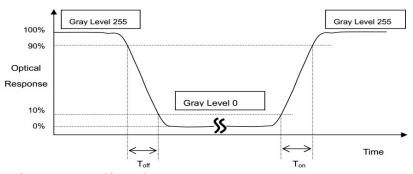


Note 3. Definition of Contrast Ratio:

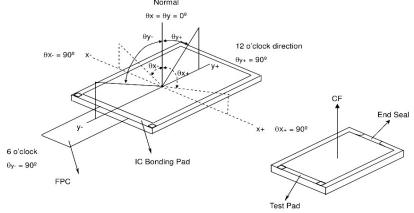
CR = Average Surface Luminance with all black pixels (P1,P2,P3,P4,P5)

Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5)

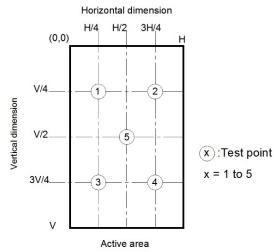
Note 4. Definition of Response Time (Ton, Toff), The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 5. Definition of view angle(θ , ψ):



Note 6. The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room. Measouring method for Contrast ratio, surface luminance, Luminance uniformity,CIE(x,y) chromaticity.



Light spot size ∅=7mm, 500mm distance from the LCD surface to detector lens measurement instrument is TOPCON's luminance meter BM-7

10.**RELIABILITY TEST**

Reliability test conditions (Polarizer characteristics null)

No.	Items	Condition	Inspection after test
1	High Temperature Storage	$T = 80^{\circ}C \pm 2^{\circ}C$ for 48 hr	
2	Low Temperature Storage	T = -30°C ±2°C for 48 hr	
3	High Temperature Operating	T = 70° C $\pm 2^{\circ}$ C for 48 hr	Inspection after 4 hours
4	Low Temperature Operating	T = -20° C $\pm 2^{\circ}$ C for 48 hr (But no condensation of dew)	storage at room temperature, the
5	High Temp. and High Humidity Operating	T = 60° ± 5° /90% for 48 hr (But no condensation dew)	sample shall be free from defects: 1.Air bubble in the LCD
6	Thermal Shock	-20±2°C~25~70±2°C×10cycles (30min.) (5min.) (30min.)	2.Sealleak; 3.Non-display;
7	Dropping test (non-operation)	Drop to the ground from 76cm height, one time, every side of carton. (Packing condition)	4.missing segments; 5.Glass crack; 6.Current ldd is twice higher than initial
8	Packing Vibration (non-operation)	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.0mm, X,Y,Z direction for total 3hours (Packing condition)	value.
9	ESD	Voltage:±6KV R: 330Ω C: 150pF Air discharge, 10time	

Note:

- (1) The test samples should be applied to only one test item.
- (2)Sample size for each test item is 5~10pcs.
- (3)In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.

Using ionizer(an antistatic blower) is recommended at working area in order to reduce electro-static voltage.

When removing protection film from LCM panel, peel off the tag slowly (recommended more than one second) while blowing with ionizer toward the peeling face to minimize ESD which may damage electrical circuit.

(4) Please use automatic switch testing mode when test operating mode.

11.INSPECTION CRITERION

This specification is made to be used as the standard acceptance/rejection criteria for Normal LCM Product.

1 Sample plan

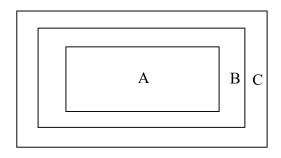
Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65 Minor defect: AQL 1.0

2. Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity 60±15%RH).

3. Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

4. Standards of inspection items

4.1 Major Defect

Item No	Items to be inspected	Inspection Standard	Classification of defects
4.1.1	All	1.No display	Major
	functional defects	2.Display abnormally	
		3.Missing vertical, horizontal segment	

		4.Short circuit
		5. Back-light no lighting, flickering and abnormal lighting.
4.1.2	Missing	Missing component
4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.
4.1.4	linearity	No more than 1.5%

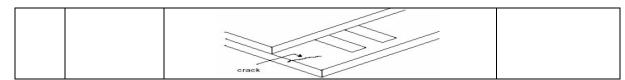
4.2 Cosmetic Defect

Item No	Items to be inspected	Ins	Classification of defects			
4.2.1	Clear		For dark/white spot, size Φ is defined as $\Phi = (x + y)/2$			
	Spots	1				
	Black and white Spot	Zone	Ad	cceptable	Qty	
	defect Pinhole,	Size(mm)	Α	В	С	Minor
	Foreign Particle,	Ф≤0.15	lgn	ore		
	polarizer	0.15<Φ≤0.20	2	2	lanere	
	Dirt	0.20<Φ≤0.30	,	1 Ignore		
		Ф>0.30	0			
		2				
	Clear	Zone	Acceptable Qty			
		Size(mm)	Α	В	С	
	Spots TP Dirt	Ф≤0.15	Ignore		l V	Minor
	II Diit	0.15<Φ≤0.20	,	2	Ignore	
		0.20<Φ≤0.30	,	1	griore	
		Ф>0.30	(0		
	Dim Spots Circle	3				Minor
	shaped and dim	Zone	Acceptable Qty		Qty	
	edged	Size(mm)	А	В	С	
	defects	Ф≤0.2	lgn	ore	Ignore	
		0.20<Φ≤0.40	4	2		

	0.40<Φ≤0.60	1	
	Ф>0.60	0	

		Ф>0.60	0>0.60					
Item No	Items to be inspected		Ins	spection Stan	dard			Classification of defects
	Line	Size(mm)			Acc	Acceptable Qty		
	defect	I (I on oth)	I (I operto) \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		Zor		ne	
	Black line, White line,			W(Width)	Α	В	С	
	Foreign	Ignore		W≤0.05	Igno	ore		Minor
	material	L ≤5.0	0.0	05 <w≤0.08< td=""><td>2</td><td></td><td>Ignore</td><td></td></w≤0.08<>	2		Ignore	
	on polarizer			W>0.08	0		Ignore	
4.2.2		The line ca		e seen after i	 mobil	e ph	one in	
	Foreign	Siz	ze(m	nm)	Acc	epta	ble Qty	
	material	L(Length)	W(Width)			Zo		Minor
	on TP film				Α .	В	С	IVIIIIOI
		Ignore L ≤5.0	0.0	W≤0.05 05 <w≤0.08< td=""><td colspan="2">Ignore 3 Ignore</td><td>lanoro</td><td></td></w≤0.08<>	Ignore 3 Ignore		lanoro	
		L ≥5.0	0.0	W>0.08	0		Ignore	
	Dim line	phone cove condition, ju the scratch non-operati	If the scratch can be seen after mobile phone cover assembling or in the operating condition, judge by the line defect of 4.2.2. If the scratch can be seen only in non-operating condition or some special angle, judge by the following.				erating 4.2.2. If	
	defect	Size(mm)			Acceptable Qty			
4.2.3	Polarizer scratch TP	1 (1		\ \		Zone		Minor
	film	L(Length)	'	W(Width)	Α	В	С	
	scratch	Ignore		W≤0.03	Igno	ore		
		5.0< L≤10.0	0.0	03 <w≤0.05< td=""><td>2</td><td></td><td>Ignore</td><td></td></w≤0.05<>	2		Ignore	
		L≤5.0	0.0	05 <w≤0.08< td=""><td>1</td><td></td><td></td><td></td></w≤0.08<>	1			
	5			W>0.08	0			
4.2.4	Polarize Air bubble	Air bubbles	Air bubbles between glass			arize	er	Minor
		Zone		Acceptable Qty		ble Qty		
		` '	Size(mm)		Α	В	С	
		Ф≤0.20			Igno		Ignore	
		0.20<Φ≤0.3	0.20<Φ≤0.3					

Item No	Items to be inspected		Classification of defects		
	Glass	(i) Chips on A:LCD Glass X(mm) ≤3.0 Notes: S=c Chips on the allowed to be perimeter separately B:TP Glass			
4.35		X(mm) ≤3.0 (ii)Usual su A:LCD Glas	Minor		
		X(mm) ≤3.0 B:TP Glass X(mm) ≤6.0 (iii) Crack Cracks tend	Y(mm) <inner <2.0="" are="" border="" break="" d="" defect="" line="" not<="" of="" seal="" td="" the="" to="" y(mm)=""><td>Z(mm) Disregard Z(mm) Disregard</td><td></td></inner>	Z(mm) Disregard Z(mm) Disregard	



12.PRECAUTIONS FOR USE OF LCD MODULES

1. Handling Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.



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- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated
- (13)Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

2. Storage precautions

- 2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature:

0°C ~ 40°C

Relatively humidity: ≤80%

- 2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 2.4 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

13.0 Packing form

TBD

