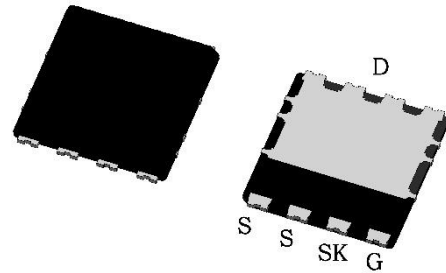


Description

Silicon Carbide (SiC) MOSFET use a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size.

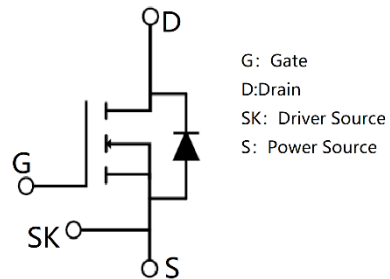
Features

- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low $R_{DS(on)}$
- Low impedance package with driver source pin
- Easy to parallel and simple to drive
- ROHS Compliant, Halogen free



Application

- EV Charging
- High Voltage DC/DC Converters
- Switch Mode Power Supplies
- Power Factor Correction Modules



Ordering Information

Part Number	Marking	Package	Packaging
A3GR160N1200MD88	A3GR160N1200MD88	PDFN8*8	Reel

Absolute Maximum Ratings(Tc=25°C)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage	1200	V
I _D	Drain Current(continuous)at Tc=25°C	20	A
I _D	Drain Current(continuous)at Tc=100°C	12	A
I _{DM}	Drain Current (pulsed)	32	A
V _{GS}	Gate-Source Voltage	-8/+19	V
P _D	Power Dissipation T _C = 25°C	93	W
T _J , Tstg	Junction and Storage Temperature Range	-55 to +175	°C

Electrical Characteristics(T_J = 25°C unless otherwise specified)
Typical Performance-Static

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DS}	Drain-source Breakdown Voltage	I _D =250uA, V _{GS} =0V	1200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =1200V, V _{GS} =0V, T _J =25°C			100	uA
I _{GSS}	Gate-body Leakage Current	V _{DS} =0V; V _{GS} =-8 to 19V			250	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =2mA	1.8	2.5	3.6	V
V _{GS(on)}	Recommended turn-on Voltage	Static		15		V
V _{GS(off)}	Recommended turn-off Voltage			-4		V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} =15V, I _D =10A		160	208	mΩ
		V _{GS} =15V, I _D =10A T _J =175°C		272		mΩ

Typical Performance-Dynamic

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C _{iss}	Input Capacitance	V _{DS} =1000V, f=1MHz, V _{AC} =25mV		715		pF
C _{oss}	Output Capacitance			42		pF
C _{rss}	Reverse Transfer Capacitance			3.2		pF
g _{fs}	Transconductance	V _{DS} =20V, I _D =10A		6		S
E _{OSS}	C _{OSS} Stored Energy	V _{DS} =1000V, f=1MHz		23		μJ
E _{ON}	Turn-On Energy (Body Diode)	V _{DS} =800V, V _{GS} =-4/15V, I _D =10A, L=300μH T _J =175°C		100		μJ
E _{OFF}	Turn-Off Energy (Body Diode)			17		μJ
Q _g	Total Gate Charge	V _{DS} =800V, V _{GS} =-4V/15V, I _D =10A		26		nC
Q _{gs}	Gate-source Charge			12		nC
Q _{gd}	Gate-Drain Charge			6		nC
R _{G(int)}	Internal Gate Resistance	f=1MHz, V _{AC} =25mV		5		Ω
t _{d(on)}	Turn-on Delay Time	V _{DS} =800V, V _{GS} =-4V/15V, I _D =10A, L=300 μH R _{ext} =2.5Ω		11		ns
t _r	Rise Time			8		ns
t _{d(off)}	Turn-off Delay Time			14		ns
t _f	Fall Time			9		ns

Typical Performance-Reverse Diode(T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{FSD}	Forward Voltage	V _{GS} =0V, I _F =5A, T _J =25°C		3.5	6	V
		V _{GS} =0V, I _F =5A, T _J =175°C		3	6	V
I _S	Continuous Diode Forward Current	V _{GS} =0V, T _C =25°C		20		A
t _{rr}	Reverse Recovery Time	V _{GS} =-4V, I _F =10 A, V _R =800 V, di/dt=1900 A/μs, T _J =175°C		7		nS
Q _{rr}	Reverse Recovery Charge			33		nC
I _{rrm}	Peak Reverse Recovery Current			9		A

Thermal Characteristics

Symbol	Parameter	Value.	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	1.6	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	40	°C/W

The values are based on the junction-to case thermal impedance which is measured with the device mounted to a large heat sink assuming maximum junction temperature of T_J(max)=175°C

Electrical Characteristics

Fig1. Output characteristics ($T_J = 25^\circ\text{C}$)

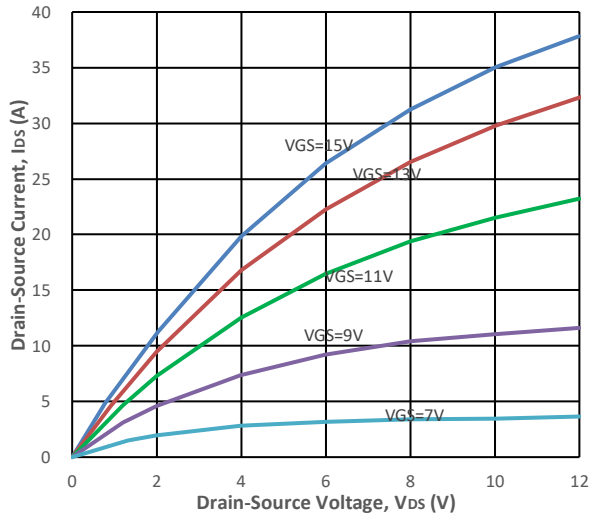


Fig2. Output characteristics ($T_J = 175^\circ\text{C}$)

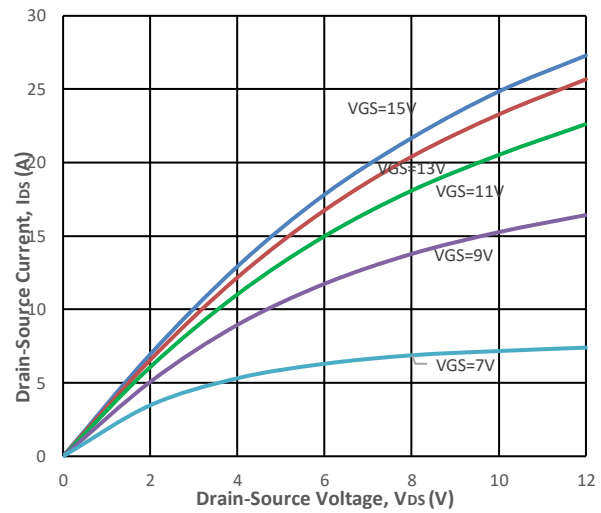


Fig3. Normalized On-Resistance vs. Temperature

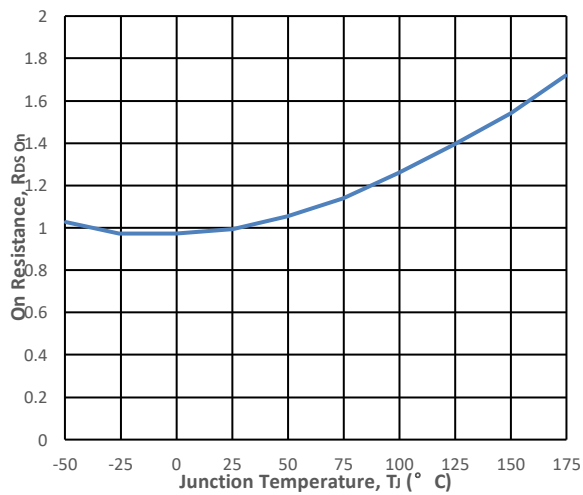


Fig4. On-Resistance vs. Temperature

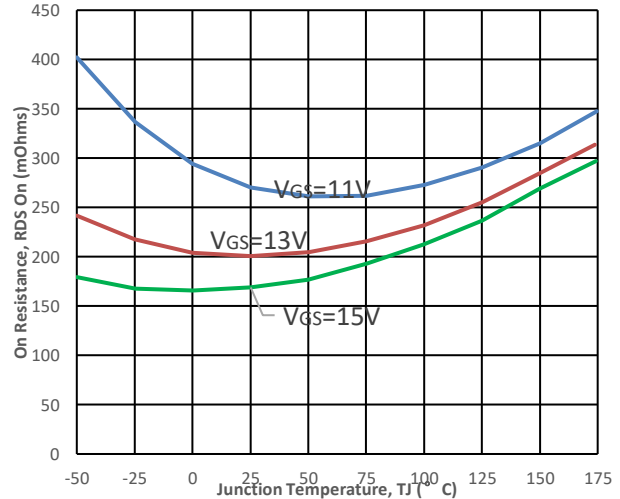


Fig5. Transfer Characteristic

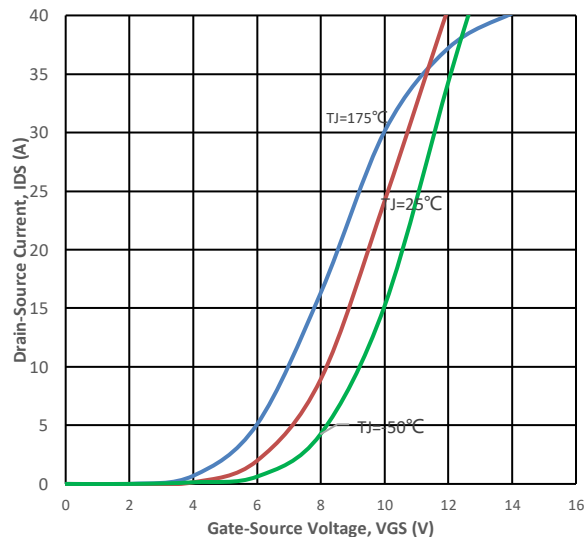


Fig6. Body Diode Characteristic at 25°C

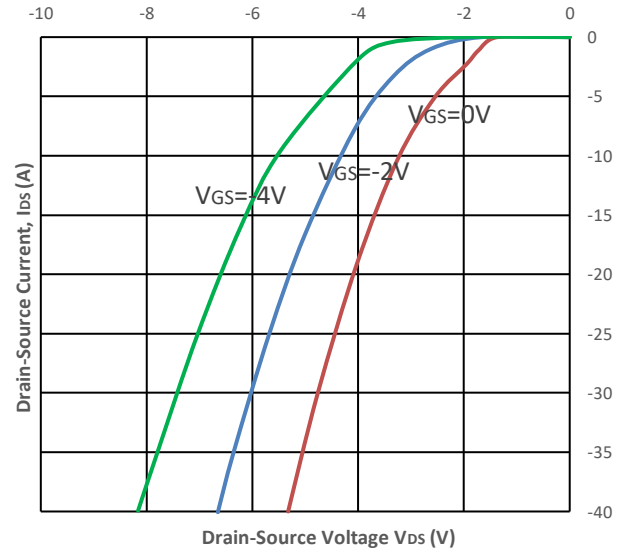


Fig7. Threshold Voltage vs. Temperature

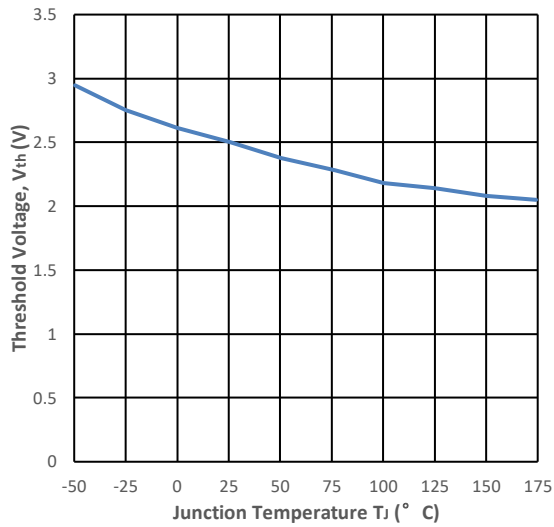


Fig8. Gate Charge Characteristics

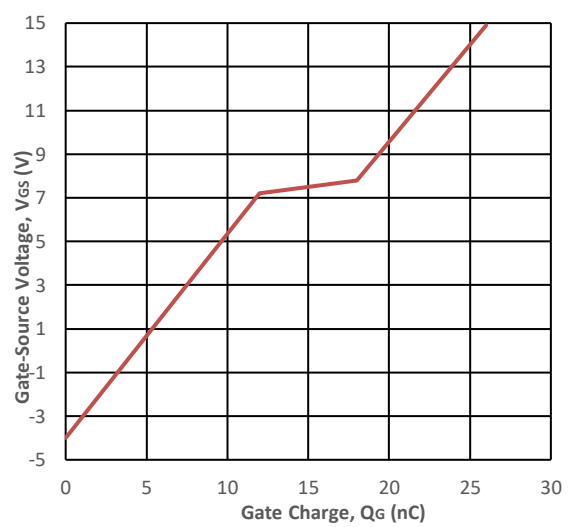


Fig9. 3rd Quadrant Characteristic at 25 °C

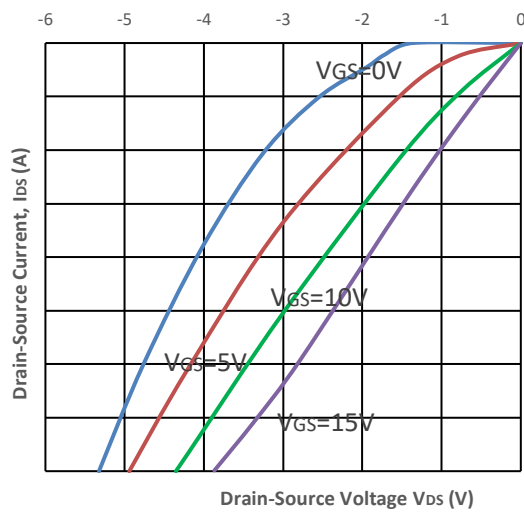


Fig10. Output Capacitor Stored Energy

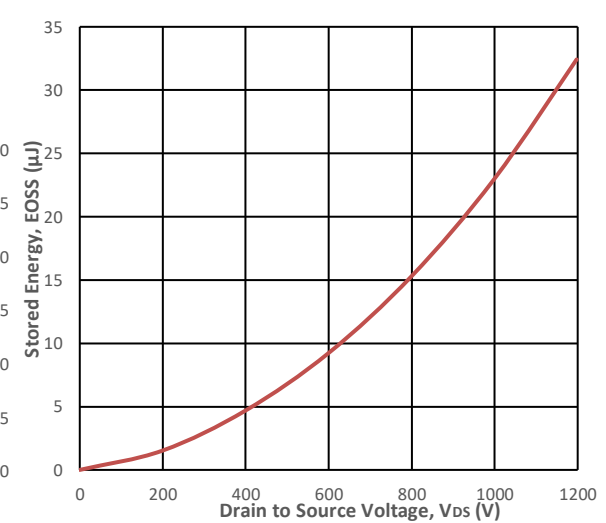


Fig11. Capacitances vs. Drain-Source

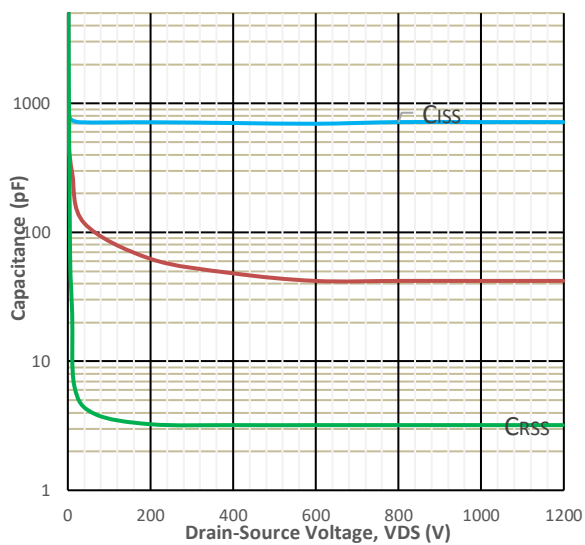


Fig12. Max Power Dissipation Derating Vs Tc

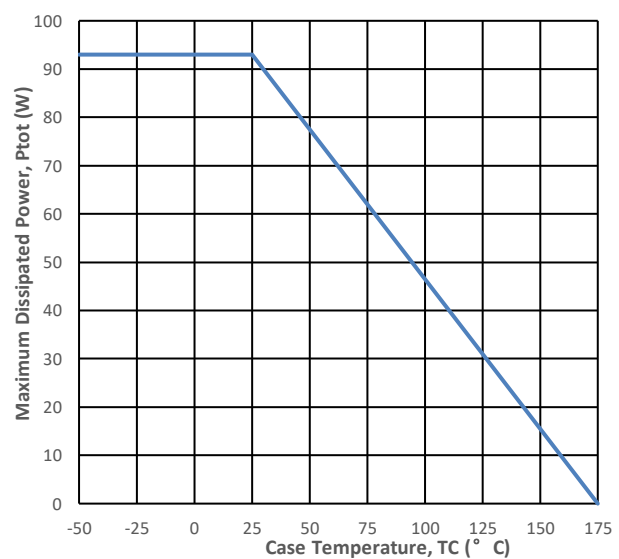


Fig13. Switching Energy vs. Drain Current

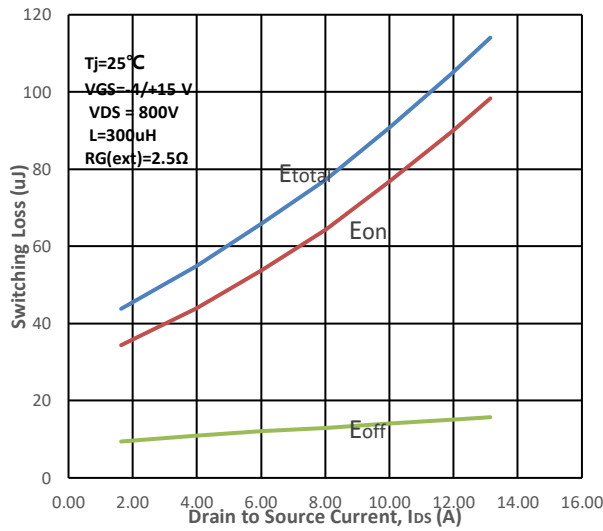


Fig14. Switching Energy vs. $R_{G(ext)}$

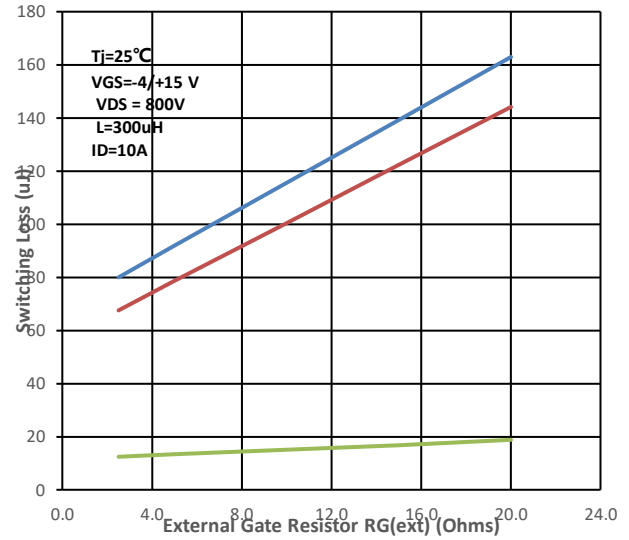


Fig15. Switching Energy vs. Temperature

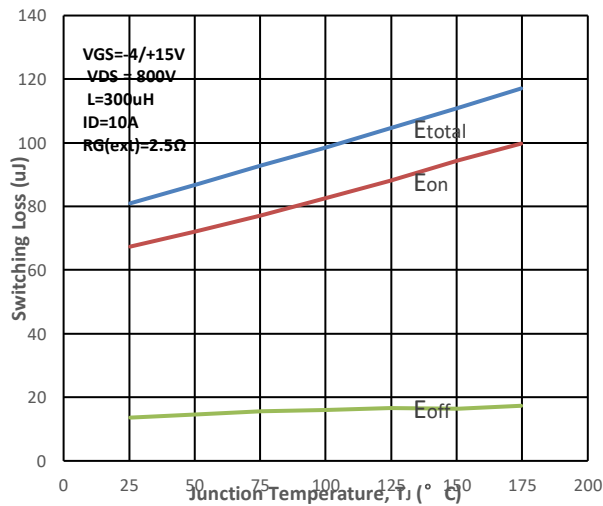


Fig16. Switching Times vs. $R_{G(ext)}$

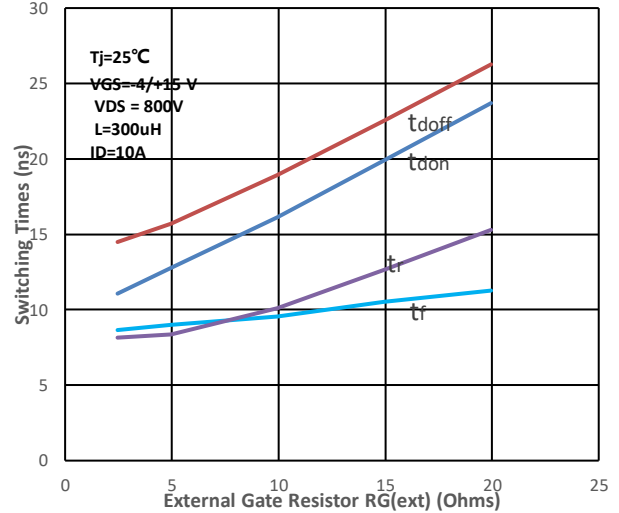


Fig17. Transient Thermal Impedance

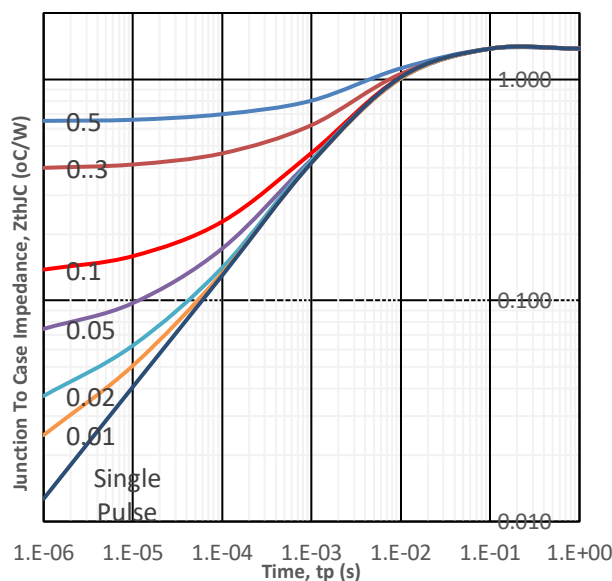
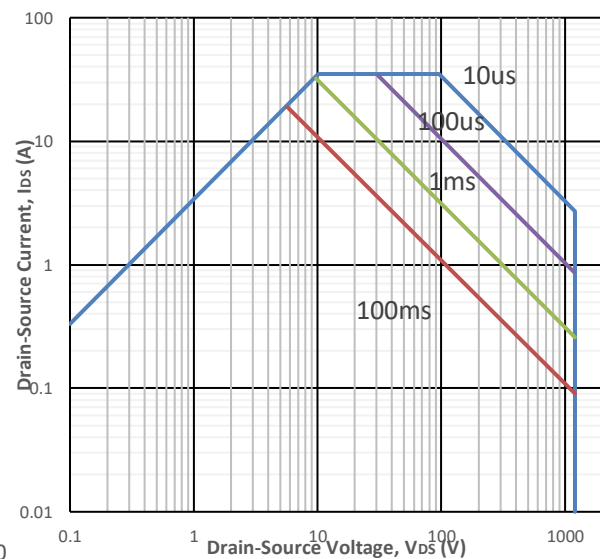
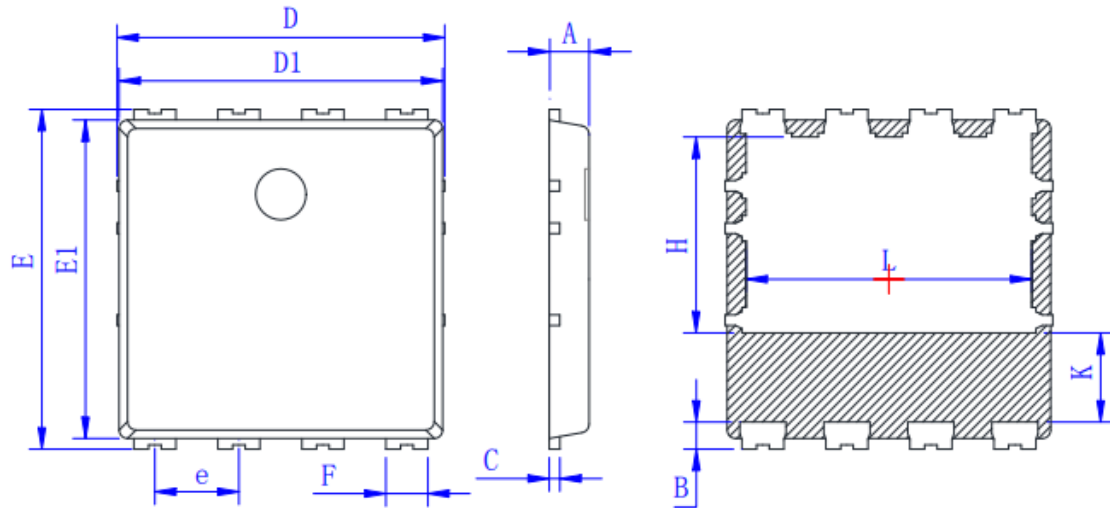


Fig18. Safe Operating Area



Package Drawing:

Dimensions (UNIT: mm)

Symbol	Min	Typ	Max
A	0.90	0.95	1.00
B	0.50	0.60	0.70
C	0.254 TYP		
D	7.70	7.80	7.90
D1	7.60	7.70	7.80
E	7.90	8.00	8.10
E1	7.40	7.50	7.60
e	2.0 TYP		
F	1.00 TYP		
H	4.40	4.60	4.70
L	6.60	6.80	6.90
K	2.00		