


DESCRIPTION: 0.25W 3KVDC Isolated Single Output DC/DC Converters

The TPR-W25 series are miniature, isolated 0.25W DC/DC converters in a SIP package. They offer the ideal solution in many space critical applications for board level power distribution. The internal SMD construction makes it possible to offer a product with high performance at low cost. The series offers smaller size, improved efficiency, lower output ripple noise and 3KVDC isolation.

FEATURES

RoHS compliant	3KVDC isolation	Efficiency up to 80%
Operating temperature: -40°C to 105°C	Power density up to 0.90W/cm ³	UL 94V-0 package material
Footprint from 1.17cm ²	Industry standard pinout	Input voltage: 3.3V, 5V, 9V, 12V, 15V, 24V
Output voltage: 3.3V, 5V, 9V, 12V, 15V, 24V	Custom solutions available	CE certification

SELECTION GUIDE

Part Number	Nominal Input Voltage	Output Voltage	Output Current (Max./Min)	Efficiency
	V	V	mA	%
TPR0303S-W25	3.3	3.3	75.8/7.58	72
TPR0305S-W25	3.3	5	50/5	74
TPR0503S-W25	5	3.3	75.8/7.58	72
TPR0505S-W25	5	5	50/5	68
TPR0509S-W25	5	9	27.8/2.78	78
TPR0512S-W25	5	12	21/2.1	76
TPR0515S-W25	5	15	16/1.6	79
TPR0524S-W25	5	24	10.4/1.04	81
TPR0905S-W25	9	5	50/5	74
TPR1203S-W25	12	3.3	75.8/7.58	74
TPR1205S-W25	12	5	50/5	69
TPR1209S-W25	12	9	27.8/2.78	76
TPR1212S-W25	12	12	21/2.1	77
TPR1215S-W25	12	15	16/1.6	76
TPR1224S-W25	12	24	10.4/1.04	84
TPR1505S-W25	15	5	50/5	69
TPR1509S-W25	15	9	27.8/2.78	76
TPR1512S-W25	15	12	21/2.1	76
TPR1515S-W25	15	15	16/1.6	75
TPR2405S-W25	24	5	50/5	71
TPR2409S-W25	24	9	27.8/2.78	78
TPR2412S-W25	24	12	21/2.1	78
TPR2415S-W25	24	15	16/1.6	80
TPR2424S-W25	24	24	10.4/1.04	80

Add suffix "P" for continuous short circuit protection, for example TPR0505SP-W25.

INPUT CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Voltage range	3.3V input	2.9	3.3	3.6	V
Voltage range	5V input	4.5	5	5.5	V
Voltage range	9V input	8.03	9	10	V
Voltage range	12V input	11	12	13.3	V
Voltage range	15V input	13.4	15	16.4	V
Voltage range	24V input	22	24	26.6	V

ISOLATION CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation voltage	Tested for 1 second	3000			VDC
Resistance	Viso=1000VDC	1			GΩ

OUTPUT CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Rated Power	TA=-40°C to 85°C			0.25	W
Voltage Set Point Accuracy	See tolerance envelope				
Line regulation	High VIN to low VIN (voltage variation +/-5%)		1.2		%/%
Load Regulation(10% load to rated load)	TPRXX03S-W25 ,TPRXX05S-W25,TPRXX09S-W25		13		%
Load Regulation(10% load to rated load)	TPRXX12S-W25		7		%
Load Regulation(10% load to rated load)	TPRXX15S-W25		7		%
Load Regulation(10% load to rated load)	TPRXX24S-W25		7		%
Ripple & Noise	20MHz bandwidth		50	150	mvp-p

ABSOLUTE MAXIMUM RATINGS

Short-circuit protection	1 second
Lead temperature 1.5mm from case for 10 seconds	300°C
Internal power dissipation	540mW
Input voltage Vin, TPR03 variants	5.5V
Input voltage Vin, TPR05 variants	7V
Input voltage Vin, TPR09 variants	11.5V
Input voltage Vin, TPR12 variants	15V
Input voltage Vin, TPR15 variants	18V
Input voltage Vin, TPR24 variants	28V

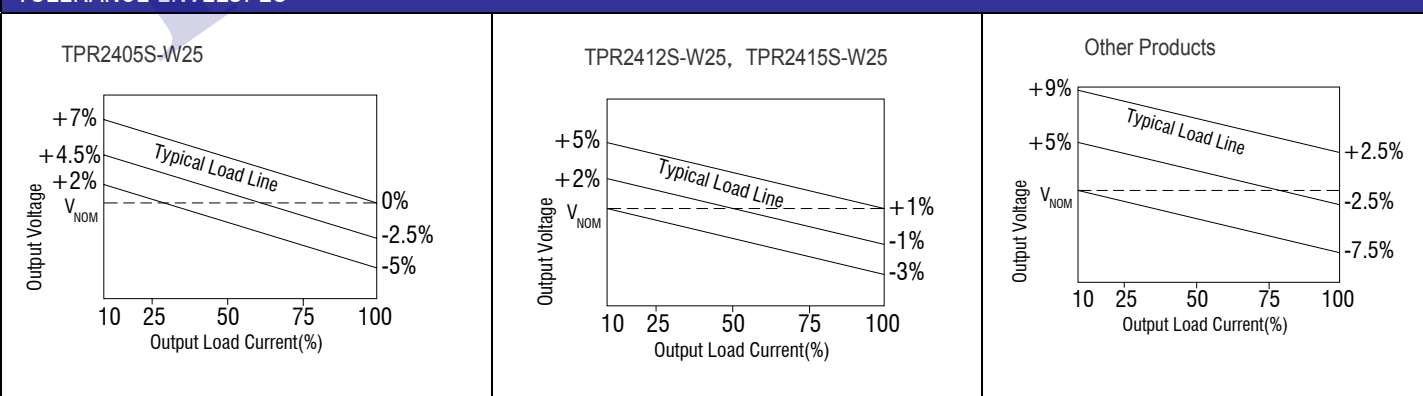
All specifications typical at TA=25° C, nominal input voltage and rated output current unless otherwise specified.

ISOLATION CHARACTERISTICS

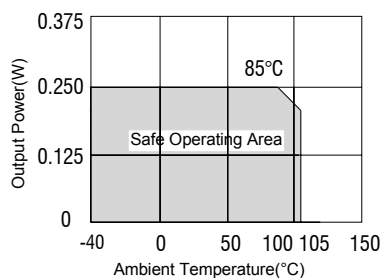
Parameter	Conditions	Min.	Typ.	Max.	Units
Switching frequency	3.3V input		120		kHz
Switching frequency	5V input		120		kHz
Switching frequency	9V input		120		kHz
Switching frequency	12V input		160		kHz
Switching frequency	15V input		90		kHz
Switching frequency	24V input		90		kHz

TEMPERATURE CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Specification	Derating if the temperature ≥85°C	-40		105	°C
Storage		-50		130	°C
Case Temperature above ambient	5V output types		35		°C
Case Temperature above ambient	All other output types		30		°C
Cooling	Free air convection				

TOLERANCE ENVELOPES


TEMPERATURE DERATING GRAPHS



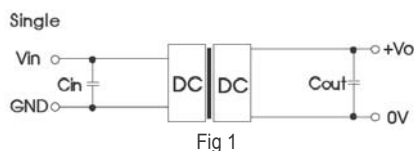
EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022 CLASS B (see Fig. 2 for recommended circuit)
EMI	RE	CISPR22/EN55022 CLASS B (see Fig. 2 for recommended circuit)
EMS	ESD	IEC/EN61000-4-2 Contact $\pm 8\text{KV}$ perf. Criteria B

DESIGN REFERENCE

Typical application circuit

If it is required to further reduce input and output ripple, a filter capacitor may be connected to the input and output terminals, see Fig. 1. Moreover, choosing a suitable filter capacitor is very important, start-up problems may be caused if the capacitance is too large. Under the condition of safe and reliable operation, the recommended capacitive load values are shown in Table 1.



Recommended capacitive load value table (Table 1)

Vin (VDC)	Cin (μF)	Single Vout (VDC)	Cout (μF)
3.3/5	4.7	3.3/5	10
9/12	2.2	9/12	2.2
15	2.2	15/24	1
24	1	--	--

EMC solution-recommended circuit

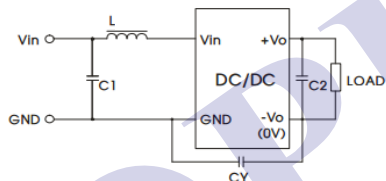


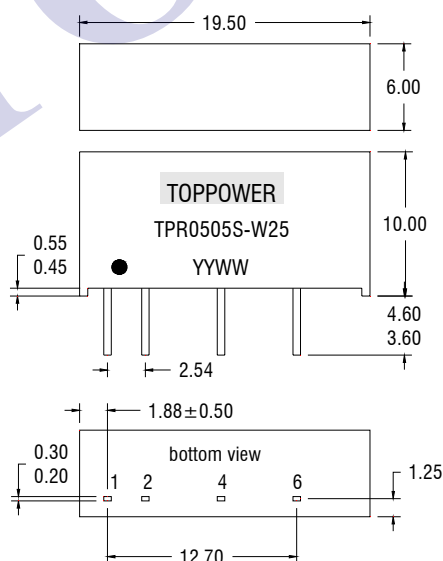
Fig 2

Input voltage (VDC)	3.3/5/9/12	15/24
C1	4.7 μF /50V	
C2	Refer to the Cout in Fig.3	
CY	--	1nF/2KV
LDM	6.8 μH	

Output load should be at min. load 10%.

MECHANICAL DIMENSIONS

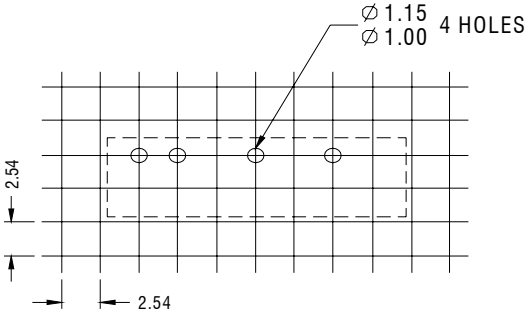
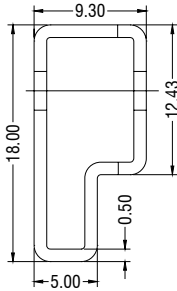
SIP Package



All dimensions in mm $\pm 0.25\text{mm}$. All pins on a 2.54mm pitch and within $\pm 0.25\text{mm}$ of true position.

PIN CONNECTIONS

7 PIN SIP	
Pin	Function
1	+Vin
2	-Vin
4	-Vout
6	+Vout

RECOMMENDED FOOTPRINT DETAILS	TUBE OUTLINE DIMENSIONS
 Ø 1.15 Ø 1.00 4 HOLES 2.54 2.54 All dimensions in mm±0.25mm	 9.30 12.43 18.00 5.00 0.50 All dimensions in mm±0.25mm Tube Quantity : 25PCS

TOPPOWER