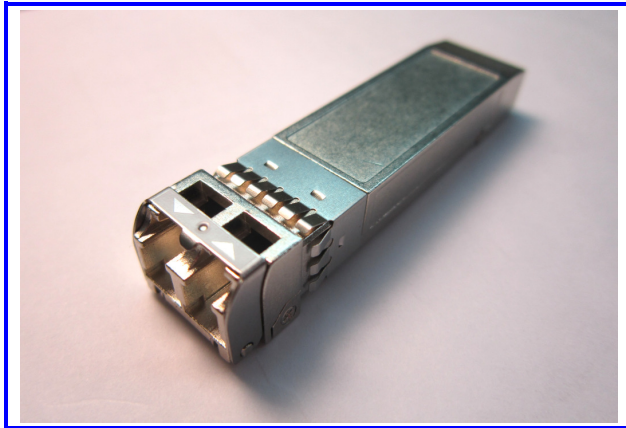




RoHS Compliant
1550 nm Single-mode Transceiver
Small Form Pluggable (SFP+), with Diagnostic Monitoring
10G BASE Ethernet/SONET/SDH



Features

- Compliant with SFF8472 diagnostic monitoring interface Duplex LC connector
- Single power supply 3.3V
- Hot Pluggable
- Up to 80km transmission on SMF
- 1550nm EML laser and APD receiver
- Class 1 laser product complies with EN 60825-1
- Internal transmitter/receiver CDR
- SONET OC-192
- SDH STM 64
- OTN G.709 OTU1e/2/2e FEC bit rates

Ordering Information

PART NUMBER	VOLTAGE	TEMPERATURE	Distance
LE48-H3U-TC-N-LA	3.3V	0°C to 70 °C	80km
LE48-H3U-TI-N-LA	3.3V	-40°C to 85 °C	80km

Diagnostics

Parameter	Range	Accuracy	Unit	Calibration
Internal Transceiver Temperature	-40 to 85	± 3	°C	Internal
Internal Transceiver Voltage	3.14 to 3.46	± 0.1	V	
Bias Current	0 to 120	± 10%	mA	
TX Power	0 to +4	± 3	dB	
RX average Power	-23 to -10	± 3	dB	



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Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNITS	NOTE
Storage Temperature	T_s	-40	85	°C	
Operating Relative Humidity	RH	0	85	%	
Supply Voltage	V_{cc}	0	3.6	V	
Input Voltage	V_{in}	0	V_{cc}	V	

Recommended Operating Conditions

PARAMETER	SYMBOL	MIN	MAX	UNITS	NOTE
Case operating Temperature	T_C	0	70	°C	LE48-H3U-TC-N-LA
		-40	85		LE48-H3U-TI-N-LA
Supply Voltage	V_{cc}	3.14	3.46	V	
Supply Current	$I_{TX} + I_{RX}$		575	mA	LE48-H3U-TC-N-LA
			675		LE48-H3U-TI-N-LA
Power Consumption @3.3V	P		1.9	W	LE48-H3U-TC-N-LA
			2.2		LE48-H3U-TI-N-LA



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Transmitter Electro-optical Characteristics

V_{cc} = 3.14 V to 3.46 V, Over Operating Case Temperature.

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Data Rate	<i>B</i>	9.95	10.3125	11.3	Gbps	
Output Optical Power	<i>P_{out}</i>	0	---	+4.0	dBm	
Extinction Ratio	<i>ER</i>	9.0			dB	
Center Wavelength	<i>λ_c</i>	1530	---	1565	nm	
Spectrum Width	<i>Δ λ</i>			1	nm	
Side mode Suppression ratio	<i>SSR_{min}</i>	30			dB	
Transmitter and Dispersion Penalty	<i>TDP</i>			3	dB	
Relative Intensity Noise	<i>RIN</i>	---	---	-128	dB/Hz	
Optical Return Loss	<i>ORL</i>	21	---	---	dB	
Output Eye						Compliant with IEEE802.3ae
Differential Input Impedance	<i>Z_d</i>		100		Ω	
Differential Input Voltage Swing	<i>V_{DIFF}</i>	300		1000	mVpp	
Transmit Fault Output-Low	<i>TX_FAULT_L</i>	0.0	---	0.5	V	
Transmit Fault Output-High	<i>TX_FAULT_H</i>	2.4	---	<i>V_{CC}</i>	V	
TX_DISABLE Assert Time	<i>t_{off}</i>	---	---	100	μs	
TX_DISABLE Negate Time	<i>t_{on}</i>	---	---	2	ms	
Tx_Fault assert for cooled module	<i>t_{fault}</i>	---	---	50	ms	
TX_DISABLE time to start reset	<i>t_{reset}</i>	10	---	---	μs	



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Receiver Electro-optical Characteristics

V_{CC} = 3.14 V to 3.46 V, Over Operating Case Temperature.

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Data Rate	<i>B</i>	9.95	10.3125	11.3	Gbps	
Optical Input Power-maximum @BER < 10 ⁻¹²	<i>P_{IN}</i>	-7	---	---	dBm	
Receiver Sensitivity(Back to Back) @BER < 10 ⁻¹²	<i>P_{IN}</i>	---	---	-23	dBm	9.95328G (OC192/STM64/10GE WAN) 10.3125G(10GE LAN)
Receiver Sensitivity(Back to Back) @BER < 10 ⁻⁴	<i>P_{IN}</i>	---	---	-25	dBm	10.709G(OUT-2), 11.0491(OUT-1e) 11.0957G(OUT-2e)
Receiver Sensitivity(80km fiber) @BER < 10 ⁻¹²	<i>P_{IN}</i>	---	---	-20	dBm	9.95328G (OC192/STM64/10GE WAN) 10.3125G(10GE LAN)
Receiver Sensitivity(80km fiber) @BER < 10 ⁻⁴	<i>P_{IN}</i>	---	---	-22	dBm	10.709G(OUT-2), 11.0491(OUT-1e) 11.0957G(OUT-2e)
Receiver Reflectance	<i>Ref</i>	---	---	-26	dB	
Operating Center Wavelength	<i>λ_C</i>	1530	---	1565	nm	
Loss of Signal-Asserted	<i>P_A</i>	-38	---	---	dBm	
Loss of Signal-Deasserted	<i>P_D</i>	---	---	-25	dBm	
Differential Output Impedance	<i>Z_d</i>	---	100	---	Ω	
Differential Output Voltage	<i>V_{DIFF}</i>	300	---	800	mVpp	
Receiver Loss of Signal Output Voltage -Low	<i>RX_LOS_L</i>	0	---	0.5	V	
Receiver Loss of Signal Output Voltage -High	<i>RX_LOS_H</i>	2.4	---	<i>V_{CC}</i>	V	
Receiver Loss of Signal Assert Time (off to on)	<i>t_{A,RX_LOS}</i>	---	---	100	μs	
Receiver Loss of Signal Assert Time (on to off)	<i>t_{D,RX_LOS}</i>	---	---	100	μs	

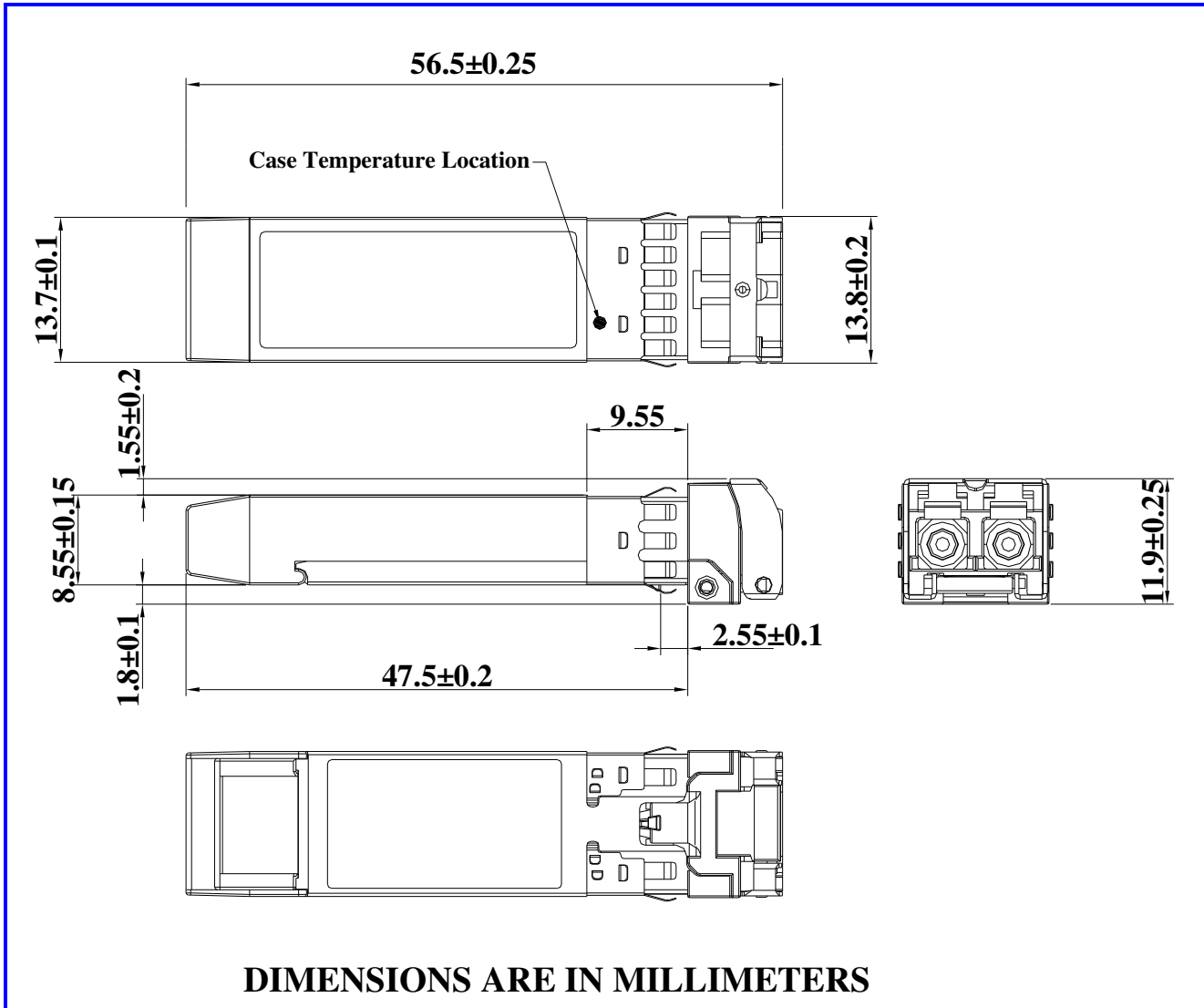
Timing Parameters

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Time to initialize	<i>t_{start_up}</i>			10	s	



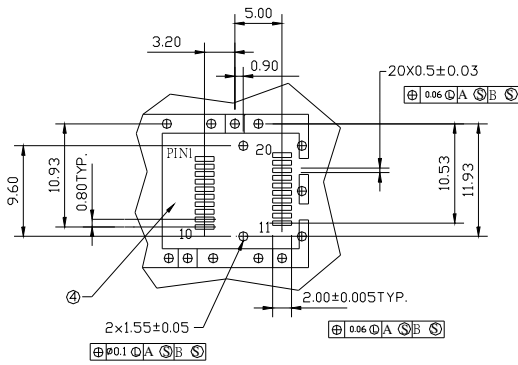
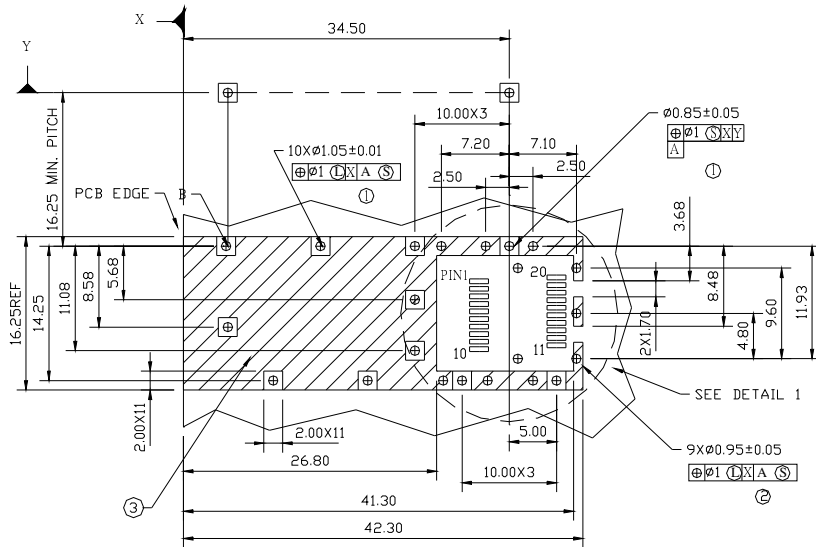
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Dimensions



Latch color: White

SFP host board mechanical layout



DETAIL 1

LEGEND

1. PADS AND VIAS ARE CHASSIS GROUND
2. THROUGH HOLES, PLATING OPTIONAL
3. HATCHED AREA DENOTES COMPONENT AND TRACE KEEPOUT (EXCEPT CHASSIS GROUND)
4. AREA DENOTES COMPONENT KEEPOUT (TRACES ALLOWED)

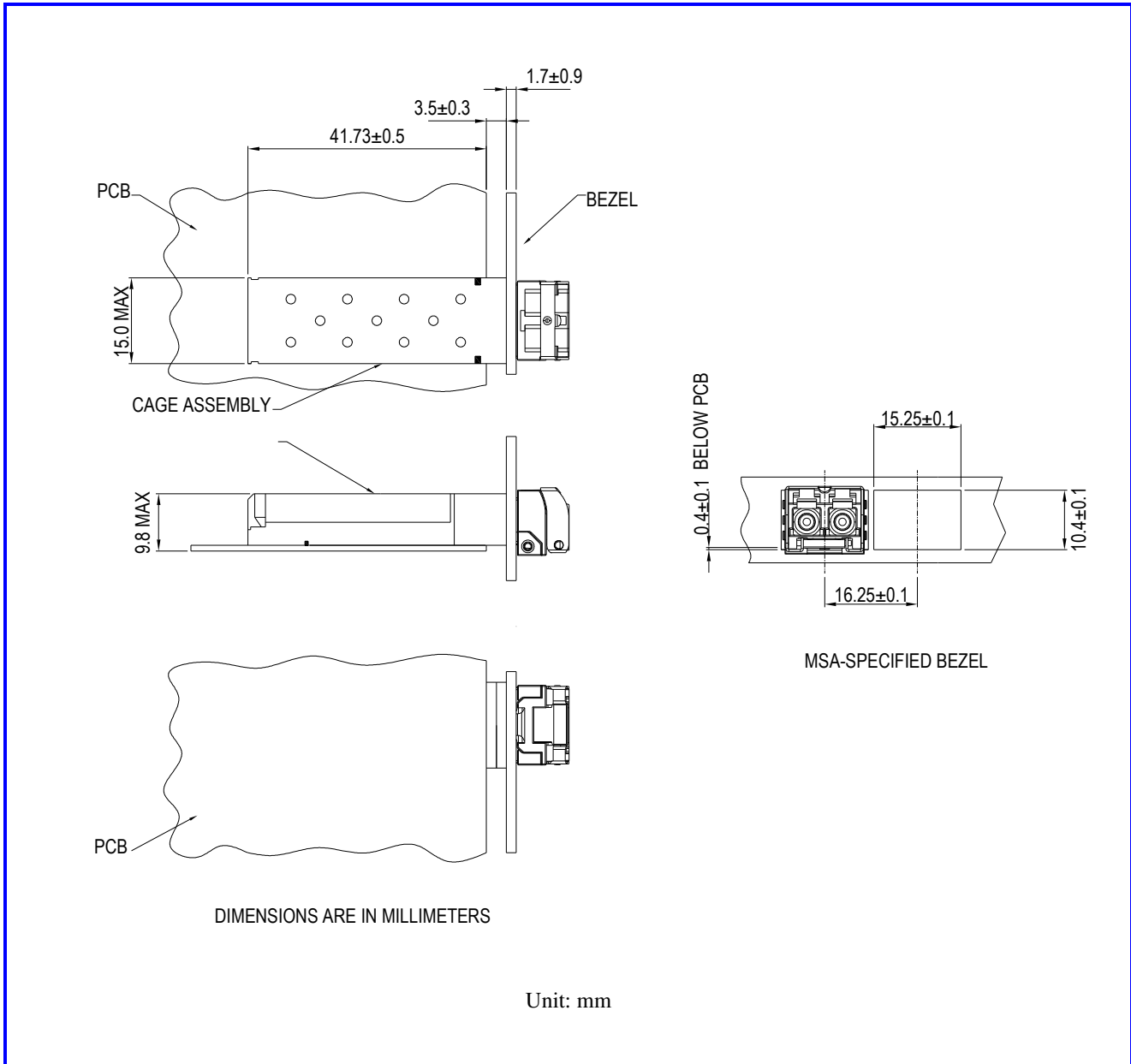
DIMENSIONS ARE IN MILLIMETERS

Unit: mm



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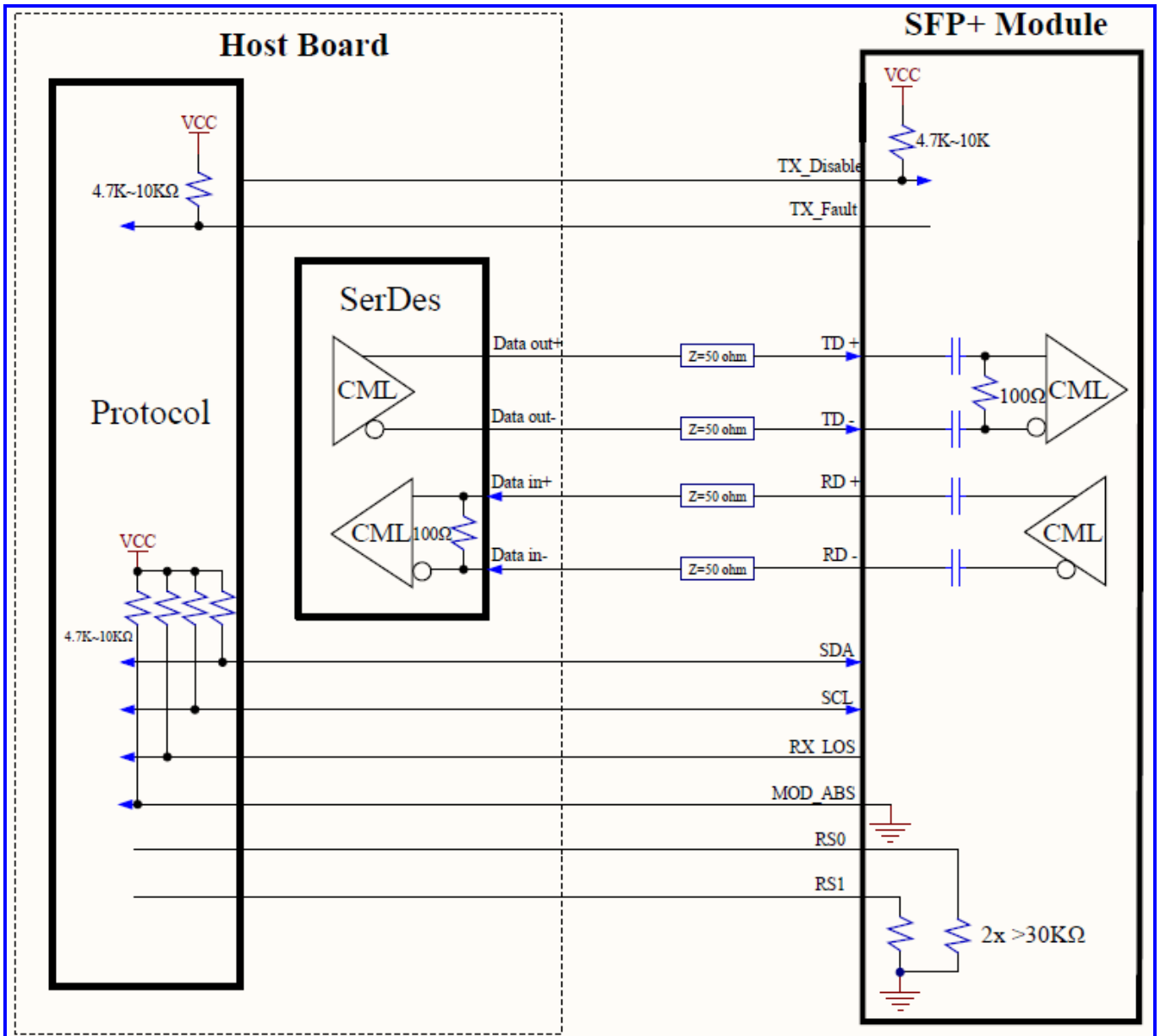
Assembly Drawing





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Recommended Interface Circuit

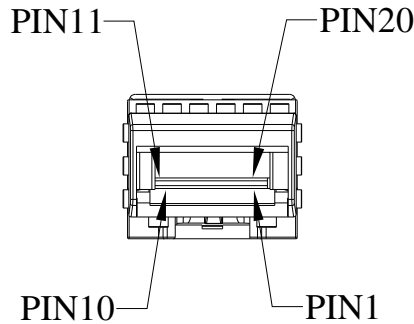




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Pin Assignment

Pin-Out



Pin	Signal Name	Description
1	T_{GND}	Transmit Ground
2	TX_FAULT	Transmit Fault
3	$TX_DISABLE$	Transmit Disable
4	$MOD_DEF (2)$	SDA Serial Data Signal
5	$MOD_DEF (1)$	SCL Serial Clock Signal
6	$MOD_DEF (0)$	TTL Low
7	$RS0$	RX Rate Select, No function implemented
8	RX_LOS	Receiver Loss of Signal, TTL High, open collector
9	$RS1$	TX Rate Select, No function implemented
10	R_{GND}	Receiver Ground
11	R_{GND}	Receiver Ground
12	$RX-$	Receive Data out Bar, ac coupled
13	$RX+$	Receive Data out, ac coupled
14	R_{GND}	Receiver Ground
15	V_{CCR}	Receiver Power Supply
16	V_{CCT}	Transmitter Power Supply
17	T_{GND}	Transmitter Ground
18	$TX+$	Transmit Data in, ac coupled
19	$TX-$	Transmit Data in Bar, ac coupled
20	T_{GND}	Transmitter Ground

Note : All information contained in this document is subject to change without notice.