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RFF70AP-1IW-DBG

SPECIFICATION

CUSTOMER:

APPROVED BY
PCB VERSION
DATE

FOR CUSTOMER USE ONLY

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

Release DATE:

TFT Display Inspection Specification: https://www.raystar-optronics.com/download/products.htm
Precaution in use of TFT module: https://www.raystar-optronics.com/download/declaration.htm



Revision History

VERSION	DATE	REVISED PAGE NO.	Note
0	2018/12/24		First issue



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1.Module Classification Information

R	F	F	70	AP	-	1	I	W	-	D	В	G
1	2	3	4	5	-	6	7	8	-	9	10	11

Description								
R: Raystar Opt	ronics Inc.							
Display Type : F→TFT Type, J→ Custom TFT								
				E:480x272				
				J:240x320				
				O:480x800				
		0 S:480x1	28 T:800x320					
	.0" TFT							
Model Type:								
J:TFT+FR+A/[D BOARD			_				
-			B: TFT+POWER	R BD				
	TFT+FR+POWER BOARD (DC TO DC)							
1: TFT+CONTI	ROL BOARD							
Polarizer	I→Transmissive,	W. T, 6:00 ;	C→Transmissive,	, N. T, 6:00				
Type.	L→Transmissive	, W.T,12:00 ;	F→Transmissive	, N.T,12:00				
	Y→Transmissive	,W.T, IPS TF	Γ;					
-	A→Transmissive	, N.T, IPS TF	Γ					
_	Z→Transmissive	, W.T, O-TFT						
7.0.11 0.11 0.11 0.11	R→Transmissive	, Super W.T,	O-TFT					
	N→Transmissive	, Super W.T,	6:00;					
	Q→Transmissive	, Super W.T,	12:00					
	V→Transmissive	, Super W.T, '	VA TFT					
Backlight	W: LED, White H: LED, High Light White							
Dacklight	F: CCFL, White							
Driver Method	D: Digital A: A	Analog L:L	VDS M:MIPI					
Interfoce	N: without control board A: 8Bit B: 16Bit							
пцепасе	S:SPI Interface R: RS232 U:USB I: I2C							
	N: Without TS	S: resisti	ve touch panel					
TS	C: capacitive to	uch panel cap	acitive touch pane	el (G-F-F)				
	G: capacitive to	uch panel(G-0	G)					
	Display Type: F Solution: A: 128 F:800x K:1280 P:640x Display Size: 7 Version Code. Model Type: A: TFT LCD E: TFT+FR+A/I N: TFT+FR+A/I S: TFT+FR+D 1: TFT+CONTI Polarizer Type, Temperature range, View direction Backlight Driver Method Interface	R: Raystar Optronics Inc. Display Type: F→TFT Type, J→ 6 Solution: A: 128x160 B:320x23-6 F:800x480 G:640x48 K:1280x800 L:240x40 P:640x320 Q:800x60 Display Size: 7.0" TFT Version Code. Model Type: A: TFT LCD E: TFT+FR+CONTROL BOARD J: TFT+FR+A/D BOARD N: TFT+FR+A/D BOARD N: TFT+FR+A/D BOARD N: TFT+FR+A/D BOARD N: TFT+FR+A/D BOARD Polarizer Type, Temperature range, View direction Polarizer Type, Temperature range, View direction Backlight Driver Method D: Digital A: A N: without control S:SPI Interface N: Without TS C: capacitive to the capacity of the capaci	R: Raystar Optronics Inc. Display Type: F→TFT Type, J→ Custom TFT Solution: A: 128x160 B:320x234 C:320x24 F:800x480 G:640x480 H:1024x K:1280x800 L:240x400 M:1024x P:640x320 Q:800x600 S:480x1 Display Size: 7.0" TFT Version Code. Model Type: A: TFT LCD E: TFT+FR+CONTROL BOARD J: TFT+FR+A/D BOARD N: TFT+FR+A/D BOARD N: TFT+FR+A/D BOARD S: TFT+FR+POWER BOARD (DC TO DC) 1: TFT-CONTROL BOARD Polarizer Type, Temperature range, View direction I→Transmissive, W. T, 6:00; L→Transmissive, W.T, 12:00; Y→Transmissive, W.T, 12:00; Y→Transmissive, W.T, IPS TFT A→Transmissive, W.T, O-TFT R→Transmissive, Super W.T, V→Transmissive, Super W.T,	R : Raystar Optronics Inc. Display Type : F→TFT Type, J→ Custom TFT Solution: A: 128x160 B:320x234 C:320x240 D:480x234 F:800x480 G:640x480 H:1024x600 I:320x480 K:1280x800 L:240x400 M:1024x768 N:128x128 P:640x320 Q:800x600 S:480x128 T:800x320 Display Size : 7.0" TFT Version Code. Model Type: A: TFT LCD A: TFT LCD E: TFT+FR+CONTROL BOARD J: TFT+FR+A/D BOARD J: TFT+FR+A/D BOARD J: TFT+FR+A/D BOARD BOARD (DC TO DC) J: TFT+FR+D/W B S: TFT+FR+POWER BOARD (DC TO DC) J: TFT+FR+D/W B S: TFT+FR+D/W B J: TFT+POWER BOARD J: TFT+POWER S: TFT+FR+D/W B J: TFT+POWER BOARD J: TFT+POWER J: TFT+POWER </th				



2.Summary

TFT 7.0"is a TN transmissive type color active matrix TFT liquid crystal display that use amorphous silicon TFT as switching devices. This module is a composed of a TFT_LCD module, It is usually designed for industrial application and this module follows RoHs.



3. General Specifications

■ Size: 7.0 inch

■ Dot Matrix: 800 x RGB x 480(TFT) dots

■ Module dimension: 165.0(W) x 100.0(H) x 13.375(D) mm

Active area: 154.08 x 85.92 mm

Dot pitch: 0.0642 x 0.179 mm

■ LCD type: TFT, Negative, Transmissive

■ View Direction: 12 o'clock

Gray Scale Inversion Direction: 6 o'clock

■ Aspect Ratio: 16:9

Backlight Type: LED, Normally White

Controller IC: RA8875

■ TFT Interface: Digital 8080 family MPU 8bit/16bit

■ CTP IC: FT5426 or Equivalent

CTP Interface: I2C

■ CTP FW Version: 03

■ With /Without TP: With CTP

Surface: Glare

*Color tone slight changed by temperature and driving voltage.



4.Interface

4.1. LCM PIN Definition

Pin	Symbol	Function	Remar k
1	GND	System ground pin of the IC.	
		Connect to system ground.	
2	VDD	Power Supply: +3.3V	_
3	BLE	Backlight control signal , H: On \ L: Off	
4	RS	Data/Command select	
5	WR	Write strobe signal	
6	RD	Read strobe signal	
7	DB0	Data bus	
8	DB1	Data bus	
9	DB2	Data bus	
10	DB3	Data bus	
11	DB4	Data bus	
12	DB5	Data bus	
13	DB6	Data bus	
14	DB7	Data bus	
15	DB8	Data bus (When select 8bits Mode, this pin is NC)	Note1
16	DB9	Data bus (When select 8bits Mode, this pin is NC)	Note1
17	DB10	Data bus (When select 8bits Mode, this pin is NC)	Note1
18	DB11	Data bus (When select 8bits Mode, this pin is NC)	Note1
19	DB12	Data bus (When select 8bits Mode, this pin is NC)	Note1
20	DB13	Data bus (When select 8bits Mode, this pin is NC)	Note1
21	DB14	Data bus (When select 8bits Mode, this pin is NC)	Note1
22	DB15	Data bus (When select 8bits Mode, this pin is NC)	Note1
23	NC	No connect	
24	CTP_INT	CTP_ External interrupt to the host	
25	CS	Chip select	
26	RST	Hardware reset	
27	L/R	Left / right selection; Default L/R=H	Note 2,3
28	U/D	Up/down selection; ; Default U/D=L	Note 2,3
29	CTP_SCL	I2C clock input	
30	CTP_SDA	I2C data input and output	
31	CTP_RST	CTP_ External Reset, Low is active	
32	NC	No connect	
33	VLED-	Power for LED Driver IC(GND)	
34	VLED-	Power for LED Driver IC(GND)	
35	VLED+	Power for LED Driver IC(+5V)	
36	VLED+	Power for LED Driver IC(+5V)	

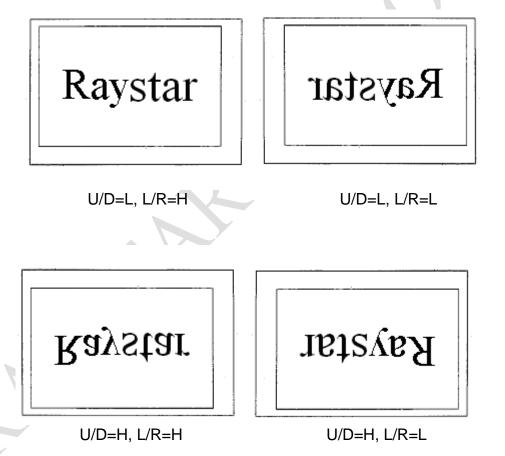


Note1: When select 8bit mode, DB0~DB7 be used, DB8~DB15 no connect When select 16bit mode, DB0~DB15 be used

Note 2: Selection of scanning mode

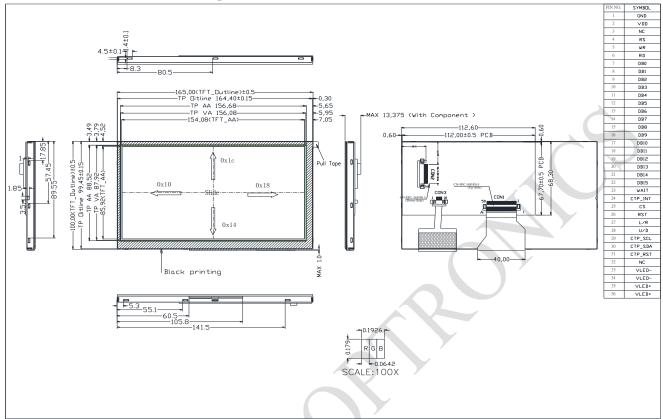
Setting of scan control input		Coopping direction
U/D	L/R	Scanning direction
GND	VDD	Up to down, left to right
VDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
VDD	VDD	Down to up, left to right

Note 3: Definition of scanning direction. Refer to the figure as below:



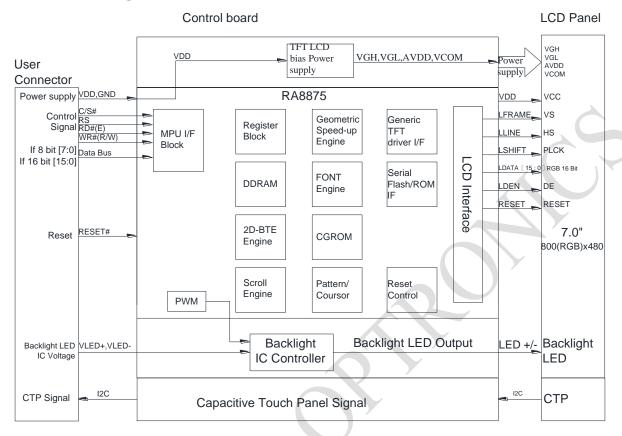


5.Contour Drawing





6.Block Diagram





7. Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	TOP	-20	_	+70	$^{\circ}\mathbb{C}$
Storage Temperature	TST	-30	_	+80	$^{\circ}\mathbb{C}$

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. \leq 60°C, 90% RH MAX. Temp. > 60°C, Absolute humidity shall be less than 90% RH at 60°C



8. Electrical Characteristics

8.1. Operating conditions:

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark
Supply Voltage For LCM	VDD	_	3.2	3.3	3.4	V	_
Supply Current For LCM	IDD	_	_	120	180	mA	Note1

Note 1 : This value is test for VDD=3.3V , Ta=25℃ only

8.2. Backlight driving conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Operation Current For LED	VLED=5V	400	_	600	mA	Note 1,2
Driver						
Power Consumption	VLED=5V	2000	- (3000	mW	Note 1,2
Supply Voltage For LED Driver	VLED+	_	5	7	V	_
LED Life Time	_	_	50,000		Hr	Note 2,3,4

Note 1 : Base on VLED= 5V for the back light driver IC specification

Note 2 : Ta = 25 °C

Note 3: Brightness to be decreased to 50% of the initial value

Note 4: The single LED lamp case



9.DC CHARATERISTICS

Doromotor	Symbol	Rating				Condition
Parameter	Symbol	Min	Тур	Max	Unit	Condition
Low level input voltage	VIL	GND	-	0.2 VDD	V	
High level input voltage	VIH	0.8 VDD	-	VDD	V	36



10.AC CHARATERISTICS

10.1. Parallel I/F Protocol

The following timing charts are used to describe the timing specification of the standard 8080 and 6800 interfaces.

6800 - 8/16-bit Interface

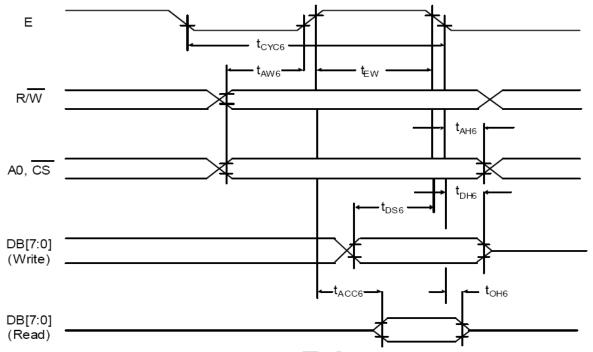


Figure 1: 6800 MCU Waveform

Table 1: 6800 MCU I/F Timing

Cymbol	Symbol Parameter		ating		Cymbol	
Symbol	Parameter	Min.	Max	Unit	Symbol	
tCYC6	Cycle time	50		ns		
tEW	Strobe Pulse width	20		ns		
tAW6	Address setup time	0		ns		
tAH6	Address hold time	10		ns	tc is one system clock	
tDS6	Data setup time	20		ns	period: tc = 1/SYS_CLK	
tDH6	Data hold time	10		ns		
tACC6	Data output access time	0	20	ns		
tOH6	Data output hold time	0	20	ns		



8080 - 8/16-bit Interface

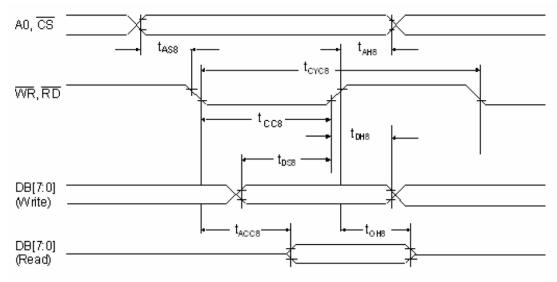


Figure 2: 8080 Waveform Table 2: 8080 MCU I/F Timing

Symbol	Parameter	Rating		Unit	Symbol	
Symbol	Parameter	Min.	Max.	Offic	Symbol	
tCYC8	Cycle time	50		ns		
tCC8	Strobe Pulse width	20		ns		
tAS8	Address setup time	0):	ns	tc is one system	
tAH8	Address hold time	10		ns	clock period:	
tDS8	Data setup time	20		ns	tc = 1/SYS_CLK	
tDH8	Data hold time	10		ns		
tACC8	Data output access time	0	20	ns		
tOH8	Data output hold time	0	20	ns		



The data bus width of RA8875 can be selected to 8-bit/16-bit by setting the Bit [1:0] of SYSR. When Bit [1:0] of SYSR is cleared to "00", then the data bus is 8-bit. If Bit [1:0] of SYSR is set to

"11", then the data transition is set as16-bit. No matter what type of MCU I/F is selected (6800/8080), both of them can be changed the bus width when need. But if the 8-bit is used, it needs double transmission time than 16-bit bus and all of the registers must be accessed by 8-bit data.

The continuous data write speed determines the display update speed. The cycle-to-cycle interval must be larger than 4 times of system clock period. Over the specification may cause the data lose or function fail. Please refer to Figure 6-5 and Figure 6-6 for waveform detail. In order to reduce the transmission interference between MCU interface and RA8875, It is suggested that a small capacitor to the GND should be added at the signal of CS#, RD#, and WR #. If using cable to connect MCU and RA8875, please keep the cable length less than 20cm. Otherwise it's suggested to add 1~10Kohm pull-up resistors on pins CS#, RD#, WR# and RS.



MPU6800 Data Write Speed Limit

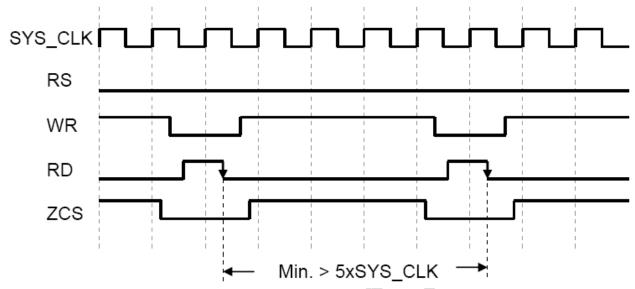


Figure 3: 6800 I/F Continuous Data Write Cycle Waveform

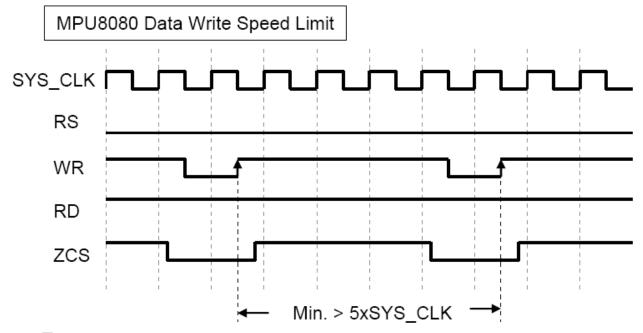


Figure 4: 8080 I/F Continuous Data Write Cycle Waveform



10.2. Data transfer order Setting

MCU Data Bus 8-Bit

The following illustration is used for 16-bit MCU.



Figure 5: Color illustrations for 16-Bit Data Bus MCU

10.3. Register Depiction

Please consult the spec of RA8875
Please consult the spec of FOCALTECH FT5x06



11. Optical Characteristics

Item		Symbol	Condition.	Min	Тур.	Max.	Unit	Remark
Response time		Tr	θ=0°、Φ=0°	-	10	20	.ms	Note 3
Kesponse til	IIE	Tf	$\theta=0$, $\Phi=0$	-	15	30	.ms	Note 5
Contrast rat	io	CR	At optimized viewing angle	400	500	-	-	Note 4
Color		Wx	θ=0°、Φ=0	0.26	0.31	0.36		Note 2,6,7
Chromaticity	White	Wy	$\theta=0$, $\Phi=0$	0.28	0.33	0.38		14016 2,0,7
Viewing angle	Hor. —	ΘR	CR≧10	60	70	ı	Deg.	Note 1
(Gray Scale		ΘL		60	70	1		
Inversion		ΦТ		40	50	-		
Direction)		ФВ		60	70	-		
Brightness		-	-	280	380	,	cd/m ²	Center of display
Uniformity		(U)	-	70	-	ı	%	Note 5

Ta=25±2°C, VLED / ILED= 5V / 400mA

Note 1: Definition of viewing angle range

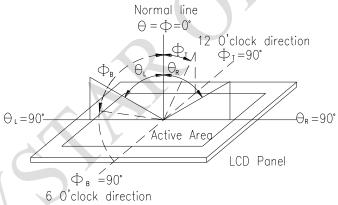


Fig. 11.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.



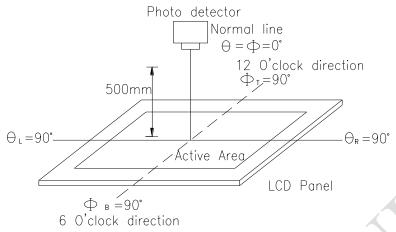
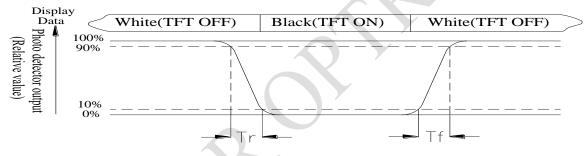


Fig. 11.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90%to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10%to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.



Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/Lmax x100%

L = Active area length

W = Active area width

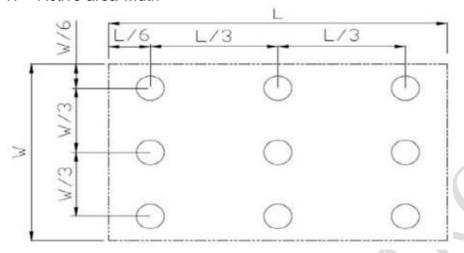


Fig11.3. Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931)
Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.



12.Reliability

Content of Reliability Test (Wide temperature, -20° C~70° C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C/70°C 10 cycles	
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact) ,±800v(air), RS=330Ω CS=150pF 10 times	

Note1: No dew condensation to be observed.

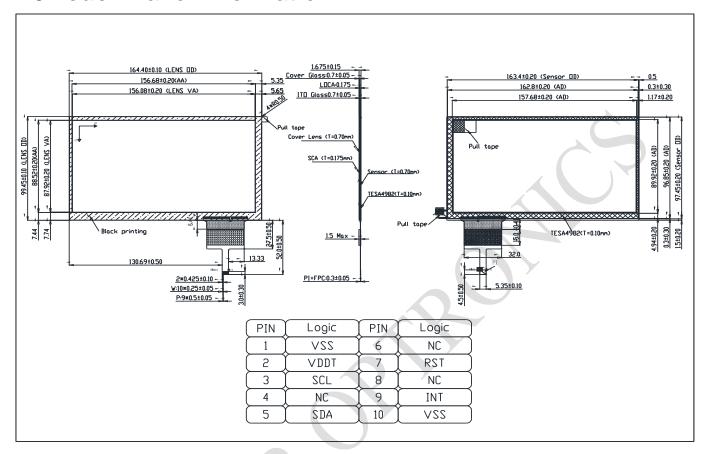
Note2: The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

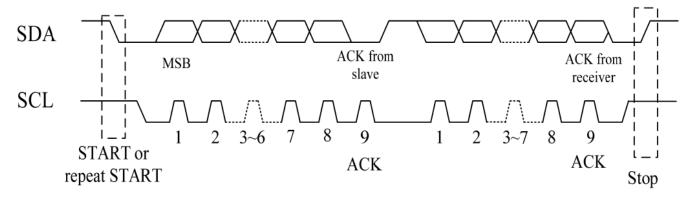


13.Touch Panel Information

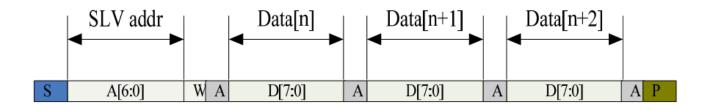




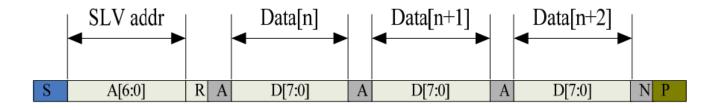
13.1. CTP I2C Timing:



I2C Serial Data Transfer Format



I2C master write, slave read



I2C master read, slave write



Mnemonics	Description					
S	I2C Start or I2C Restart					
A[6:0]	Slave address					
R/W	READ/WRITE bit, '1' for read, '0'for write					
A(N)	ACK(NACK) bit					
Р	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)					

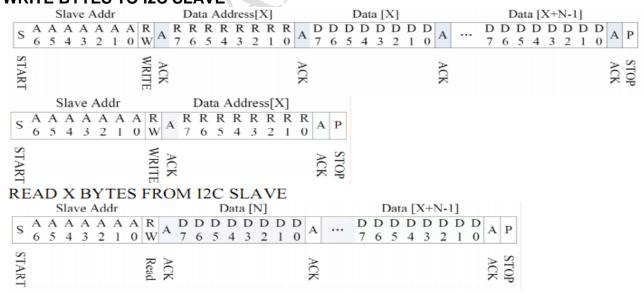
Lists the meanings of the mnemonics used in the above figures

Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	1.3	\
Hold time (repeated) ST ART condition	us	0.6	\
Data setup time	ns	100	\
Setup time for a repeated START condition	us	0.6	\
Setup time for STOP condition	us	0.6	\

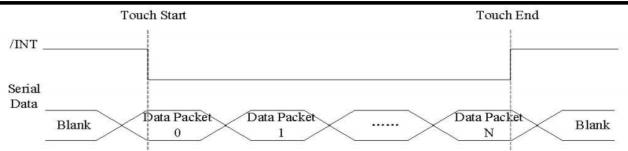
Interface Timing Characteristics

AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA.HERE IS THE TIMING TO GET TOUCH DATA.

WRITE BYTES TO I2C SLAVE







Address: 0X38

TOUCH DATA READ PROTOCOL

NAME	VALUE	DESCRIPTION
START CH	0X00	START COMMAND FOR CTPM TOUCH DATA PACKET,HOST MUST SEND CTPM A START CH COMMAND BEFORE READ TOUCH DATA
Lst READ BYTE~ LAST READ BYTE		TOUCH DATA PACKET SENT BY CTPM,EACH BYTE HAS 8-BIT DATA ,A TOUCH DATA PACKET CONSISTS OF N BYTE

Address	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0	Host Access
00h	DevideMode		Devic	e Mode	el[2:0]		_	_		RW
01h	Gest_ID			,	Gesture	D[7:0]]			R
02h	TD_Status	N	-	_		Numb touch	er of points[3	3:0]		R
03h	Touch1_XH		1 st Event Flag			1 st Touch X Position[11:8]			R	
04h	Touch1_XL	1 st Tou	1 st Touch X Position[7:0]				R			
05h	Touch1_YH	1 st Tou	1 st Touch ID[3:0] 1 st Touch Y Position[11:8]				R			
06h	Touch1_YL	1 st Tou	1 st Touch Y Position[7:0]				R			
09h	Touch2_XH	2 nd Event Flag		_	_	2 nd Tou X Pos	uch ition[11:	:8]		R
0Ah	Touch2_XL	2 nd To	2 nd Touch X Position[7:0]				R			
0Bh	Touch2_YH	2nd Touch ID		ID[3:0]		2ndTo Y Pos	uch ition[11:	:8]		R
0Ch	Touch2_YL	2nd To	2nd Touch Y Position[7:0]						R	
0Fh	Touch3_XH	3rdE Fla	vent ag	_	_	3rdTo	uch ition[11:	:8]		R





10h	Touch3_XL	3rd Touch X	3rd Touch X Position[7:0]		R
11h	Touch3_YH	3rdTouch ID[3:0]		3rdTouch Y Position[11:8]	R
12h	Touch3_YL	3rd Touch Y	Position[7:0]		R
15h	Touch4_XH	4thEvent Flag	_	4thTouch X Position[11:8]	R
16h	Touch4_XL	4th Touch X	4th Touch X Position[7:0]		R
17h	Touch4_YH	4thTouch ID[3:0]		4thTouch Y Position[11:8]	R
18h	Touch4YL	4th Touch Y Position[7:0]			R
1Bh	Touch5_XH	5thEvent Flag	_	5thTouch X Position[11:8]	R
1Ch	Touch5_XL	5th Touch X Position[7:0]		R	
1Dh	Touch5_YH	5thTouch ID[3:0]		5thTouch Y Position[11:8]	R
1Eh	Touch5_YL	5th Touch Y	5th Touch Y Position[7:0]		



14.Initial Code For Reference

```
void Initial RA8875()
 RES = 1:
 Delay1ms (10);
               //Active low
 RES = 0:
 Delay1ms (50);
 RES = 1:
 Delay1ms (100);
 LCD CmdWrite(0x88);
 LCD DataWrite(0x0b);
 LCD_DataRead();
 Delay1ms(1);
 LCD CmdWrite(0x89):
 LCD DataWrite(0x02);
 LCD DataRead();
 Delay1ms(1);
 LCD_CmdWrite(0x10);
 LCD DataWrite(0x0F);
 LCD_CmdWrite(0x04); LCD_DataWrite(0x81);
 Delay1ms(1);
  LCD_CmdWrite(0x14);
  LCD DataWrite(0x63);
  LCD_CmdWrite(0x15);
  LCD DataWrite(0x03):
  LCD CmdWrite(0x16);
  LCD_DataWrite(0x03);
  LCD CmdWrite(0x17);
  LCD DataWrite(0x02):
  LCD CmdWrite(0x18);
  LCD_DataWrite(0x00);
  LCD CmdWrite(0x19);
  LCD DataWrite(0xdf):
 LCD CmdWrite(0x1a);
  LCD_DataWrite(0x01);
  LCD_CmdWrite(0x1b);
  LCD DataWrite(0x14);
  LCD CmdWrite(0x1c):
  LCD DataWrite(0x00);
  LCD CmdWrite(0x1d):
  LCD_DataWrite(0x06);
  LCD CmdWrite(0x1e);
  LCD_DataWrite(0x00);
```



```
LCD_CmdWrite(0x1f);
   LCD_DataWrite(0x01);
   LCD_CmdWrite(0x30);
   LCD_DataWrite(0x00);
   LCD_CmdWrite(0x31);
   LCD_DataWrite(0x00);
   LCD_CmdWrite(0x34);
   LCD_DataWrite(0x1F);
   LCD_CmdWrite(0x35);
   LCD_DataWrite(0x03);
   LCD_CmdWrite(0x32);
   LCD_DataWrite(0x00);
   LCD_CmdWrite(0x33);
   LCD_DataWrite(0x00);
   LCD_CmdWrite(0x36);
   LCD_DataWrite(0xdf);
   LCD_CmdWrite(0x37);
   LCD_DataWrite(0x01);
}
```



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	LCM Sample	Estimate Feedback Sheet					
Module Number :							
1 · Panel Specification :							
1. Panel Type:	□ Pass	□ NG ,					
2. View Direction:	□ Pass	□ NG ,					
3. Numbers of Dots:	□ Pass	□ NG ,					
4. View Area:	□ Pass	□ NG ,					
5. Active Area:	□ Pass	□ NG ,					
6.Operating Temperature:	□ Pass	□ NG ,					
7.Storage Temperature :	□ Pass	□ NG ,					
8.Others:							
2 · Mechanical Specification :							
1. PCB Size :	□ Pass	□ NG ,					
2.Frame Size :	□ Pass	□ NG ,					
3.Materal of Frame:	□ Pass	□ NG ,					
4.Connector Position:	□ Pass	□ NG ,					
5.Fix Hole Position:	□ Pass	□ NG ,					
6.Backlight Position:	□ Pass	□ NG ,					
7. Thickness of PCB:	□ Pass	□ NG ,					
8. Height of Frame to PCB:	□ Pass	□ NG ,					
9.Height of Module:	□ Pass	□ NG ,					
10.Others:	□ Pass	□ NG ,					
3 · Relative Hole Size :							
1.Pitch of Connector:	□ Pass	□ NG ,					
2.Hole size of Connector:	□ Pass	□ NG ,					
3.Mounting Hole size:	□ Pass	□ NG ,					
4.Mounting Hole Type:	□ Pass	□ NG ,					
5.Others:	□ Pass	□ NG ,					
4 · Backlight Specification :							
1.B/L Type:	□ Pass	□ NG ,					
2.B/L Color:	□ Pass	□ NG ,					
3.B/L Driving Voltage (Referen	ce for LED Ty	pe) : □ Pass □ NG ,					
4.B/L Driving Current:	□ Pass	□ NG ,					
5.Brightness of B/L:	□ Pass	□ NG ,					
6.B/L Solder Method:	□ Pass	□ NG ,					
7.Others:	□ Pass	□ NG ,					



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Module Number :		
5 · Electronic Characteristics of	Module :	
1.Input Voltage:	□ Pass	□ NG ,
2.Supply Current:	□ Pass	□ NG ,
3.Driving Voltage for LCD:	□ Pass	□ NG ,
4.Contrast for LCD:	□ Pass	□ NG ,
5.B/L Driving Method:	□ Pass	□ NG ,
6.Negative Voltage Output:	□ Pass	□ NG ,
7.Interface Function:	□ Pass	□ NG ,
8.LCD Uniformity:	□ Pass	□ NG ,
9.ESD test:	□ Pass	□ NG ,
10.Others:	□ Pass	□ NG ,
Sales signature :		
Customer Signature :		Date: / /
Customer Signature		Date · / /