

A2G20N3300MT4

3300V N-Channel MOSFET

Features

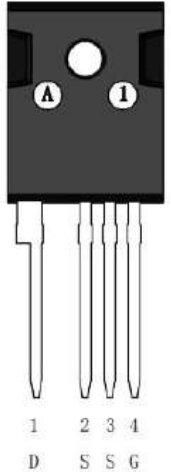
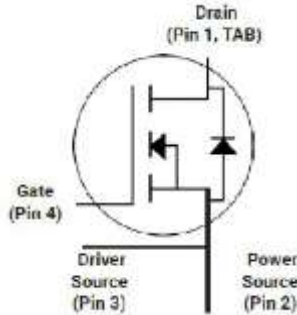
- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low $R_{DS(on)}$
- Optimized package with separate driver source pin
- Easy to parallel and simple to drive
- ROHS Compliant, Halogen free

Application

- EV motor drive
- High Voltage DC/DC Converters
- Switch Mode Power Supplies
- Solar inverters
- EV charging

Product Summary

V_{DS}	3300V
I_D	20A



Ordering Information

Part Number	Marking	Package	Packaging
A2G20N3300MT4	A2G20N3300MT4	TO-247-4	Tube

Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage	3300	V
I_D	Drain Current (continuous) at $T_C=25^\circ\text{C}$	20	A
I_D	Drain Current (continuous) at $T_C=100^\circ\text{C}$	12	A
I_{DM}	Drain Current (pulsed)	80	A
V_{GS}	Gate-Source Voltage	-10/+22	V
P_D	Power Dissipation $T_C=25^\circ\text{C}$	230	W
T_J, T_{stg}	Junction and Storage Temperature Range	-55 to +175	$^\circ\text{C}$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Typical Performance-Static

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV_{DS}	Drain-source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	3300			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=3300\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$			100	μA
I_{GSS}	Gate-body Leakage Current	$V_{DS}=0\text{V}, V_{GS}=-10\text{ to }22\text{V}$			150	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=20\text{mA}$	2	3	4	V
$V_{GS(on)}$	Recommended turn-on Voltage	Static		18		V
$V_{GS(off)}$	Recommended turn-off Voltage			-5		V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS}=18\text{V}, I_D=20\text{A}$		160	200	$\text{m}\Omega$
		$V_{GS}=18\text{V}, I_D=20\text{A}, T_J=175^\circ\text{C}$		288		$\text{m}\Omega$

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Typical Performance-Dynamic

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C _{iss}	Input Capacitance	V _{DS} =1000V, f=1MHz V _{AC} =25mV		2068		pF
C _{oss}	Output Capacitance			38		pF
C _{rss}	Reverse Transfer Capacitance			3.5		pF
g _{fs}	Transconductance	V _{DS} =10V, I _D =10A		5		S
E _{OSS}	C _{OSS} Stored Energy	V _{DS} =1000V, f=1MHz		25		uJ
E _{ON}	Turn-On Energy (Body Diode)	V _{DS} =1700V V _{GS} =-5/18V, I _D =10A L=200uH, T _J =25°C		186		uJ
E _{OFF}	Turn-Off Energy (Body Diode)			55		uJ
Q _g	Total Gate Charge			110		nC
Q _{gs}	Gate-source Charge	V _{DS} =1000V V _{GS} =-5/18V		20		nC
Q _{gd}	Gate-Drain Charge	I _D =10A		22		nC
R _{G(int)}	Internal Gate Resistance	f=1MHz, V _{AC} =25mV		1.5		Ω
t _{d(on)}	Turn-on Delay Time	V _{DS} =1700V V _{GS} =-5/18V, I _D =10A L=200uH, R _{ext} =5Ω		26		ns
t _r	Rise Time			20		ns
t _{d(off)}	Turn-off Delay Time			23		ns
t _f	Fall Time			15		ns

Typical Performance-Reverse Diode (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{FSD}	Forward Voltage	V _{GS} =0V, I _F =10A, T _J =25°C		3.3	6	V
		V _{GS} =0V, I _F =10A, T _J =175°C		3.1	6	V
I _S	Continuous Diode Forward Current	V _{GS} =0V, T _C =25°C		20		A
t _{rr}	Reverse Recovery Time	V _{GS} =-5V, I _F =10A		62		ns
Q _{rr}	Reverse Recovery Charge	V _R =1700V		850		nC
I _{rrm}	Peak Reverse Recovery Current	di/dt=500A/μs, T _J =175°C		12		A

Thermal Characteristics

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.65	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	40	°C/W

The values are based on the junction-to case thermal impedance which is measured with the device mounted to a large heat sink assuming maximum junction temperature of T_J(max)=175°C.

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Electrical Characteristics

Fig1. Output characteristics ($T_J = 25^\circ\text{C}$)

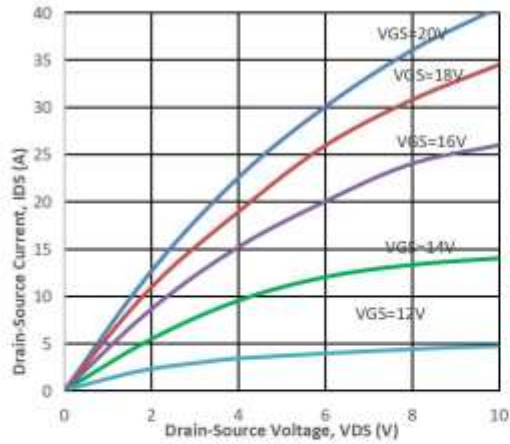


Fig2. Output characteristics ($T_J = 175^\circ\text{C}$)

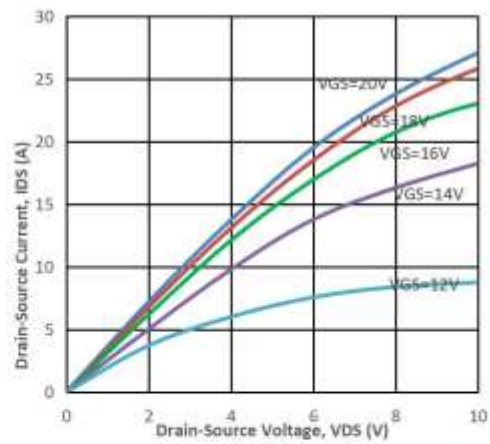


Fig3. Normalized On-Resistance vs. Temperature

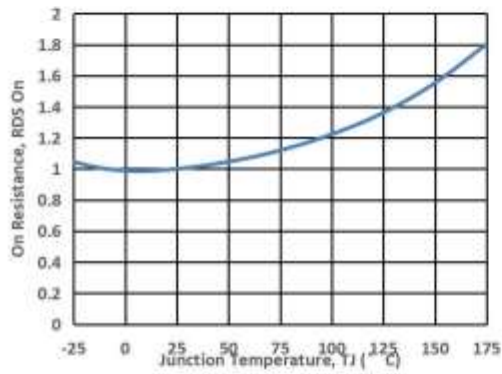


Fig4. On-Resistance vs. Temperature

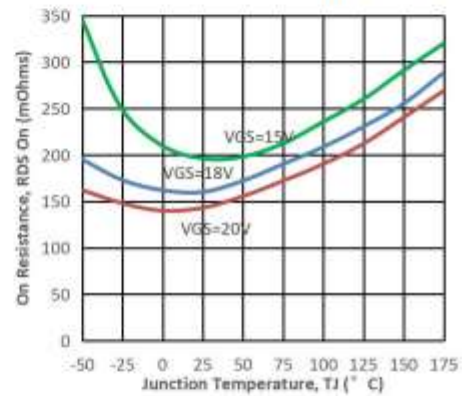


Fig5. Transfer Characteristic

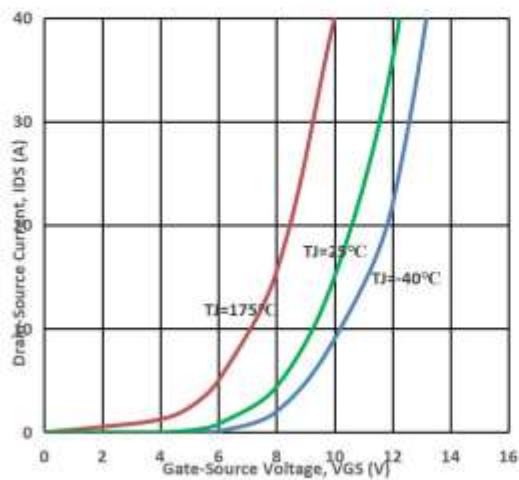
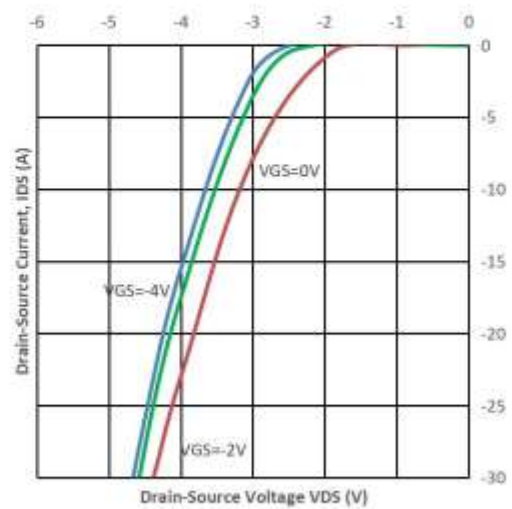


Fig6. Body Diode Characteristic at 25°C



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Fig7. Threshold Voltage vs. Temperature

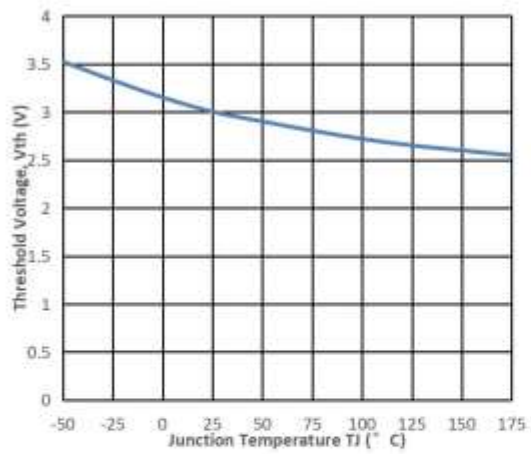


Fig8. Gate Charge Characteristics

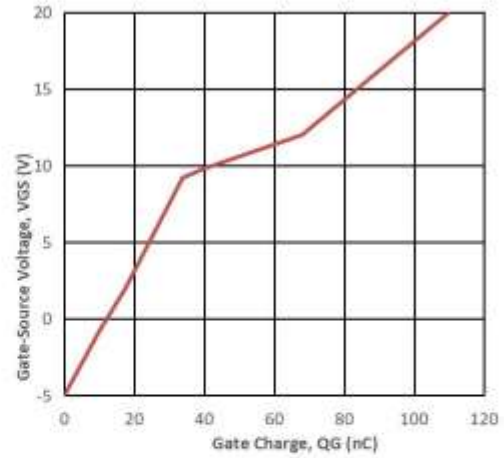


Fig9. 3rd Quadrant Characteristic at 25 °C

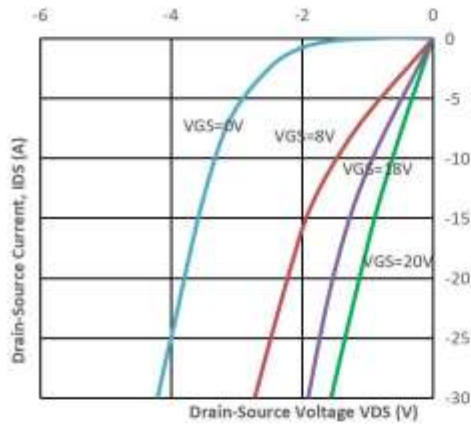


Fig10. Output Capacitor Stored Energy

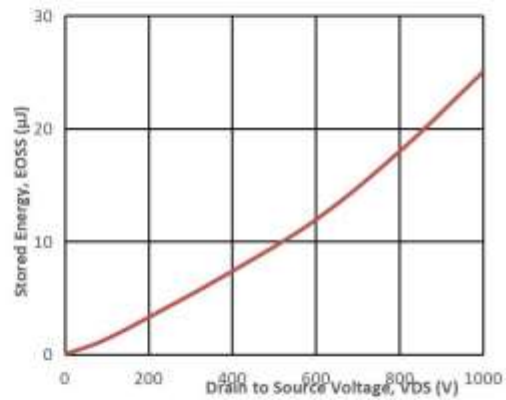


Fig11. Capacitances vs. Drain-Source

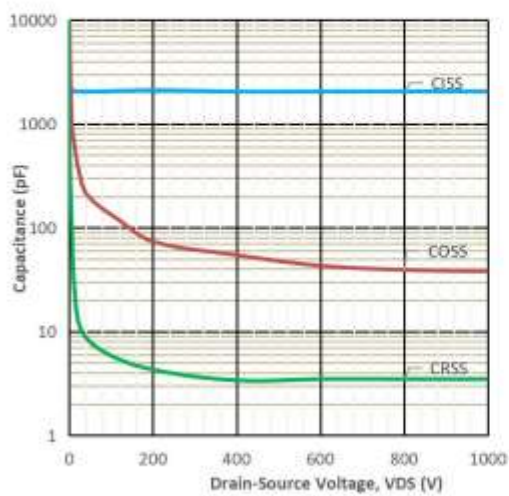
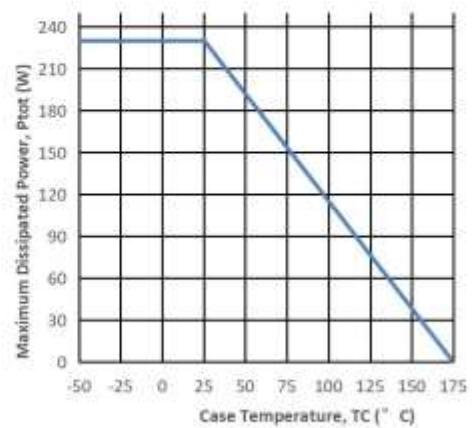


Fig12. Max Power Dissipation Derating Vs Tc



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Fig13. Switching Energy vs. Drain Current

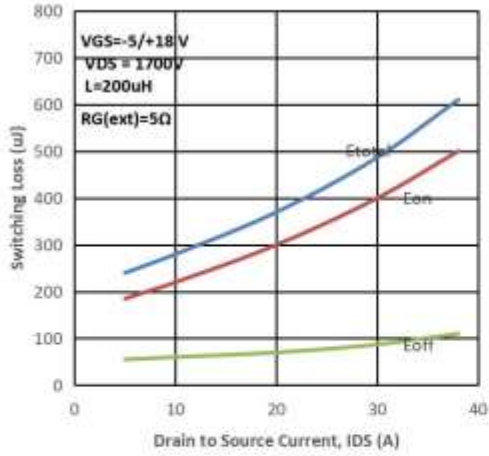


Fig14. Switching Energy vs. $R_{G(ext)}$

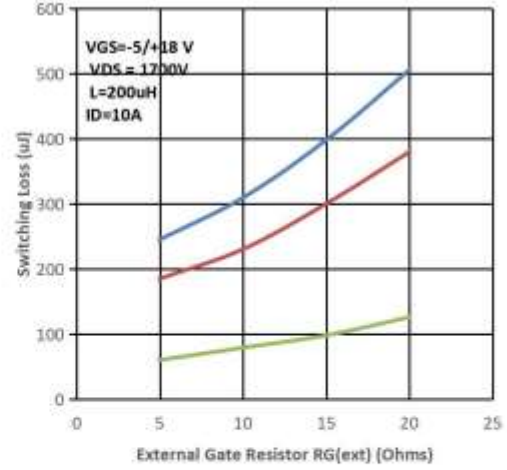


Fig15. Switching Energy vs. Temperature

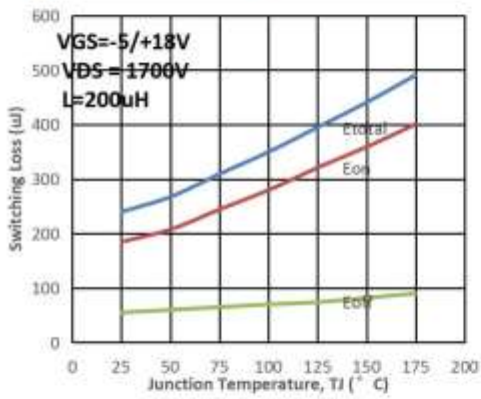


Fig16. Switching Times vs. $R_{G(ext)}$

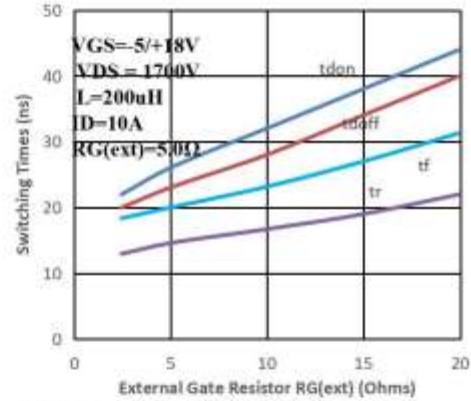


Fig17. Transient Thermal Impedance

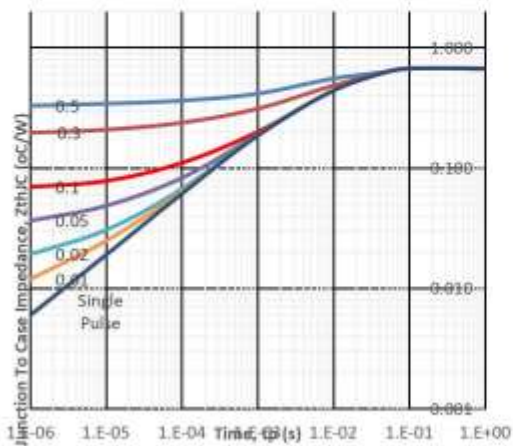
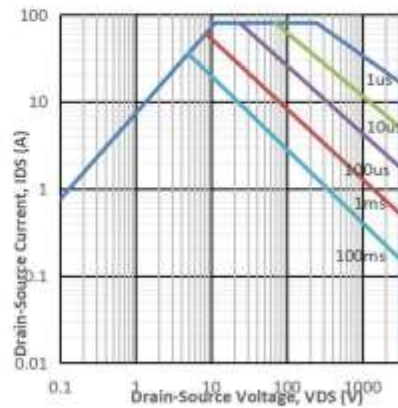
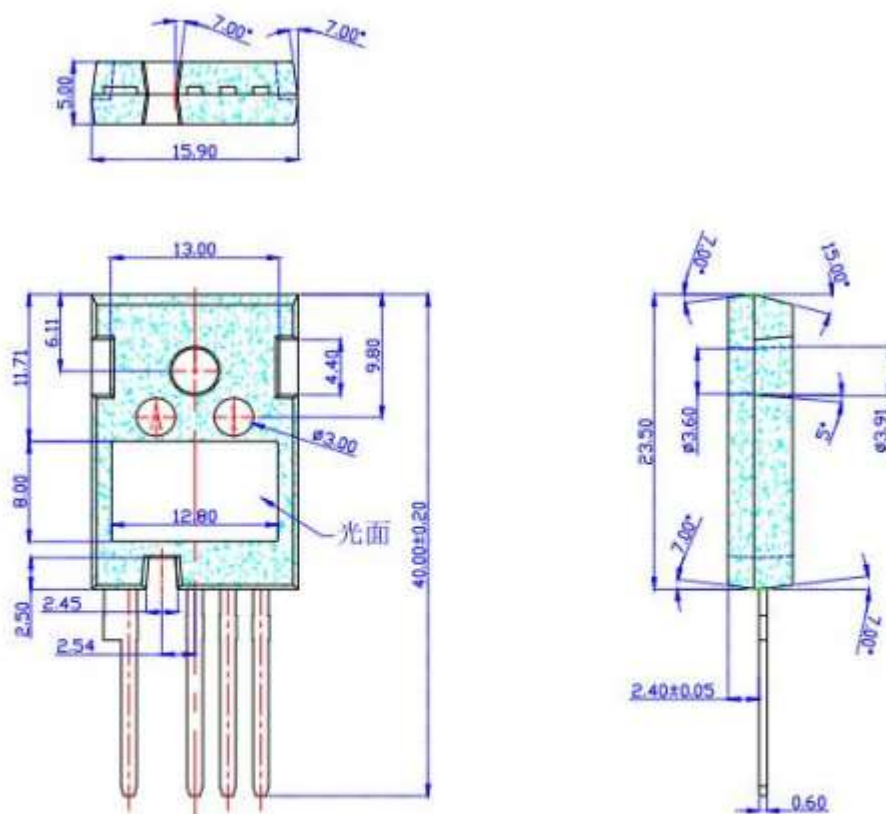


Fig18. Safe Operating Area



Package Drawing (Unit: mm)



Revision version	Description	Date
1	Initial	05.2024