



SDR & FPGA
Solution Provider

Puzhi Product Guide

ARM/FPGA Solution Provider



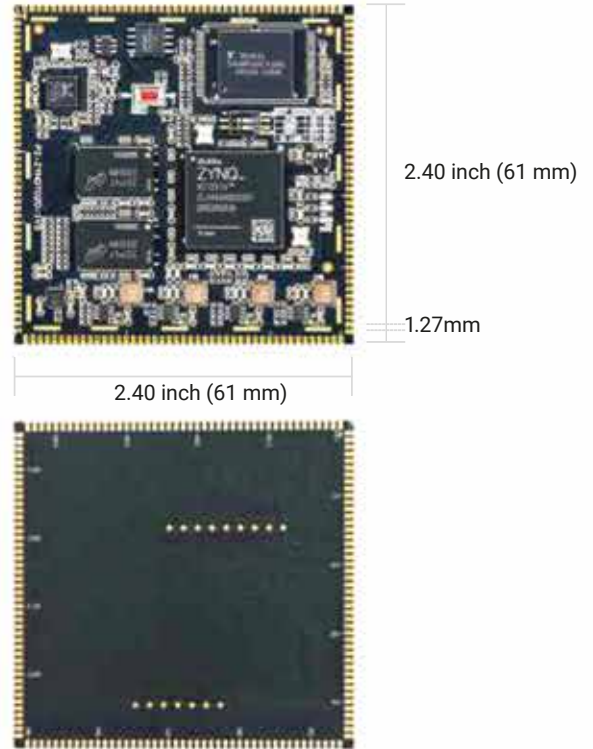
МАКРО
ГРУПП

www.macrogroupp.ru

Официальный дистрибьютор

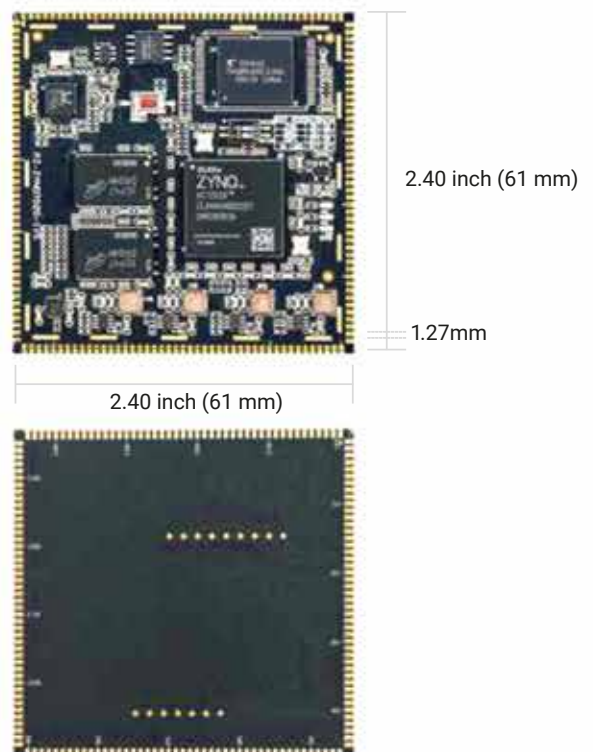
PZ7010-SOM

SPECIFICATION	
FPGA Chip	XC7Z010-2CLG400I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 667Mhz
Logic Cells	28K
Look up Tables	17600
Flip-Flops	35200
DSP Slice	80
BLOCK RAM	2.1Mb
DDR3/DD3L	512MB/ 16bit
QSPI FLASH	128Mb
EMMC	4GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	2
Number of IO	24 IOs on the PS Side, 94 IOs on the PL Side
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	2.40 inch x 2.40 inch (61mm x 61mm)
Voltage/ Current	5V/1A



PZ7020-SOM

SPECIFICATION	
FPGA Chip	XC7Z020-2CLG400I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 766Mhz
Logic Cells	85K
Look up Tables	53200
Flip-Flops	106400
DSP Slice	220
BLOCK RAM	4.9Mb
DDR3/DD3L	1GB/ 32bit
QSPI FLASH	128Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	2
Number of IO	24 IOs on the PS Side, 118 IOs on the PL Side
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	2.40 inch x 2.40 inch (61mm x 61mm)
Voltage/ Current	5V/1A



PZ7010S-SOM

SPECIFICATION	
FPGA Chip	XC7Z010-2CLG400I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 667Mhz
Logic Cells	28K
Look up Tables	17600
Flip-Flops	35200
DSP Slice	80
BLOCK RAM	2.1Mb
DDR3/DD3L	512MB/ 16bit
QSPI FLASH	256Mb
Gigabit Ethernet Chip	1
User LED	2
Number of IO	30 IOs on PS Side (24 fixed 1.8V, 6 fixed 3.3V) 92 IOs on PL Side (1.8/2.5/3.3V adjustable, default 3.3V)
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	2.36 inch x 1.77 inch (60mm x 45mm)
Voltage/ Current	5V/1A



1.77 inch (45 mm)

2.36 inch (60 mm)



PZ7020S-SOM

SPECIFICATION	
FPGA Chip	XC7Z020-2CLG400I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 766Mhz
Logic Cells	85K
Look up Tables	53200
Flip-Flops	106400
DSP Slice	220
BLOCK RAM	4.9Mb
DDR3/DD3L	1GB/ 32bit
QSPI FLASH	256Mb
Gigabit Ethernet Chip	1
User LED	2
PS Side MIO	30 IOs on PS Side (24 fixed 1.8V, 6 fixed 3.3V)
PL Side IO	116 IOs on PL Side (1.8/2.5/3.3V adjustable, default 3.3V)
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	2.36 inch x 1.77 inch (60mm x 45mm)
Voltage/ Current	5V/1A



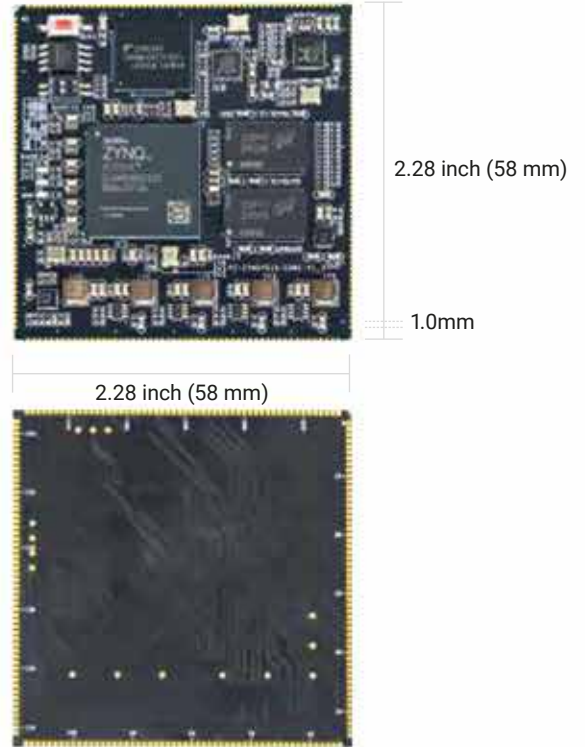
1.77 inch (45 mm)

2.36 inch (60 mm)



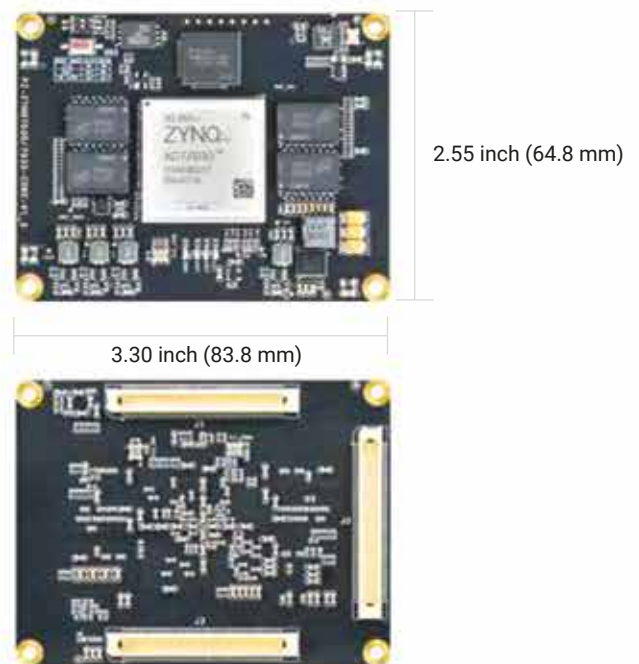
PZ7015-SOM

SPECIFICATION	
FPGA Chip	XC7Z015-2CLG485I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 766Mhz
Logic Cells	74K
DSP slice	160 (18 x 25MACCs)
DDR3/DD3L	1GB/ / 32bit
QSPI FLASH	128Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
USB2.0	1-way, Master / Slave Interface Compatible
User LED	2
Number of IO	12 IOs on PS, 140 IOs on PL (1.8/2.5/3.3V adjustable)
Number of GTX Ports	1 BANK, 4 Pairs of TX/RX Each
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	2.28 inch x 2.28 inch (58mm x 58mm)
Voltage/ Current	5V/1A



PZ7030-SOM-676

SPECIFICATION	
FPGA Chip	XC7Z030-2FFG676I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz
Logic Cells	125K
Look up Tables	78600
Flip-Flops	157200
DSP slice	400
BLOCK RAM	9.3Mb
DDR3/DD3L	1GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	3
Number of IO	24 IOs on PS Side, 164 IOs on PL (48 IOs fixed 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Number of GTX	1 BANK, 4 pairs of TX/RX each
Voltage/ Current	5V/3A
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



PZ7035-SOM-676

SPECIFICATION	
FPGA Chip	XC7Z035-2FFG676I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz
Logic Cells	275K
Look up Tables	171900
Flip-Flops	343800
DSP slice	900
BLOCK RAM	17.6Mb
DDR3/DD3L	1GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	3
Number of IO	24 IOs on PS Side, 164 IOs on PL (48 IOs fixed 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Number of GTX	2 BANK, 8 Pairs of TX/RX Each
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ7035-SOM-900

SPECIFICATION	
FPGA Chip	XC7Z035-2FFG900I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz
Logic Cells	275K
Look up Tables	171900
Flip-Flops	343800
DSP slice	900
BLOCK RAM	17.6Mb
DDR3/DD3L	1GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	5
Number of IO	24 IOs on PS Side, 240 IOs on PL (48 IOs fixed 1.8V; 192 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Number of GTX	4 BANK, 16 Pairs of TX/RX Each
Voltage/ Current	12V/3A
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ7045-SOM-900

SPECIFICATION	
FPGA Chip	XC7Z045-2FFG900I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz
Logic Cells	350K
Look up Tables	218600
Flip-Flops	437200
DSP slice	900
BLOCK RAM	19.2Mb
DDR3/DD3L	1GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	5
Number of IO	24 IOs on PS Side, 240 IOs on PL (48 IOs fixed 1.8V; 192 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Number of GTX	4 BANK, 16 Pairs of TX/RX Each
Voltage/ Current	12V/3A
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ7100-SOM-900

SPECIFICATION	
FPGA Chip	XC7Z100-2FFG900I
ARM Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz
Logic Cells	444K
LUTS/Flip-Flops	277400
DSP slice	2020
BLOCK RAM	26.5Mb
DDR3/DD3L	1GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
User LED	5
Number of IO	24 IOs on PS Side, 240 IOs on PL (48 IOs fixed 1.8V; 192 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Number of GTX	4 BANK, 16 Pairs of TX/RX Each
Voltage/ Current	12V/3A
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



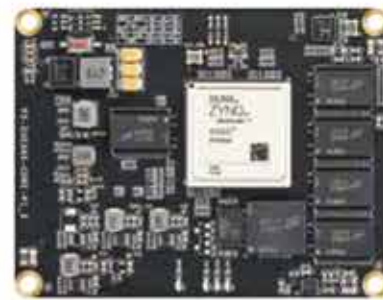
2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ-ZU2CG-SOM

SPECIFICATION	
FPGA Chip	XCZU2CG-2SFVC784I
ARM Processor Core	2 x Cortex-A53 1.2Ghz, 2 x Cortex-R5 500Mhz
Logic Cells	103K
CLB Flip-Flops	94K
CLB LUTs	47K
BLOCK ROM	5.3Mb
DSP Slices	240
DDR4	4GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
User LED	3
Reset KEY	1
PS Side GTR	4 Pairs of TX/RX
Number of I O	38 IOs on PS Side, 192 IOs on PL (96 IOs 1.2/1.8V Adjustable Default 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
Connector Height	3mm



2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ-ZU3EG-SOM

SPECIFICATION	
FPGA Chip	XCZU3EG-2SFVC784I
ARM Processor Core	4 x Cortex-A53 1.2Ghz, 2 x Cortex-R5 500Mhz
Logic Cells	154K
CLB Flip-Flops	141K
CLB LUTs	71K
BLOCK ROM	7.6Mb
DSP Slices	360
DDR4	4GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
User LED / Reset KEY	3/ 1
PS Side GTR	4 Pairs of TX/RX
Number of I O	38 IOs on PS Side, 192 IOs on PL (96 IOs 1.2/1.8V Adjustable Default 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
Connector Height	3mm



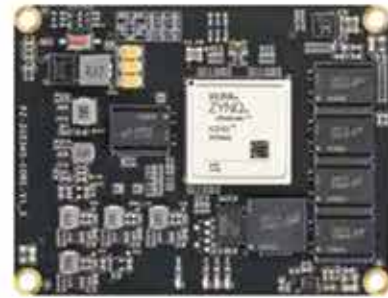
2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ-ZU4EV-SOM

SPECIFICATION	
FPGA Chip	XCZU4EV-2SFVC784I
ARM Processor Core	4 x Cortex-A53 1.3Ghz, 2 x Cortex-R5 533Mhz
Logic Cells	192K
CLB Flip-Flops	176K
CLB LUTs	88K
BLOCK ROM	4.5Mb
UltraRAM	13.5Mb
DSP Slices	728
Video Codec	H264/H265
DDR4	4GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
User LED / Reset KEY	3/1
PS GTR / PL GTH	4 Paris of TX/RX / 4 Paris of TX/RX
Number of I O	38 IOs on PS Side, 192 IOs on PL (96 IOs 1.2/1.8V Adjustable Default 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



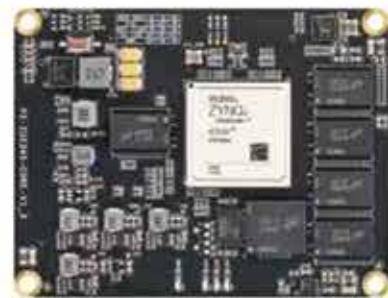
2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ-ZU5EV-SOM

SPECIFICATION	
FPGA Chip	XCZU5EV-2SFVC784I
ARM Processor Core	4 x Cortex-A53 1.3Ghz, 2 x Cortex-R5 533Mhz
Logic Cells	256K
CLB Flip-Flops	234K
CLB LUTs	117K
BLOCK ROM/ UltraRAM	5.1Mb/18Mb
DSP Slices	1248
Video Codec	H264/H265
DDR4	4GB on PS Side, 1GB on PL Side
QSPI FLASH	256Mb
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
User LED / Reset KEY	3/ 1
PS GTR / PL GTH	4 Paris of TX/RX / 4 Paris of TX/RX
Number of I O	38 IOs on PS Side, 192 IOs on PL (96 IOs 1.2/1.8V Adjustable Default 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



PZ-ZU7EG-SOM

SPECIFICATION	
FPGA Chip	XCZU7EG-2FFVC1156I
ARM Processor Core	ARM: 4 x Cortex-A53 (1.3Ghz) RPU: 2 x Cortex-R5 (533Mhz) GPU: Mali-400MP2 (667Mhz)
Logic Cells, LUTs	504K, 230K
Flip-Flops	461K
BLOCK ROM/ UltraRAM	11Mb / 27Mb
DSP Slices	1728
DDR4/DDR4L	PS 4GB 2400Mhz*64bit / PL 4GB 2400Mhz*64bit
QSPI FLASH	256Mb QSP0 + 256Mb QSPI1
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, On-board DIP Switch Selection
Gigabit Ethernet Chip	1 (PS Side)
User LED	3 LEDs (PL Side, High ON Low OFF)
Number of IO	MIO: 38 IOs (Fixed 1.8V) HP: 144 IOs (1.2/1.8V Adjustable, Default 1.8V) HD: 48 IOs (1.8/2.5/3.3V Adjustable, Default 3.3V)
PS GTR / PL GTH	4 Paris of TX/RX / 20 Paris of TX/RX
Voltage/ Current	8-12V/5A (Recommended Voltage 8V)
Form Factors	3.35 inch x 2.95 inch (85 mm x 75mm)
Connector Height	4mm



2.95 inch (75 mm)

3.35 inch (85 mm)



PZ-ZU7EV-SOM

SPECIFICATION	
FPGA Chip	XCZU7EV-2FFVC1156I
ARM Processor Core	ARM: 4 x Cortex-A53 (1.3Ghz) RPU: 2 x Cortex-R5 (533Mhz) GPU: Mali-400MP2 (667Mhz)
Logic Cells, LUTs	504K, 230K
Flip-Flops	461K
DDR4/DDR4L	PS 4GB 2400Mhz * 64bit / PL 4GB 2400Mhz*64bit
QSPI FLASH	256Mb QSP0 + 256Mb QSPI1
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, On-board DIP Switch Selection
Gigabit Ethernet Chip	1 (PS Side)
User LED	3 LEDs (PL Side, High ON Low OFF)
Number of IO	MIO: 38 IOs (Fixed 1.8V) HP: 144 IOs (1.2/1.8V Adjustable, Default 1.8V) HD: 48 IOs (1.8/2.5/3.3V Adjustable, Default 3.3V)
PS GTR / PL GTH	4 Paris of TX/RX / 20 Paris of TX/RX
Voltage/ Current	8-12V/5A (Recommended Voltage 8V)
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	3.35 inch x 2.95 inch (85 mm x 75mm)
Connector Height	4mm



2.95 inch (75 mm)

3.35 inch (85 mm)



PZ-ZU11EG-SOM

SPECIFICATION	
FPGA Chip	XCZU11EG-2FFVC1156I
ARM Processor Core	ARM: 4 x Cortex-A53 (1.3Ghz) RPU: 2 x Cortex-R5 (533Mhz) GPU: Mali-400MP2 (667Mhz)
Logic Cells, LUTs	653K, 299K
Flip-Flops	597K
DDR4/DDR4L	PS 4GB 2400Mhz * 64bit / PL 4GB 2400Mhz*64bit
QSPI FLASH	256Mb QSP0 + 256Mb QSPI1
EMMC	8GB
Start-up Mode	JTAG/QSPI/SD/EMMC, On-board DIP Switch Selection
Gigabit Ethernet Chip	1 (PS Side)
User LED	3 LEDs (PL Side, High ON Low OFF)
Number of IO	MIO: 38 IOs (Fixed 1.8V) HP:144 IOs (1.2/1.8V Adjustable, Default 1.8V) HD: 48 IOs (1.8/2.5/3.3V Adjustable, Default 3.3V)
PS GTR / PL GTH	4 Paris of TX/RX / 20 Paris of TX/RX
Voltage/ Current	8-12V/5A (Recommended Voltage 8V)
Chip Level	Industrial Grade (-40°C ~ +85°C)
Form Factors	3.35 inch x 2.95 inch (85 mm x 75mm)
Connector Height	4mm



2.95 inch (75 mm)

3.35 inch (85 mm)



PZ-ZU9EG-SOM

SPECIFICATION	
FPGA Chip	XCZU9EG-2FFVB1156I
ARM Processor Core	4 x Cortex-A53 1.333Ghz, 2 x Cortex-R5F 533Mhz
GPU	Mali™ -400 MP2, 667Mhz
Logic Cells	600K
Look up Tables	274K
Flip-Flops	548K
Block RAM	32.1Mb
DSP Slices	2520
DDR4	4GB on PS Side/2GB on PL Side
EMMC	8GB
QSPI FLASH	256Mb QSP0 + 256Mb QSPI1
Start-up Mode	JTAG/QSPI/SD/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
Number of IO	38 IOs on PS Side (Fixed 1.8V), 192 IOs on PL (96 IOs, 1.2/1.8V Adjustable, Default 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
GTH	6 BANK, Total 24 Pairs of TX/ RX, Up to 16.25Gb/s
Voltage/ Current	8-12V / 4A
Form Factors	3.35 inch x 2.95 inch (85 mm x 75mm)



2.95 inch (75 mm)

3.35 inch (85 mm)



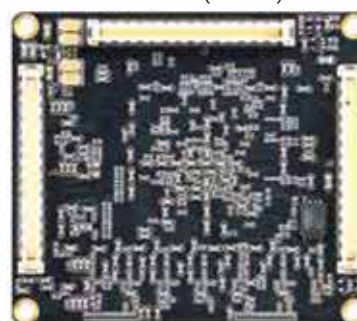
PZ-ZU15EG-SOM

SPECIFICATION	
FPGA Chip	XCZU15EG-2FFVB1156I
ARM Processor Core	4 x Cortex-A53 1.333Ghz, 2 x Cortex-R5F 533Mhz
GPU	Mali™ -400 MP2, 667Mhz
Logic Cells	747K
Look up Tables	341K
Flip-Flops	682K
Max.DistributedRAM	11.3Mb
Block RAM	26.2Mb
Ultra RAM (Mb) / DSP Slices	31.5Mb/ 3528
DDR4	4GB on PS Side/2GB on PL Side
EMMC	8GB
QSPI FLASH	256Mb QSPI0 + 256Mb QSPI1
Start-up Mode	JTAG/QSPI/SD/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
Number of IO	38 IOs on PS Side (Fixed 1.8V), 192 IOs on PL (96 IOs, 1.2/1.8VAdjustable, Default 1.8V; 96 IOs 1.8/2.5/3.3V Adjustable, Default 3.3V)
GTH	6 BANK, Total 24 Pairs of TX/ RX, Up to 16.25Gb/s
Voltage/ Current	8-12V/4A
Form Factors	3.35 inch x 2.95 inch (85 mm x 75mm)



2.95 inch (75 mm)

3.35 inch (85 mm)



PZ-ZU19EG-SOM

SPECIFICATION	
FPGA Chip	XCZU19EG-2FFVC1760I
ARM Processor Core	4 x Cortex-A53 1.333Ghz, 2 x Cortex-R5F 533Mhz
GPU	Mali™-400 MP2, 667Mhz
Logic Cells	1143K
Look up Tables	1045K
Flip-Flops	523K
Max.DistributedRAM	9.8Mb
Block RAM	34.6Mb
Ultra RAM (Mb)/DSP Slices	36.0Mb/1968
DDR4/EMMC	8GB on PS Side (with ECC)/8GB on PL Side
QSPI FLAS	512Mb QSPI0 + 512Mb QSPI1
Start-up Mode	JTAG/SD/QSPI/EMMC, Default SD Startup
Gigabit Ethernet Chip	1
Number of IO	38 IO on PS Side (Fixed 1.8V) 336 IOs on PL Side (240 IOs 1.2/1.8V Adjustable Default 1.8V, 96 IOs, 1.8/2.5/3.3V Adjustable Default 3.3V)
GTH	GTH: 32 Paris (16.3G)/ GTY: 16 Pairs (32.75G)
Voltage/ Current	8-12V / 5A
Form Factors	3.54 inch x 2.95 inch (90 mm x 75mm)



3.54 inch (90 mm)

2.95 inch (75 mm)



PZ-ZU47DR-SOM

SPECIFICATION	
FPGA Chip	XCZU47DR-2FFVE1156I
Gigabit Ethernet	4 x Cortex-A53 1.333Ghz, 2 x Cortex-R5F 533Mhz
14bit ADC/14bit DAC	8 Ways of 5Gsps ADC, 8 Ways of 9.85Gsps DAC
Logic Cells	930K
Look up Tables	425K
DSP Slices	4272
Max.DistributedRAM	13Mb
Block RAM	38Mb
Ultra RAM (Mb)	22.5Mb
DDR4	8GB on PS Side/4GB on PL Side
EMMC	32GB
QSPI FLASH	256Mb QSP0 + 256Mb QSP11
Start-up Mode	JTAG/QSPI/SD/EMMC, On-board DIP Switch Selection
Gigabit Ethernet Chip	1
Number of IO	38 IOs on PS Side (Fixed 1.8V) 44 IOs on PL Side(1.8/2.5/3.3V Adjustable, Default 3.3V)
GTY	8 Pairs of TX/RX (375G)
Voltage/ Current	8-12V / 5A
Form Factors	3.54 inch x 2.95 inch (90 mm x 75mm)



3.54 inch (90 mm)

2.95 inch (75 mm)



PZ-A735T-SOM

SPECIFICATION	
FPGA Chip	XC7A35T-2FQG484I
Logic Cells	33280
Slices	5200
BLOCK ROM	1800
DSP Slices	90
DDR3/DD3L	1GB/32bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	2
Reset KEY	1
GTP	4 Paris of TX/RX
IO (Level Adjustable)	138 IOs
Voltage/ Current	5V/2A
Form Factors	2.36 inch x 1.77 inch (60 mm x 45mm)
Connector Height	3mm



1.77 inch (45 mm)

2.36 inch (60 mm)



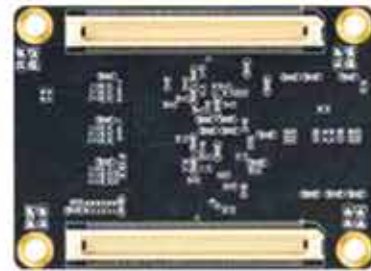
PZ-A775T-SOM

SPECIFICATION	
FPGA Chip	XC7A75T-2FGG484I
Logic Cells	75520
Slices	11800
BLOCK ROM	3780
DSP Slices	180
DDR3/DD3L	1GB/32bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	2
Reset KEY	1
GTP	4 Pairs of TX/RX
IO (Level Adjustable)	172 IOs
Voltage/ Current	5V/2A
Form Factors	2.36 inch x 1.77 inch (60 mm x 45mm)
Connector Height	3mm



1.77 inch (45 mm)

2.36 inch (60 mm)



PZ-A7100T-SOM

SPECIFICATION	
FPGA Chip	XC7A100T-2FGG484I
Logic Cells	101440
Slices	15850
BLOCK ROM	4846
DSP Slices	240
DDR3/DD3L	1GB/32bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	2
Reset KEY	1
GTP	4 Pairs of TX/RX
IO (Level Adjustable)	172 IOs
Voltage/ Current	5V/2A
Form Factors	2.36 inch x 1.77 inch (60 mm x 45mm)
Connector Height	3mm



1.77 inch (45 mm)

2.36 inch (60 mm)



PZ-A7200T-SOM

SPECIFICATION	
FPGA Chip	XC7A200T-2FBG484I
Logic Cells	215360
Slices	33650
BLOCK ROM	13140
DSP Slices	740
DDR3/DD3L	1GB/32bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	2
Reset KEY	1
GTP	4 Pairs of TX/RX
IO (Level Adjustable)	172 IOs
Voltage/ Current	5V/2A
Form Factors	2.36 inch x 1.77 inch (60 mm x 45mm)
Connector Height	3mm



1.77 inch (45 mm)

2.36 inch (60 mm)



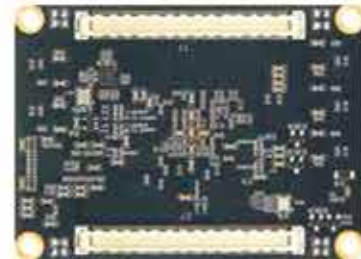
PZ-AU15P-SOM

SPECIFICATION	
FPGA Chip	XCAU15P-2FFVB676I
Logic Cells	170k
CLB Flip-Flops	156k
CLB LUTS	78k
BLOCK ROM	5.1Mb
DSP Slices	576
DDR4	1GB/16bit Width
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
User LED	3
GPH	12 Pairs of TX/RX
IO (Level Adjustable)	Total: 168 HP: 96 IOs (1.2/1.8V Adjustable, Default 1.8V) HD: 72 IOs (1.8/2.5/3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/2A
Form Factors	2.95 inch x 2.17 inch (75 mm x 55 mm)
Connector Height	4mm



55mm

2.95 inch (75 mm)



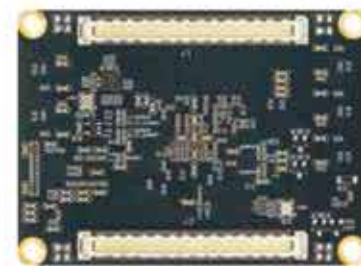
PZ-AU20P-SOM

SPECIFICATION	
FPGA Chip	XCAU20P-2FFVB676I
Logic Cells	238k
CLB Flip-Flops	218k
CLB LUTS	109k
BLOCK ROM	7.0Mb
DSP Slices	900
DDR4	1GB/16bit Width
QSPI FLASH	256Mb, to Store Startup Files and User Files
Start-up Mode	JTAG/QSPI, Default QSPI Startup
User LED	3
GPH	12 Pairs of TX/RX
Number of IO	Total: 168 HP: 96 IOs (1.2/1.8V Adjustable, Default 1.8V) HD: 72 IOs (1.8/2.5/3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/2A
Form Factors	2.95 inch x 2.17 inch(75 mm x 55 mm)
Connector Height	4mm



2.17 inch (55 mm)

2.95 inch (75 mm)



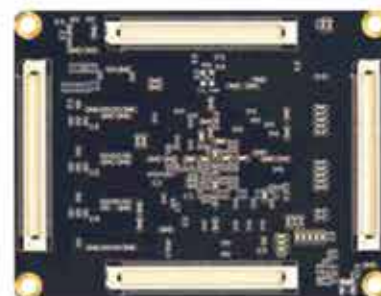
PZ-K7325T-SOM

SPECIFICATION	
FPGA Chip	XC7K325T-2FFG900I
Logic Cells	326080
Slices	50950
BLOCK ROM	16020
DSP Slices	840
DDR3/DD3L	2GB/64bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	5
Reset KEY	1
GT	16 Pairs of TX/RX
Number of IO	288 IOs, 1.8/2.5/3.3V Adjustable, Default 3.3V
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
Connector Height	3mm



2.55 inch (64.8 mm)

3.30 inch (83.8 mm)



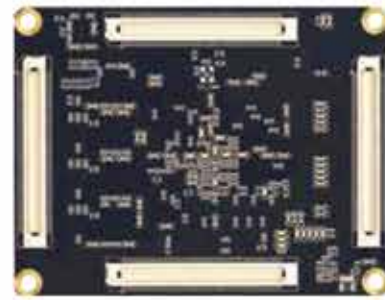
PZ-K7410T-SOM

SPECIFICATION	
FPGA Chip	XC7K410T-2FFG900I
Logic Cells	406720
Slices	63550
BLOCK ROM	28620
DSP Slices	1540
DDR3/DD3L	2GB/64bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	5
Reset KEY	1
GTX	16 Pairs of TX/RX
Number of IO	288 IOs, 1.8/2.5/3.3V Adjustable, Default 3.3V
Voltage/ Current	5V/3A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
Connector Height	3mm



64.8mm (2.55inch)

83.8 mm (3.30inch)



PZ- KU040-SOM

SPECIFICATION	
FPGA Chip	XCKU040-2FFVA1156I
Logic Cells	530250
LUTs	242400
Flip-Flops	484800
BLOCK ROM	21.1Mb
DSP Slices	1920
DDR4	4GB/64bit
QSPI FLASH	256Mb QSPI0 + 256Mb QSPI1
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	5
Reset KEY	1
GTH	20 Pairs of TX/RX
Number of IO	Total 296 IOs HR: 8 Single-ended (Default 3.3V) HR: 24 Differential Pairs/48 Single-ended (1.8/2.5/3.3V adjustable) HP: 120 Differential Pairs/240 Single-ended (1.2/1.8V adjustable)
Voltage/ Current	5V/6A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



64.8mm (2.55 inch)

83.8 mm (3.30inch)



PZ-KU060-SOM

SPECIFICATION	
FPGA Chip	XCKU060-2FFVA1156I
Logic Cells	725550
LUTs	331680
Flip-Flops	663360
BLOCK ROM	38Mb
DSP Slices	2760
DDR4	4GB/64bit
QSPI FLASH	256Mb QSPI0 + 256Mb QSPI1
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	5
Reset KEY	1
GTH	20 Pairs of TX/RX Total: 296 IOs
Number of IO	HR: 8 Single-ended (Default 3.3V) HR: 24 Differential Pairs/48 Single-ended (1.8/2.5/3.3V adjustable) HP: 120 Differential Pairs/240 Single-ended (1.2/1.8V adjustable)
Voltage/ Current	5V/6A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)



64.8mm (2.55inch)

83.8 mm (3.30inch)



PZ-KU095-SOM

SPECIFICATION	
FPGA Chip	XCKU095-2FFVA1156I
Logic Cells	1176000
LUTs	537600
Flip-Flops	1075200
BLOCK ROM	59.1Mb
DSP Slices	768
DDR4	4GB/64bit
QSPI FLASH	256Mb QSPI0 + 256Mb QSPI1
Start-up Mode	JTAG/QSPI, Default QSPI Startup
Gigabit Ethernet Chip	1
User LED	5
Reset KEY	1
GTH	20 Pairs of TX/RX Total : 296 IOs
Number of IO	HR: 8 Single-ended (Default 3.3V) HR: 24 Differential Pairs/48 Single-ended (1.8/2.5/3.3V adjustable) HP: 120 Differential Pairs/240 Single-ended (1.2/1.8V adjustable)
Voltage/ Current	5V/6A
Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
Connector Height	4mm



64.8mm (2.55inch)

83.8 mm (3.30inch)



PZ-KU3P-SOM

SPECIFICATION	
FPGA Chip	XCKU3P-2FFVB676I
Logic Cells	356k
CLB Flip-Flops	325k
CLB LUTS	163k
BLOCK ROM	12.7Mb
DSP Slices	1368
DDR4	2GB/32bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
User LED	3
Reset KEY	1
GTY	16 Pairs of TX/RX
Number of IO	Total: 168 IOs HP: 96 IOs (1.2V/ 1.8V Adjustable, Default 1.8V) HD: 72 IOs (1.8V/ 2.5V/ 3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/3A
Form Factors	2.95 inch x 2.17inch (75 mm x 55 mm)
Connector Height	5mm



2.17 inch (55 mm)

2.95 inch (75 mm)



PZ-KU5P-SOM

SPECIFICATION	
FPGA Chip	XCKU5P-2FFVB676I
Logic Cells	475k
CLB Flip-Flops	434k
CLB LUTS	217k
BLOCK ROM	16.9Mb
DSP Slices	1824
DDR4	2GB/32bit
QSPI FLASH	256Mb
Start-up Mode	JTAG/QSPI, Default QSPI Startup
User LED	3
Reset KEY	1
GTY	16 Pairs of TX/RX
Number of IO	Total: 168 IOs HP: 96 IOs (1.2/ 1.8V Adjustable, Default 1.8V) HD: 72 IOs (1.2/ 1.8 / 3.3V Adjustable, Default 3.3V)
Voltage/ Current	5V/3A
Form Factors	2.95 inch x 2.17inch (75 mm x 55 mm)
Connector Height	5mm



2.17 inch (55 mm)

2.95 inch (75 mm)

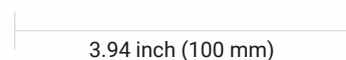


PZ-V7690T-SOM

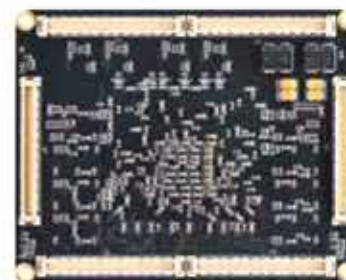
SPECIFICATION	
FPGA Chip	XC7V690T-2FFG17611
Slices	108300
Logic Cells	693120
CLB Flip-Flops	866400
Distributed RAM	10888Kb
BLOCK RAM	1470
TotalBlockRAM	52920Kb
DSP Slices	3600
DDR3	2GB/64bit + 2GB/64bit
Start-up Mode	JTAG/QSPI/BPI
User LED	2
GTH	36 Pairs of TX/RX
Number of IO	Total: 528 IOs HP: 264 Differential Pairs/528 Single-end (1.2/ 1.8V Adjustable, Default 1.8V)
Voltage/ Current	8-12V/5A
Form Factors	3.94 inch x 3.15 inch(100 mm x 80 mm)
Connector Height	4mm



3.15 inch (80 mm)



3.94 inch (100 mm)



PZ-VU9P-SOM

SPECIFICATION	
FPGA Chip	XCVU9P-2FLGB2104I
Logic Cells	2586k
CLB Flip-Flops	2364k
CLB LUTS	1182k
BLOCK ROM	75.9Mb
DSP Slices	6840
DDR4	Dual Group of DDR4 (8GB + 8GB)
Start-up Mode	JTAG/BPI/SPI, Default JTAG Startup
GTY	76 Pairs of TX/RX
Number of IO	360 IOs (1.2/1.8V Adjustable, Default 1.8V)
Voltage/ Current	12V/20A
Form Factors	3.94 inch x 3.94 inch (100 mm x 100 mm)
Connector Height	5mm



3.94 inch (100 mm)

3.94 inch (100 mm)



PZ-VU13P-SOM

SPECIFICATION	
FPGA Chip	XCVU13P-2FHGB2104I
Logic Cells	3780k
CLB Flip-Flops	3456k
CLB LUTS	1728k
BLOCK ROM	94.5Mb
DSP Slices	12288
DDR4	Dual Group of DDR4 (8GB + 8GB)
Start-up Mode	JTAG/BPI/SPI, Default JTAG Startup
GTY	76 Pairs of TX/RX
Number of IO	360 IOs (1.2/1.8V Adjustable, Default 1.8V)
Voltage/ Current	12V/20A
Form Factors	3.94 inch x 3.94 inch (100 mm x 100 mm)
Connector Height	5mm



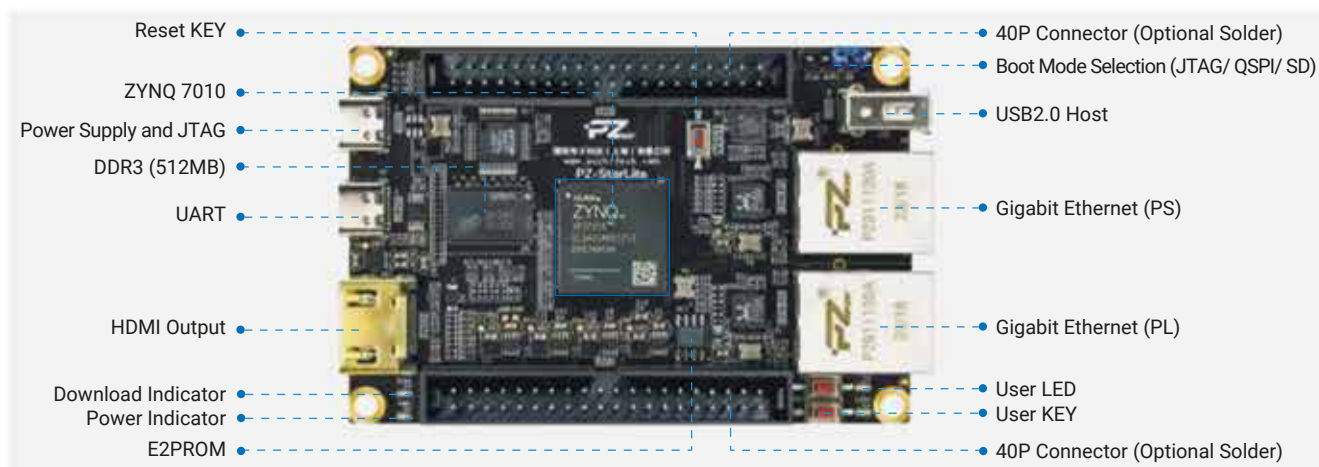
3.94 inch (100 mm)

3.94 inch (100 mm)



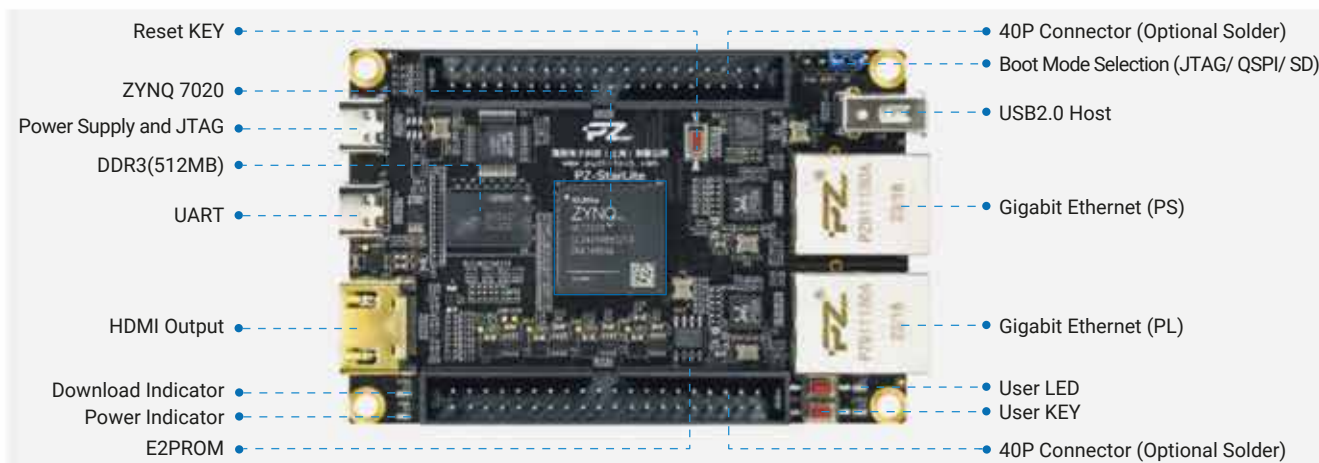
PZ7010- StarLite

SPECIFICATION			
FPGA Chip	XC7Z010-CLG400	USB2.0 Host	1
DDR3	512MB/ 16bit	Gigabit Ethernet	1 on PS Side, 1 on PL Side
QSPI FLASH	128Mb	HDMI	1 HDMI Output
E2PROM	64Kbit	LED / KEY	2 LEDs/ 2 KEYS
JTAG	1	2 x 40P Expansion Ports	64 Single-ended /32 Differential Pairs (PL Side)
UART	1	Form Factors	3.54 inch x 2.36 inch (90 mm x 60 mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



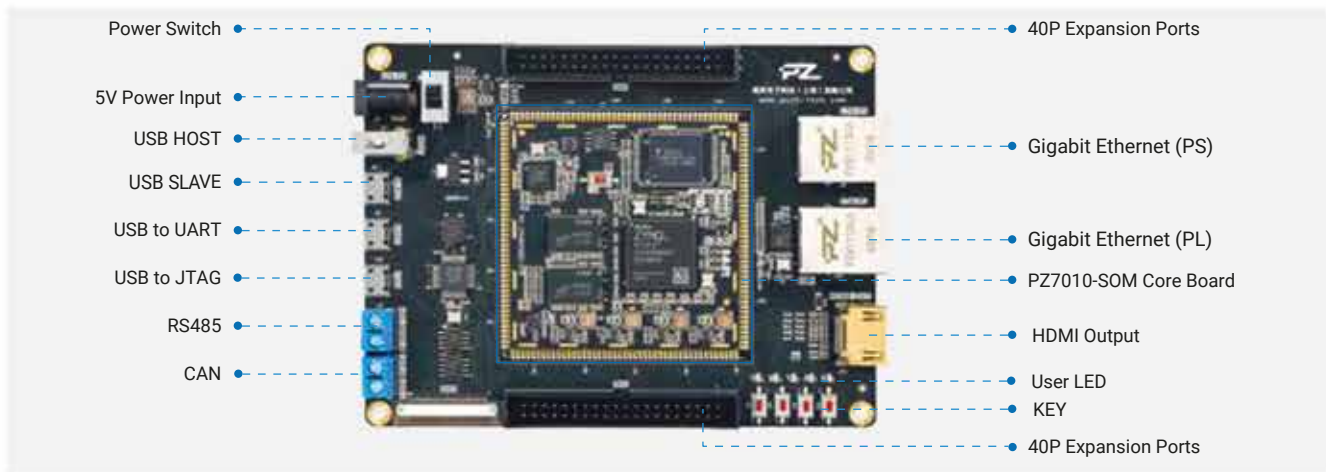
PZ7020- StarLite

SPECIFICATION			
FPGA Chip	XC7Z020-CLG400	USB2.0 Host	1
DDR3	512MB/ 16bit	Gigabit Ethernet	1 on PS Side, 1 on PL Side
QSPI FLASH	128Mb	HDMI	1 HDMI Output
E2PROM	64Kbit	LED/ KEY	2 LEDs/ 2 KEYS
JTAG	1	MIPI CSI	1 (2 Lane)
UART	1	2*40P Expansion Ports	64 Single-ended /32 Differential Pairs (PL Side)
SD Card Slot Interface	1	Form Factors	3.54 inch x 2.36 inch (90 mm x 60 mm)



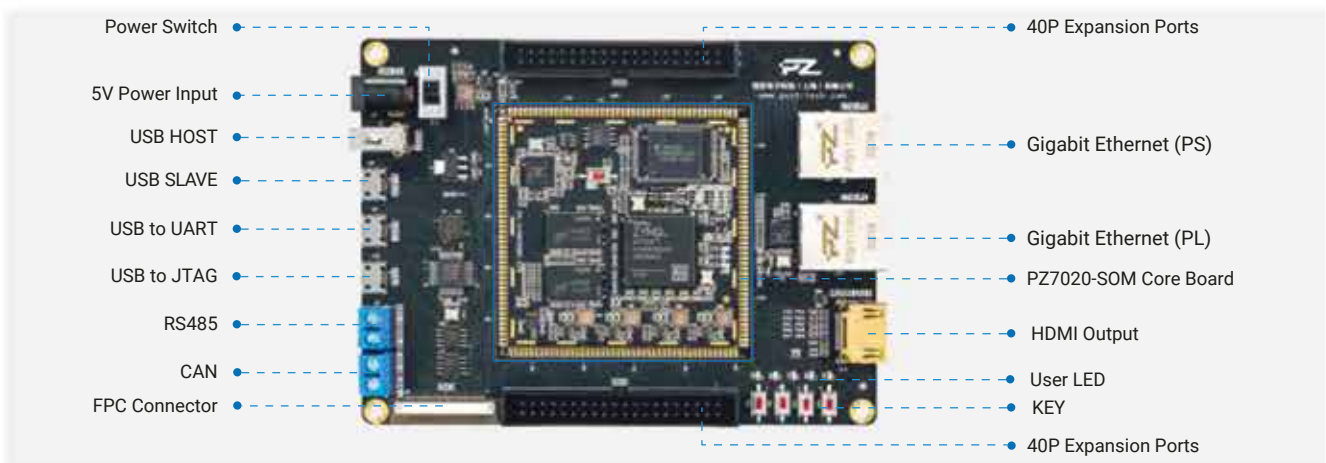
PZ7010-KFB

SPECIFICATION			
FPGA Chip	XC7Z010-2CLG400I	USB2.0 Host/ Slave	1/ 1
DDR3	512MB/ 16bit	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
EMMC	4GB	HDMI	1 HDMI Output
QSPI FLASH	128Mb	LED/ KEY	5 LEDs/ 4 KEYS
E2PROM	64Kbit	2 x 40P Expansion Ports	56 Single-ended Signals
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	SOM Form Factors	2.40 inch x 2.40 inch (61mm x 61mm)
SD Card Slot Interface	1	Board Form Factors	5.04 inch x 3.78 inch (128mm x 96mm)



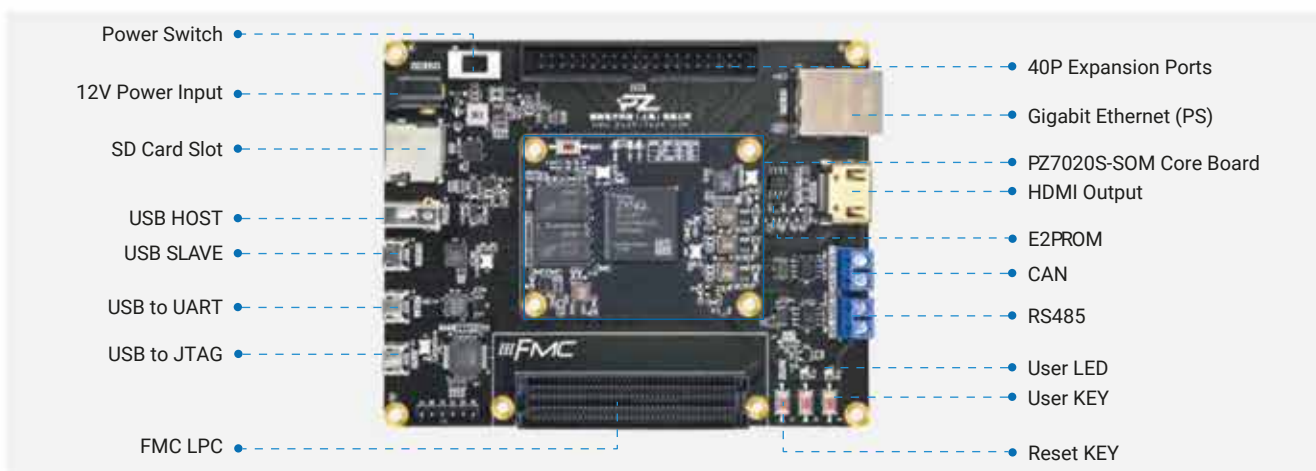
PZ7020-KFB

SPECIFICATION			
FPGA Chip	XC7Z020-2CLG400I	USB2.0 Host/ Slave	1/ 1
DDR3	1GB/ 32bit	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
EMMC	8GB	HDMI	1 HDMI Output
QSPI FLASH	128Mb	LED / KEY	5 LEDs/ 4 KEYS
E2PROM	64Kbit	2 x 40P Expansion Ports	56 Single-ended Signals
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	SOM Form Factors	2.40 inch x 2.40 inch (61mm x 61mm)
SD Card Slot Interface	1	Board Form Factors	5.04 inch x 3.78 inch (128mm x 96mm)



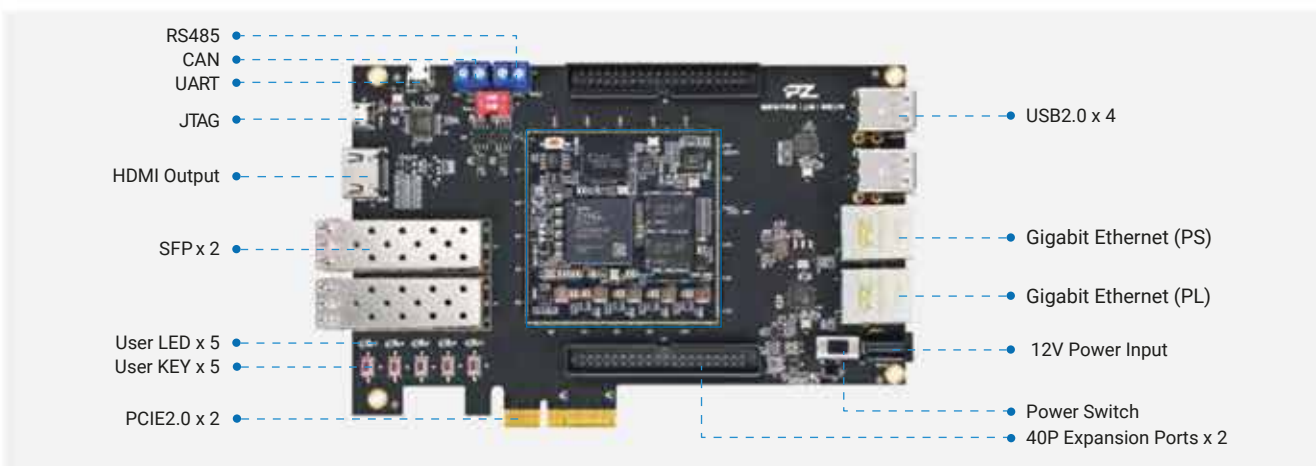
PZ7020S-FL-KFB

SPECIFICATION			
FPGA Chip	XC7Z020-2CLG400I	USB2.0 Host/ Slave	1/ 1
Logic cells	85K	Gigabit Ethernet	1 on PS Side
DDR3	1GB/ 32bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	LED/KEY	4 LEDs / 4 KEYS
E2PROM	64Kbit	40P Expansion Ports	32 Single-ended/ 16 Differential Pairs
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	FMC LPC	72 Single-ended/ 36 Differential Pairs
SD Card Slot Interface	1	Board Form Factors	4.72 inch x 3.74 inch (120mm x 95mm)



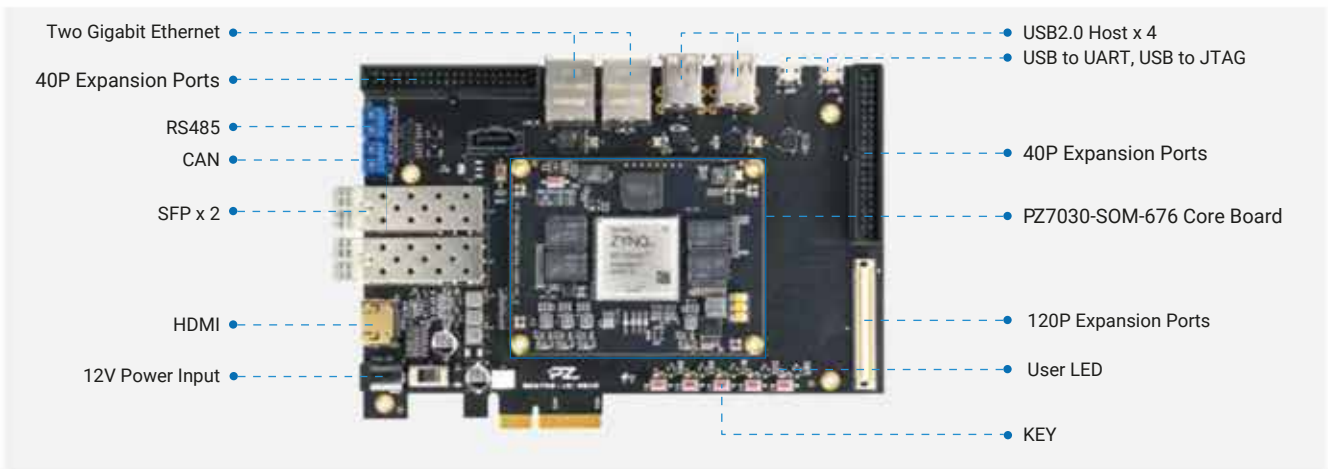
PZ7015-KFB

SPECIFICATION			
FPGA Chip	XC7Z015-2CLG485I	Gigabit Ethernet	1 on PS Side, 1 on PL Side
DDR3	1GB/ 16bit	HDMI	1 HDMI Output
EMMC	8GB	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	128Mb	SFP/PCIE2.0	SFP x 2/ PCIE2.0 x 2
JTAG	1	2 x 40P Expansion Ports	64 Single-ended/ 32 Differential Pairs
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	Voltage/ Current	12V/2A
SD Card Slot Interface	1	Board Form Factors	6.57 inch x 4.09 inch (167mm x 104mm)



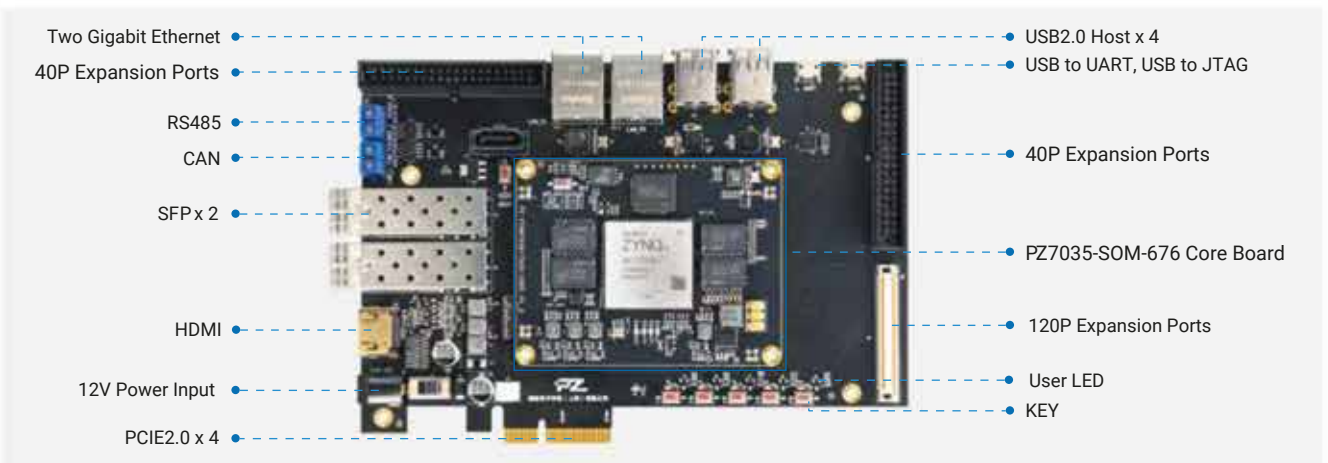
PZ7030-KFB-676 (PCIE not Available)

SPECIFICATION			
FPGA Chip	XC7Z030-2FFG6761	HDMI Output	1
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
QSPI FLASH	256Mb	LED/KEY	5 LEDs / 5 KEYS
EMMC	8GB	SFP / PCIE2.0	SFP x 2/ PCIE not Available
JTAG/UART/RS485/CAN	1/1 / 1 / 1	Expansion Ports	240P x 2 /120P x 1
SD Card Slot Interface	1	Voltage/ Current	12V/ 3A
USB2.0 Host	4	Board Form Factors	6.77inch x 4.57inch (172mm x 116mm)



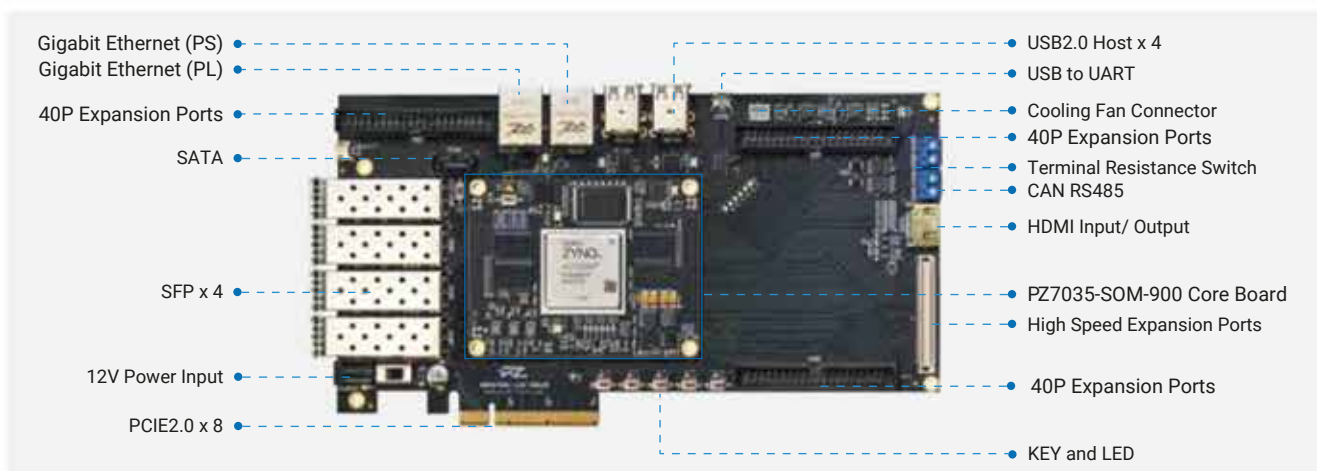
PZ7035-KFB-676

SPECIFICATION			
FPGA Chip	XC7Z035-2FFG6761	HDMI Output	1
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
QSPI FLASH	256Mb	LED/KEY	5 LEDs / 5 KEYS
EMMC	8GB	SFP/PCIE2.0	SFP x 2/ PCIE2.0 x 4
JTAG/UART/RS485/CAN	1 / 1 / 1 / 1	Expansion Ports	2 x 40P /1 x 120P
SD Card Slot Interface	1	Voltage/ Current	12V/3A
USB2.0 Host	4	Board Form Factors	6.77inch x 4.57inch (172mm x 116mm)



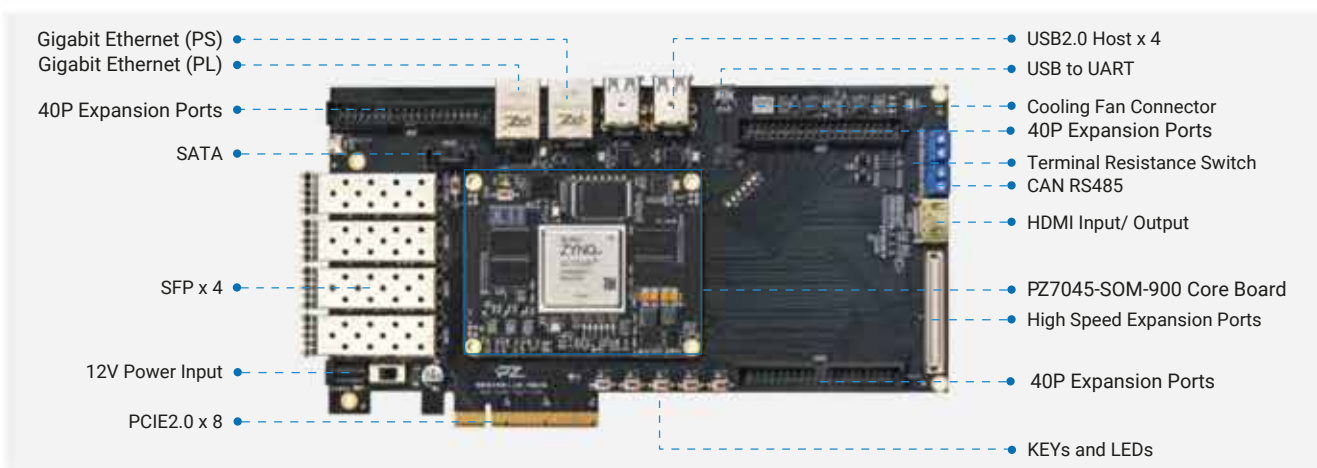
PZ7035-KFB-900

SPECIFICATION			
FPGA Chip	XC7Z035-2FFG900I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	SATA Interface	1
EMMC	8GB	SFP/ PCIE2.0	SFP x 4/ PCIE2.0 x 8
JTAG/UART/RS485/CAN	1/ 1 / 1 / 1	40P Expansion Ports	JM1/ JM2/ JM3
SD Card Slot Interface	1	Voltage/ Current	12V/3A
USB2.0 Host	4	Board Form Factors	8.66 inch x 4.57 inch (220mm x 116mm)



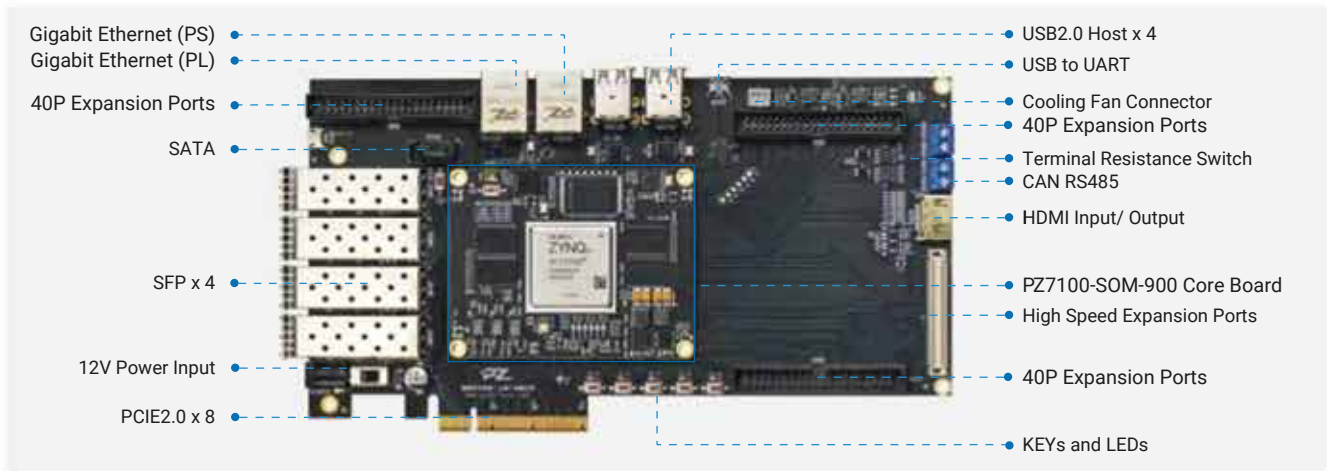
PZ7045-KFB-900

SPECIFICATION			
FPGA Chip	XC7Z045-2FFG900I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	SATA Interface	1
EMMC	8GB	SFP/ PCIE2.0	SFP x 4/ PCIE2.0 x 8
JTAG/UART/RS485/CAN	1/ 1 / 1 / 1	40P Expansion Ports	JM1/ JM2/ JM3
SD Card Slot Interface	1	Voltage/ Current	12V/3A
USB2.0 Host	4	Board Form Factors	8.66 inch x 4.57 inch (220mm x 116mm)



PZ7100-KFB-900

FPGA Chip	XC7Z100-2FFG900I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	SATA Interface	1
EMMC	8GB	SFP/PCIE2.0	SFP x 4/ PCIE2.0 x 8
JTAG/UART/RS485/CAN	1 / 1 / 1/ 1	40P Expansion Ports	JM1/ JM2/ JM3
SD Card Slot Interface	1	Voltage/ Current	12V/3A
USB2.0 Host	4	Board Form Factors	8.66 inch x 4.57 inch (220mm x 116mm)



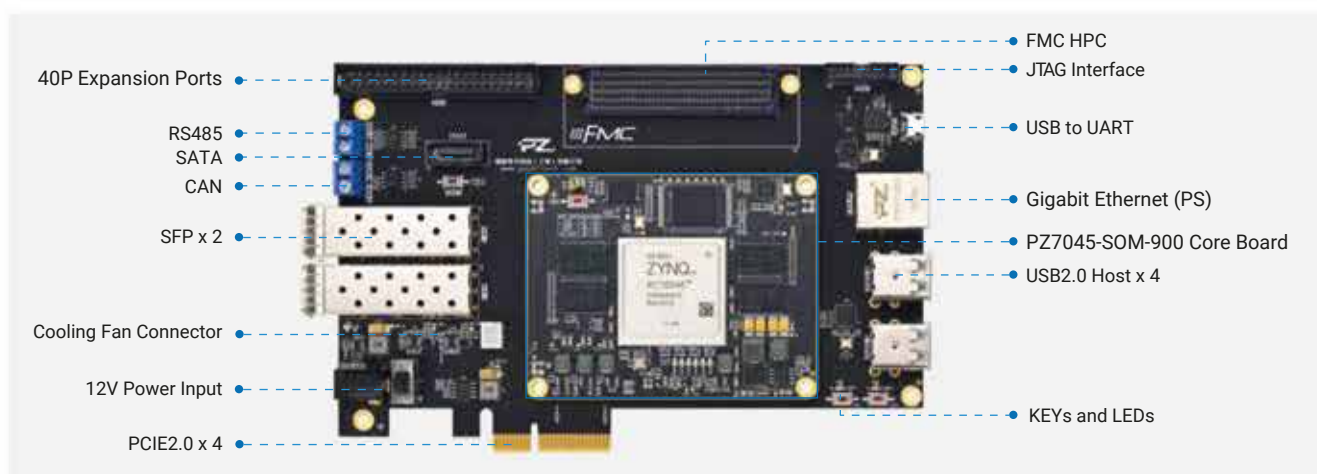
PZ7035-FH-KFB

FPGA Chip	XC7Z035-2FFG900I	Gigabit Ethernet	1
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	LED/KEY	2 LEDs / 2 KEYS
QSPI FLASH	256Mb	SFP Interface	2
EMMC	8GB	PCIE 2.0	PCIE2.0 x 4
JTAG/UART/RS485/CAN	1 / 1 / 1/ 1	FMC Interface	FMC HPC, IO All Connected
SD Card Slot Interface	1	Voltage/ Current	12V/ 3A
USB2.0 Host	4	Board Form Factors	6.69 inch x 3.94 inch (170mm x 100mm)



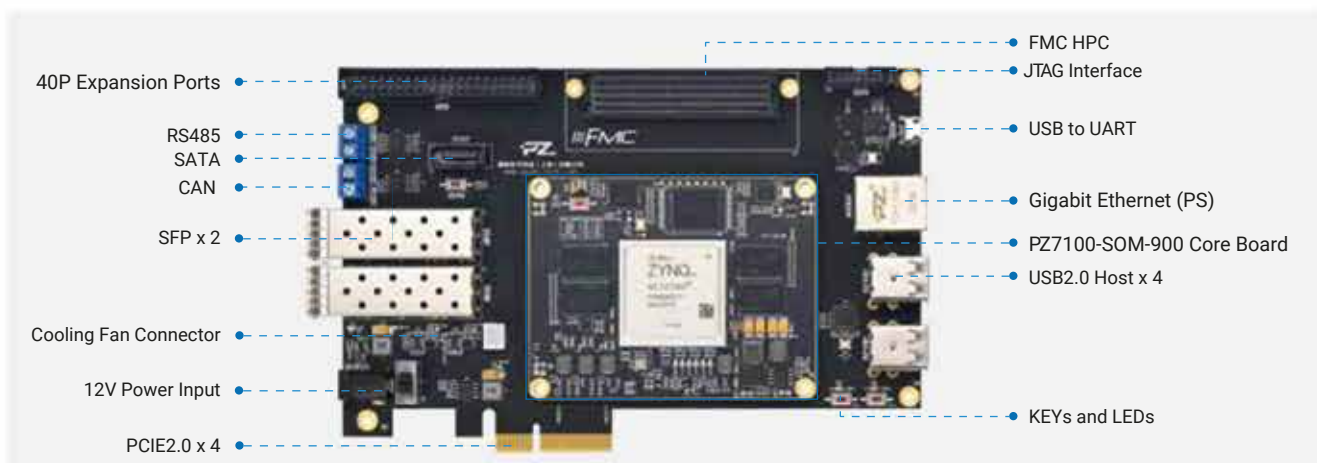
PZ7045-FH-KFB

SPECIFICATION			
FPGA Chip	XC7Z045-2FFG900I	Gigabit Ethernet	1
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	LED/KEY	2 LEDs / 2 KEYS
QSPI FLASH	256Mb	SFP	2
EMMC	8GB	PCIE2.0	x 4
JTAG/UART/RS485/CAN	1 / 1 / 1/1	FMC Interface	FMC HPC, IO All Connected
SD Card Slot Interface	1	Voltage/ Current	12V/3A
USB2.0 Host	4	Board Form Factors	6.69 inch x 3.94 inch (170mm x 100mm)



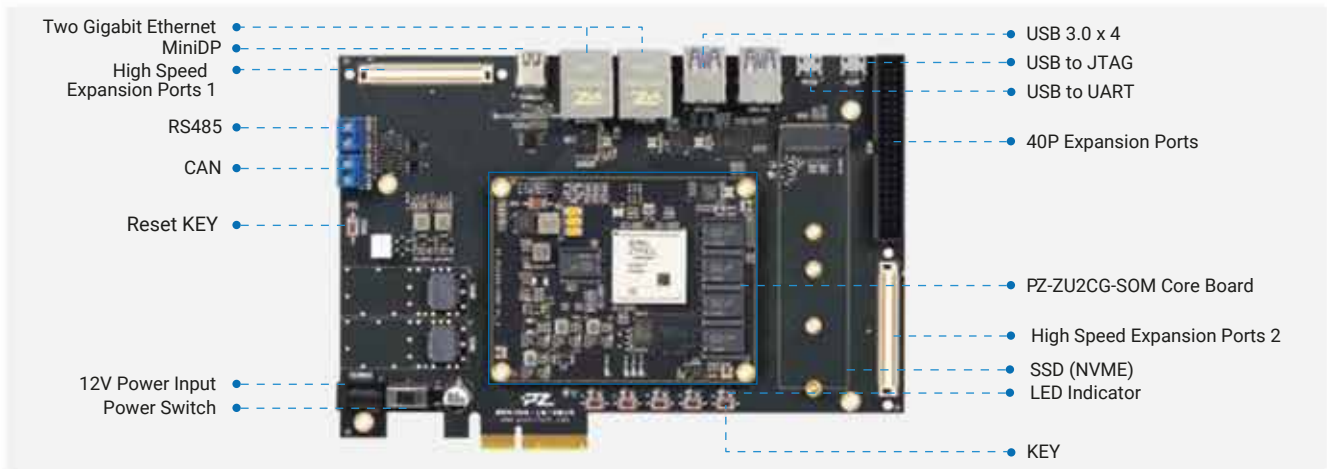
PZ7100-FH-KFB

SPECIFICATION			
FPGA Chip	XC7Z100-2FFG900I	Gigabit Ethernet	1
DDR3/DD3L	1GB on PS Side/ 1GB on PL Side	LED/KEY	2 LEDs / 2 KEYS
QSPI FLASH	256Mb	SFP Interface	2
EMMC	8GB	PCIE2.0	x 4
JTAG/UART/RS485/CAN	1 / 1 / 1/1	FMC Interface	FMC HPC, IO All Connected
SD Card Slot Interface	1	Voltage/ Current	12V/3A
USB2.0 Host	4	Board Form Factors	6.69 inch x 3.94 inch (170mm x 100mm)



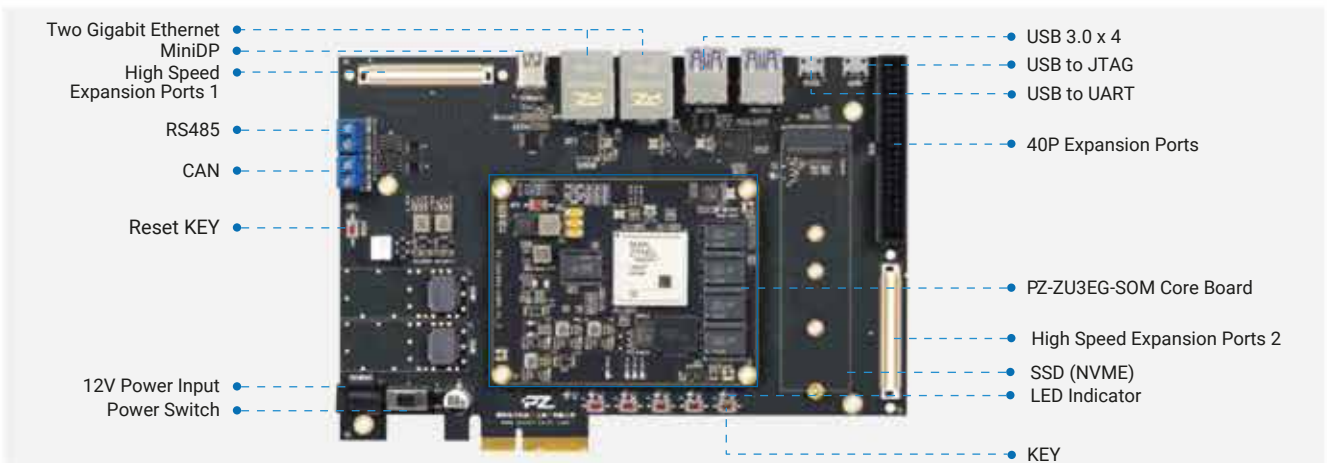
PZ-ZU2CG-KFB (PCIE not Available)

SPECIFICATION			
FPGA Chip	XCZU2CG-2SFVC784I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 4GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	Mini DP Output	1
EMMC	8GB	RTC	1
E2PROM	64Kbit	SSD (NVME)	1 on PS Side
JTAG/UART/RS485/CAN	1/1 / 1 / 1	40P Expansion Ports	1, 32 Single-ended
SD Card Slot Interface	1	120P Expansion Ports	2, 80 Single-ended



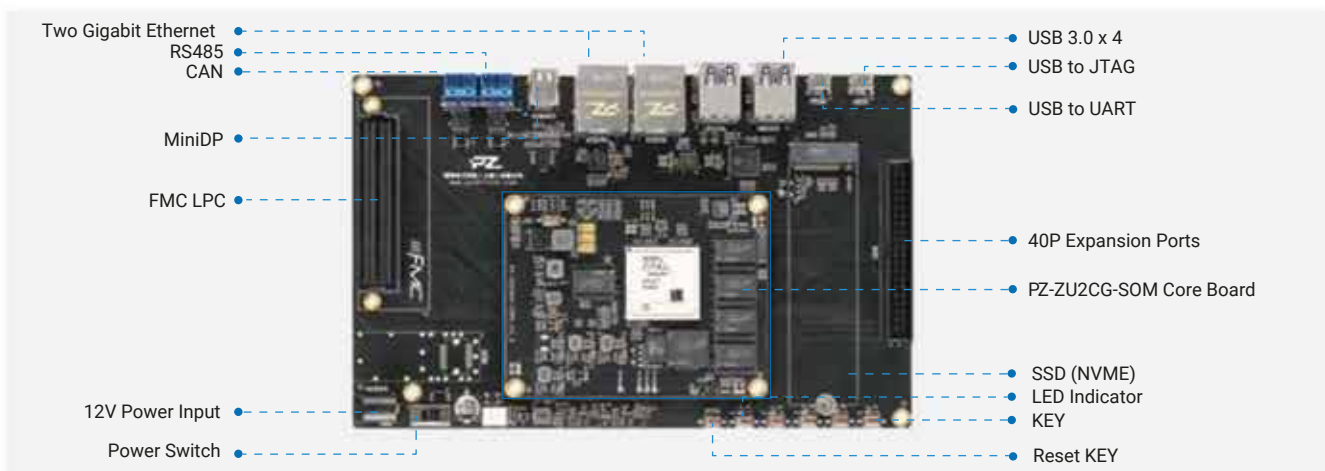
PZ-ZU3EG-KFB (PCIE not Available)

SPECIFICATION			
FPGA Chip	XCZU3EG-2SFVC784I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	Mini DP Output	1
EMMC	8GB	RTC	1
E2PROM	64Kbit	SSD (NVME)	1 on PS Side
JTAG/UART/RS485/CAN	1/1 / 1 / 1	40P Expansion Ports	1, 32 Single-ended
SD Card Slot Interface	1	120P Expansion Ports	2, 80 Single-ended



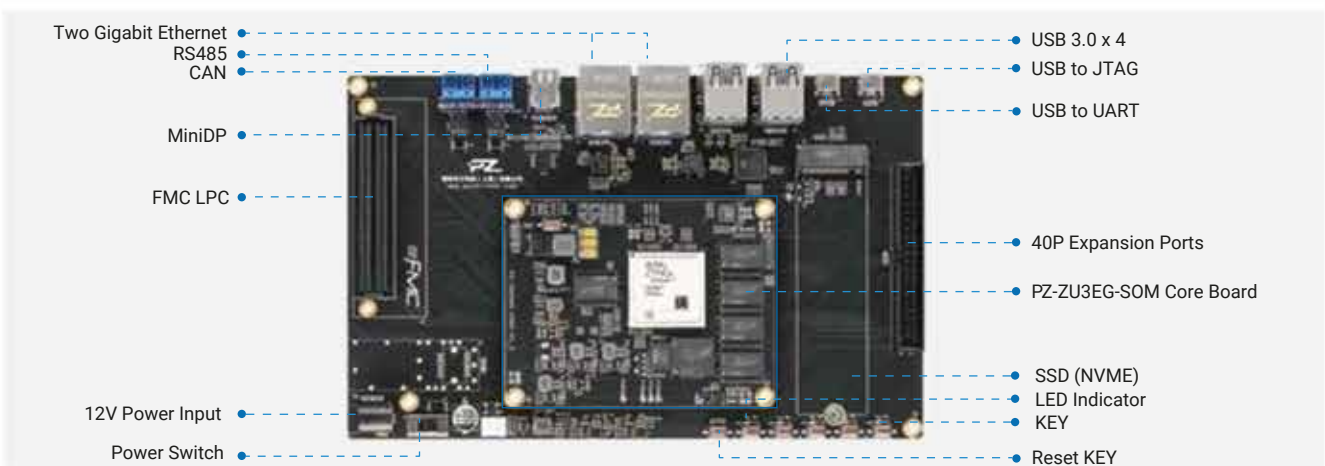
PZ-ZU2CG-FL-KFB

SPECIFICATION			
FPGA Chip	XCZU2CG-2SFVC784I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	MIPI Interface	1
EMMC	8GB	Mini DP Output	1
E2PROM	64Kbit	SSD (NVME)	1 on PS Side
JTAG/UART/RS485/CAN	1/1 / 1 / 1	40P Expansion Ports	32 Single-ended/16 Differential pairs
SD Card Slot Interface	1	FMC Interface	FMC LPC



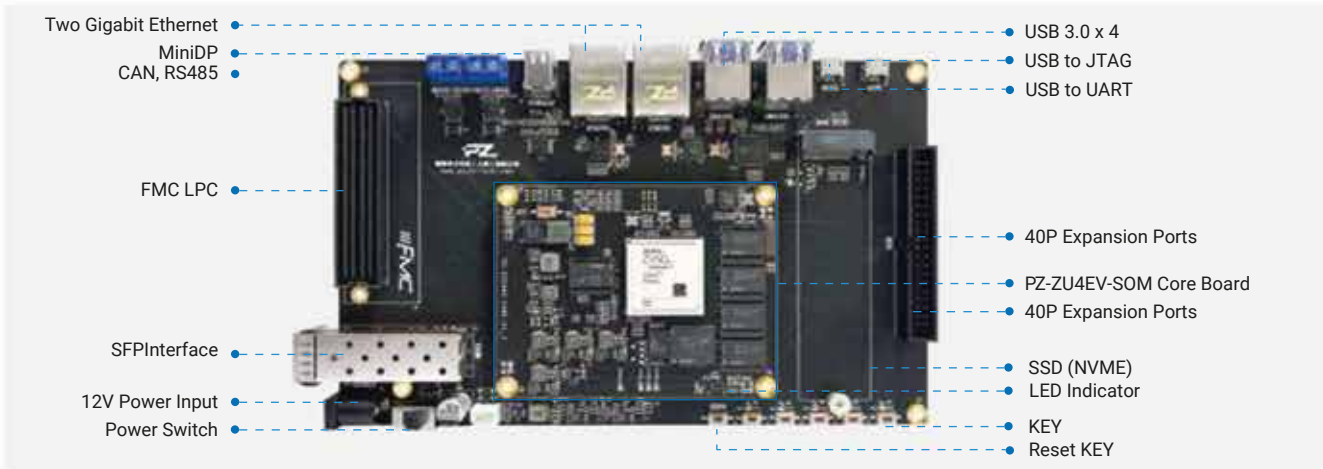
PZ-ZU3EG-FL-KFB

SPECIFICATION			
FPGA Chip	XCZU3EG-2SFVC784I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	MIPI Interface	1
EMMC	8GB	Mini DP Output	1
E2PROM	64Kbit	SSD (NVME)	1 on PS Side
JTAG/UART/RS485/CAN	1/1 / 1 / 1	40P Expansion Ports	32 Single-ended/16 Differential pairs
SD Card Slot Interface	1	FMC Interface	FMC LPC



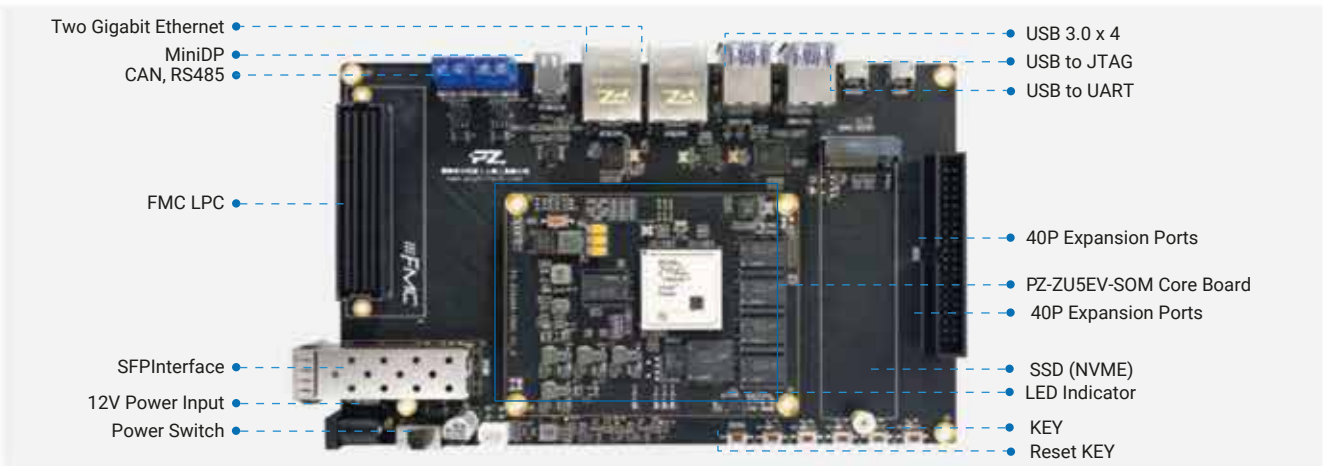
PZ-ZU4EV-FL-KFB

SPECIFICATION			
FPGA Chip	XCZU4EV-2SFVC784I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	Mini DP Output	1
EMMC	8GB	RTC	1
E2PROM	64Kbit	SSD (NVME)	1 on PS Side
JTAG/UART/RS485/CAN	1 / 1 / 1 / 1	40P Expansion Ports	1, 32 Single-ended
SD Card Slot Interface	1	120P Expansion Ports	2, 80 Single-ended



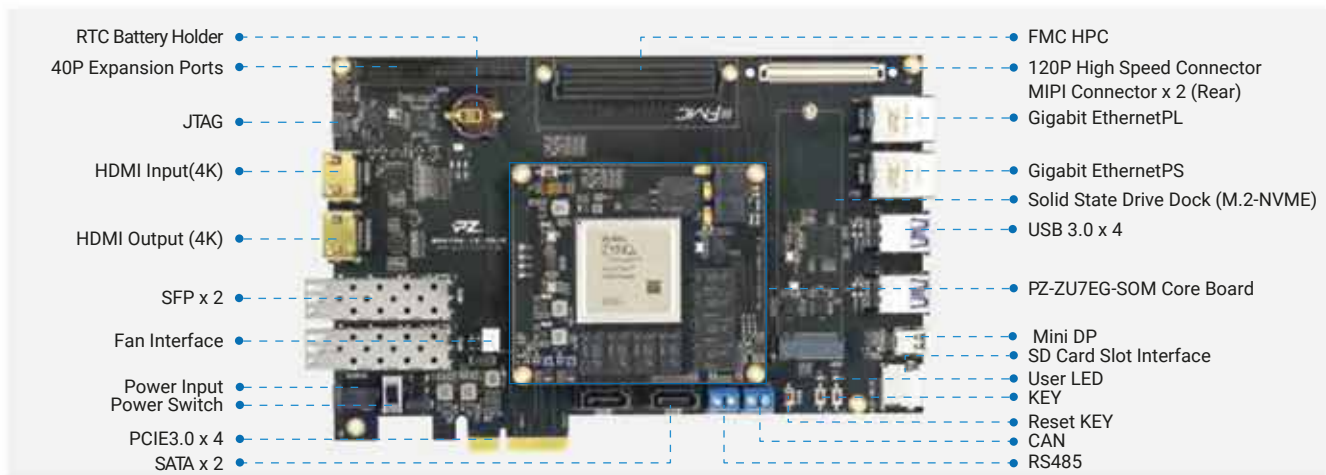
PZ-ZU5EV-FL-KFB

SPECIFICATION			
FPGA Chip	XCZU5EV-2SFVC784I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 1GB on PL Side	LED/KEY	5 LEDs / 5 KEYS
QSPI FLASH	256Mb	Mini DP Output	1
EMMC	8GB	RTC	1
E2PROM	64Kbit	SSD (NVME)	1 on PS Side
JTAG/UART/RS485/CAN	1 / 1 / 1 / 1	40P Expansion Ports	1, 32 Single-ended
SD Card Slot Interface	1	120P Expansion Ports	2, 80 Single-ended



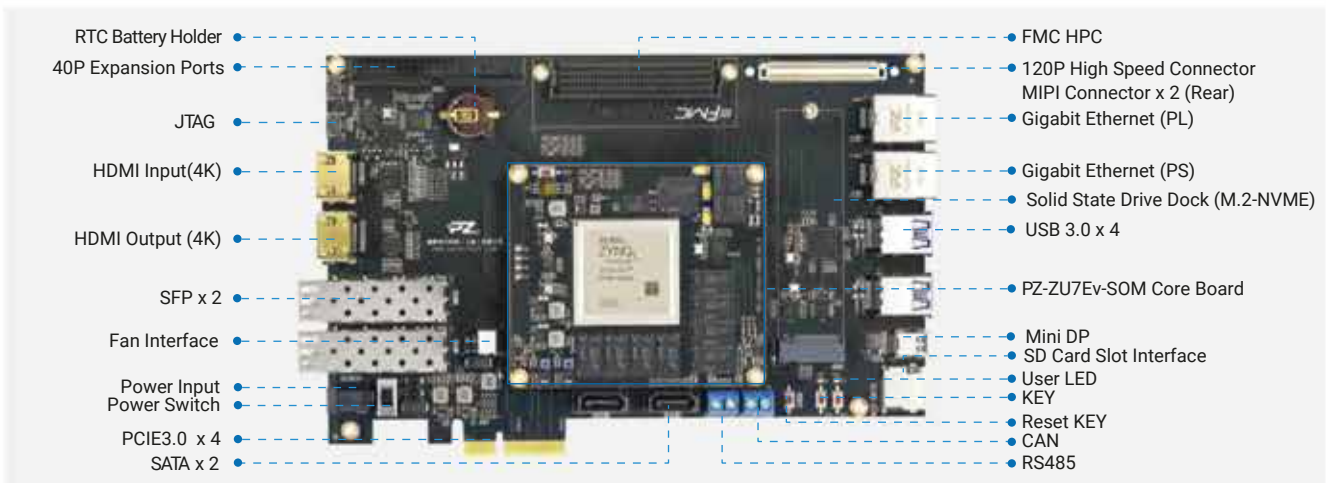
PZ-ZU7EG-KFB

SPECIFICATION			
FPGA Chip	XCZU7EG-2FFVC1156I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 4GB on PL Side	LED / KEY	2 LEDs / 2 KEYS
QSPI FLASH	256Mb + 256Mb	Mini DP Output	1
EMMC	8GB	HDMI (4K)	1 Input/1 Output, Supporting 4K/60 fps
JTAG/UART/RS485/CAN	1/ 1/1/ 1	SSD	1 (PS Side)
SD Card Slot Interface	1	SATA/SFP/PCIE3.0	SATA x 2/ SFP x 2/ PCIE 3.0 x 4
MIPI	2 (2lane)	FMC Expansion Ports	FMC HPC



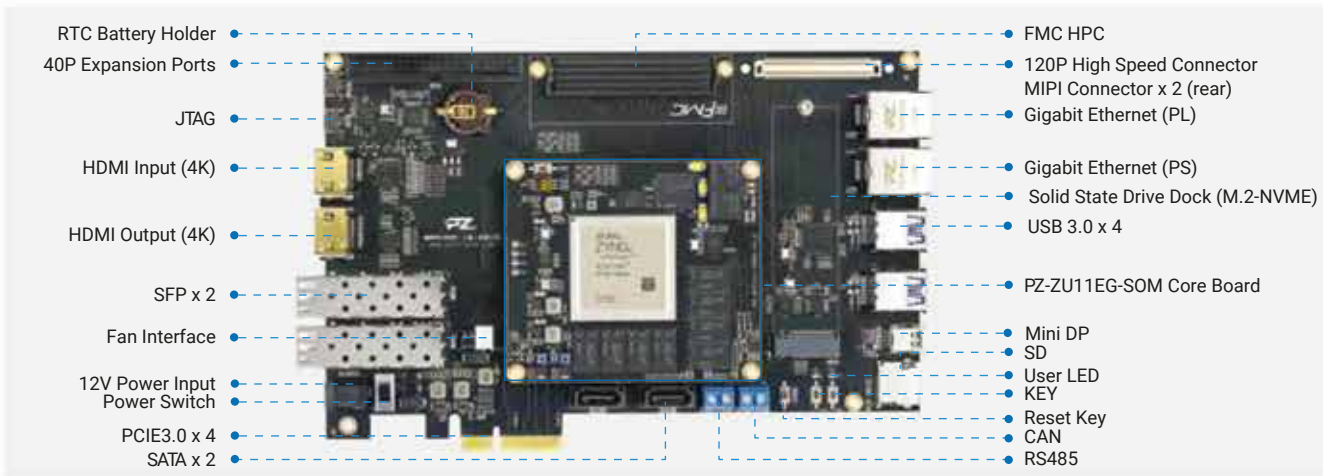
PZ-ZU7EV-KFB

SPECIFICATION			
FPGA Chip	XCZU7EV-2FFVC1156I	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
DDR4	4GB on PS Side/ 4GB on PL Side	LED / KEY	2 LEDs / 2 KEYS
QSPI FLASH	256Mb+256Mb	Mini DP Output	1
EMMC	8GB	HDMI (4K)	1 Input/1 Output, Supporting 4K/60 fps
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	SSD	1 (PS Side)
SD Card Slot Interface	1	SATA/SFP/PCIE3.0	SATA x 2/ SFP x 2/ PCIE 3.0 x 4
MIPI	2 (2lane)	FMC Expansion Ports	FMC HPC



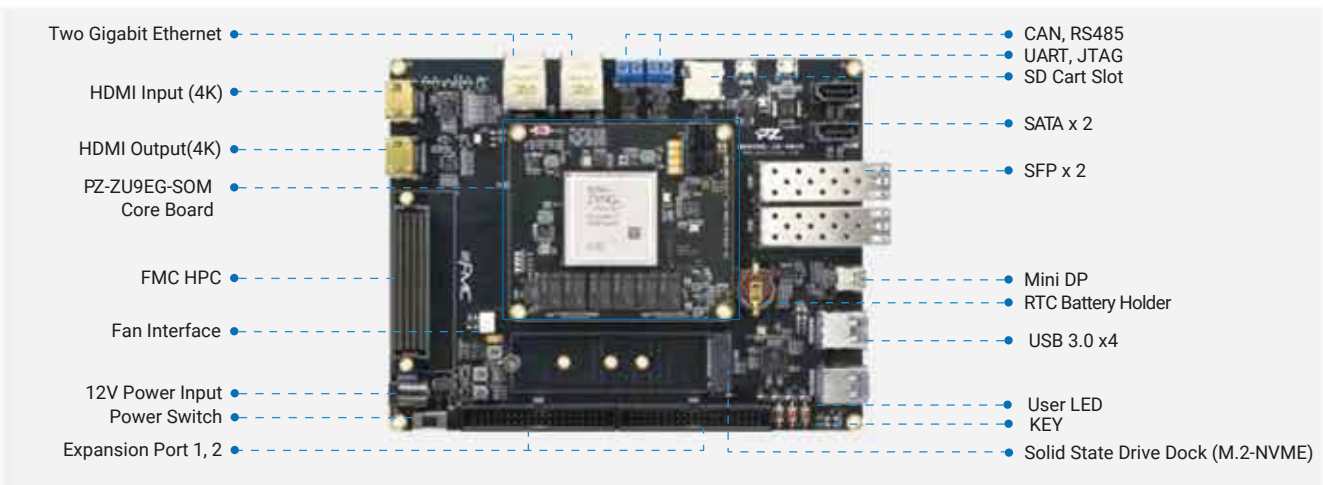
PZ-ZU11EG-KFB

SPECIFICATION			
FPGA Chip	XCZU11EG-2FFVC1156I	Gigabit Ethernet	1 on PS Side/1 on PL Side
DDR4	4GB on PS Side/ 4GB on PL Side	LED / KEY	2 LEDs / 2 KEYS
QSPI FLASH	256Mb + 256Mb	Mini DP Output	1
EMMC	8GB	HDMI (4K)	1 Input/1 Output , support 4K/60 fps
JTAG/UART/RS485/CAN	1/ 1/1/ 1	SSD	1 (PS Side)
SD Card Slot Interface	1	SATA/SFP/PCIE3.0	SATA x 2/ SFP x 2/ PCIE 3.0 x 4
MIPI	MIPI x 2	FMC Expansion Ports	FMC HPC



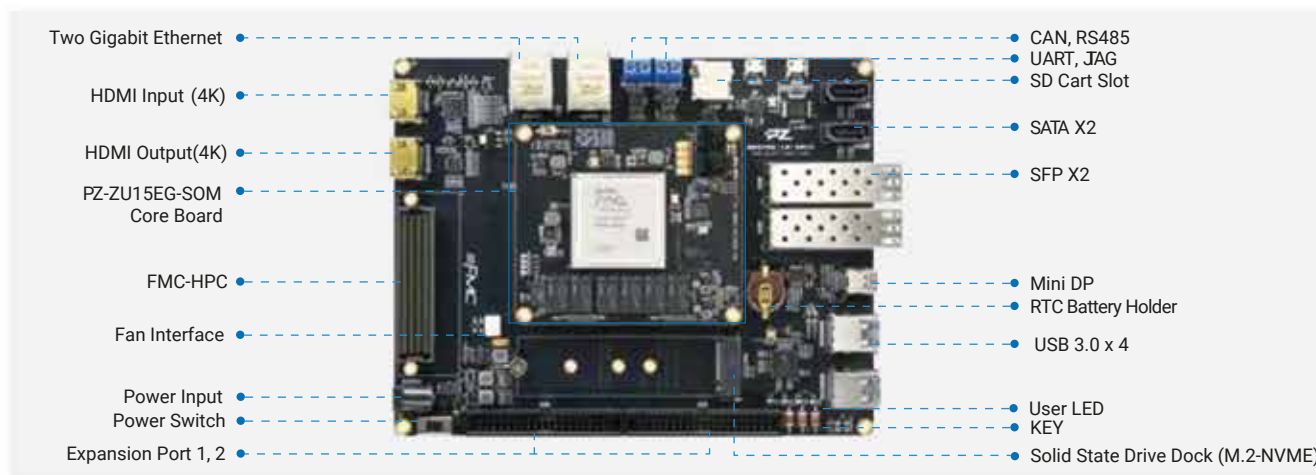
PZ-ZU9EG-KFB

SPECIFICATION			
FPGA Chip	XCZU9EG-2FFVB1156I	Gigabit Ethernet	1 on PS Side/1 on PL Side
DDR4	4GB on PS Side/ 2GB on PL Side	MIPI/ USB 3.0	MIPI x 2/ USB 3.0 x 4
QSPI FLASH	256Mb + 256Mb	LED / KEY	4 LEDs / 3 KEYS
EMMC	8GB	Mini DP Interface	1
E2PROM	64Kbit	HDMI (4K)	1 HDMI Input, 1 HDMI Output
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	SSD	1 (PS Side)
SD Card Slot Interface	1	SATA/SFP	SATA x 2/ SFP x2



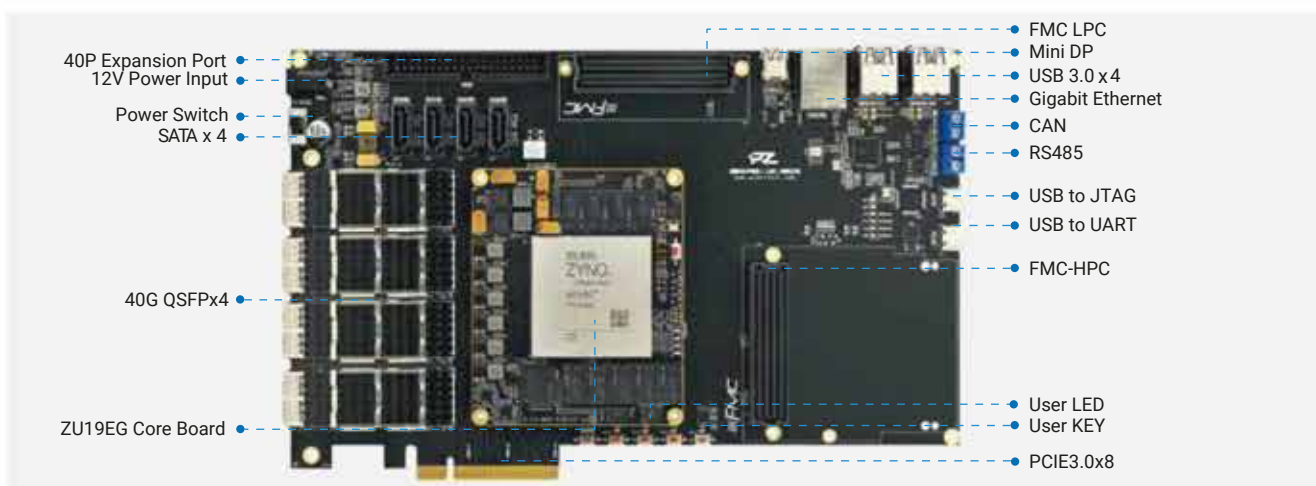
PZ-ZU15EG-KFB

SPECIFICATION			
FPGA Chip	XCZU15EG-2FFVB1156I	Gigabit Ethernet	1 on PS Side/1 on PL Side
DDR4	4GB on PS Side/ 2GB on PL Side	MIPI/ USB 3.0	MIPI x 2/ USB 3.0 x 4
QSPI FLASH	256Mb + 256Mb	LED/KEY	4 LEDs/ 3 KEYS
EMMC	8GB	Mini DP Interface	1
E2PROM	64Kbit	HDMI (4K)	1 HDMI Input, 1 HDMI Output
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	SSD	1 (PS Side)
SD Card Slot Interface	1	SATA/SFP	SATA x 2/ SFP x2



PZ-ZU19EG-KFB

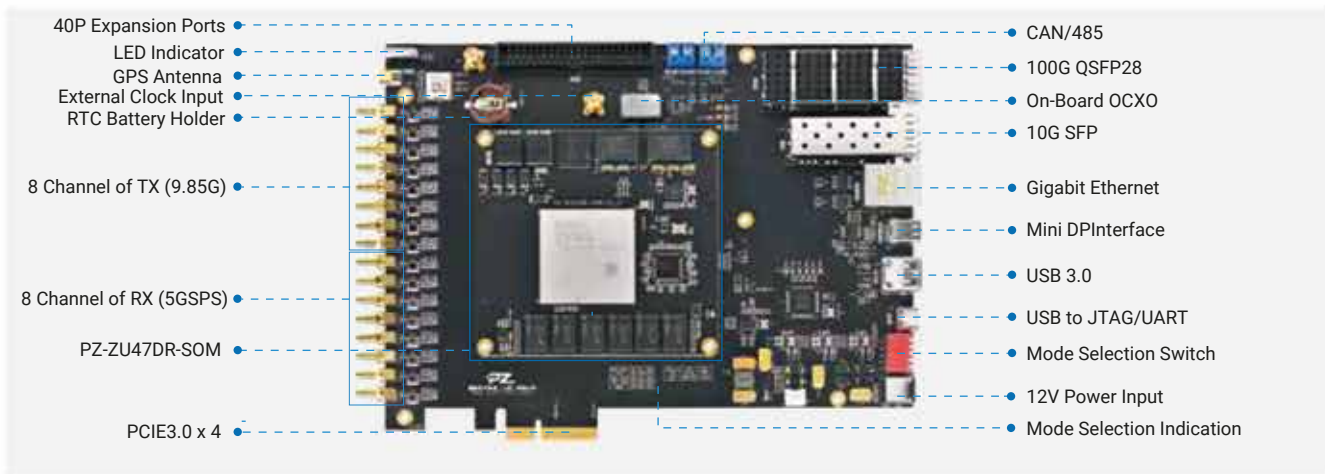
SPECIFICATION			
FPGA Chip	XCZU19EG-2FFVC1760I	Mini DP Interface	1
DDR4	8GB on PS side (with ECC), 8GB on PL side	SSD Interface (NVME)	1
EMMC/ E2PROM	32GB/64Kbit	QSF28 / SATA	QSFP28 (100G) x 4/ 4 SATA
QSPI FLASH	128MB	MIPI/40P Expansion Port	MIPI x 2/1 x 40P
Start-up Mode	JTAG/SD/QSPI/EMMC	FMC	1 HPC FMC, 1 LPC FMC
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	LED/KEY	4 LEDs/ 4 KEYS
Gigabit Ethernet/USB 3.0	1 Gigabit Ethernet on PS Side/ 4 USB 3.0	Voltage/ Current	12V/ 3A



PZ-ZU47DR-KFB

SPECIFICATION

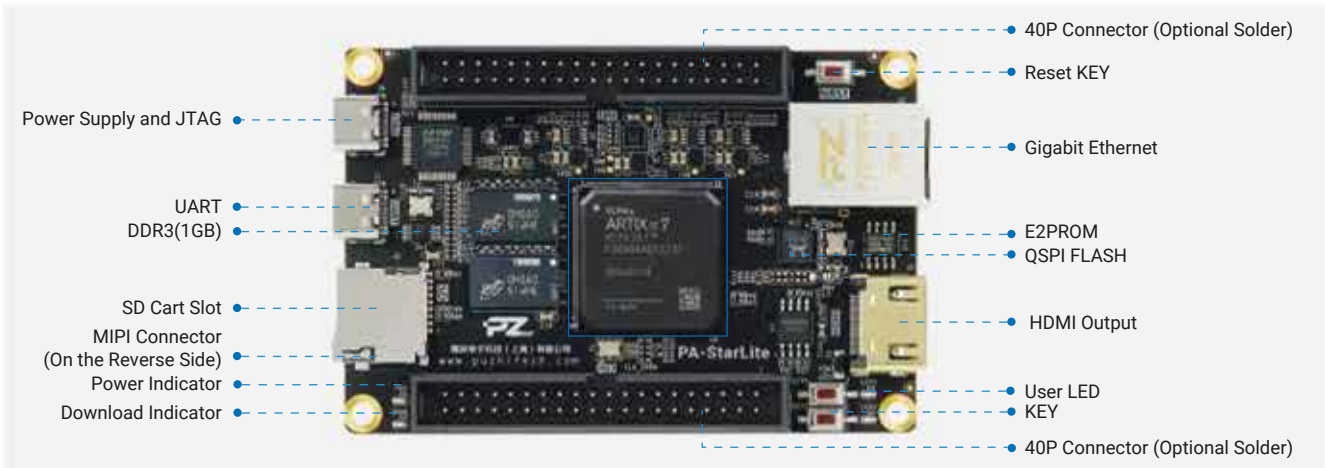
FPGA Chip	XCZU47DR-2FFVE1156I	Mini DP Interface	1
DDR4	8GB on PS Side (with ECC), 4GB on PL Side	SSD Interface (NVME)	1
EMMC/ E2PROM	32GB	QSF28 /SFP	QSFP28 (100G) x 1/ SFP x 1
QSPI FLASH	512Mb QSPI 0 + 512Mb QSPI 1	40P Expansion Ports	1
Start-up Mode	JTAG/SD/QSPI/EMMC	TX/RX	8 Channels Each
JTAG/UART/RS485/CAN	1/ 1/ 1/ 1	GPS	1
Gigabit Ethernet/USB 3.0	1/ 1	Voltage/ Current	12V/3A



PA35T-StarLite

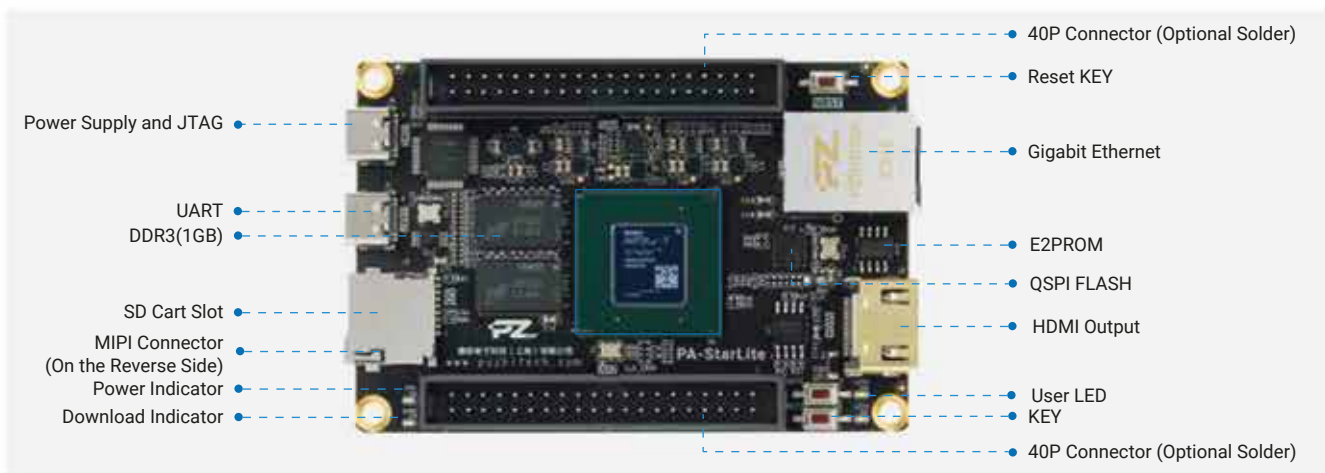
SPECIFICATION

FPGA Chip	XC7A35T-2FGG484I	Gigabit Ethernet	1
DDR3	1GB/ 16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	LED	2
E2PROM	64Kbit	KEY	2
JTAG	1	2 * 40P Expansion Ports	64 Single-end / 32 Differential Pairs
UART	1	Form Factors	3.54 inch x 2.36 inch (90 mm x 60 mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



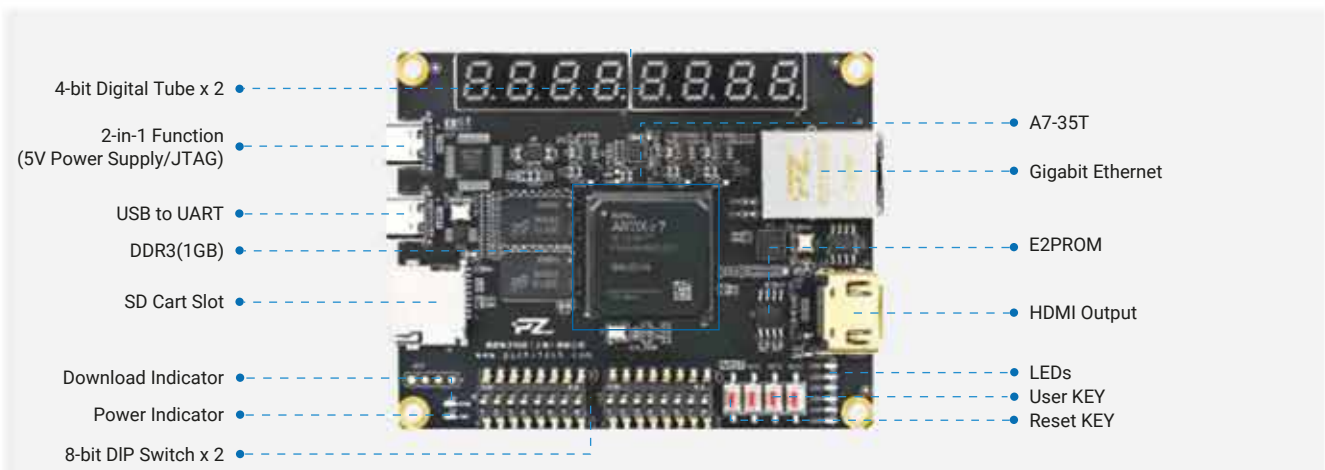
PA200T-StarLite

SPECIFICATION			
FPGA Chip	XC7A200T-2FBG484I	Gigabit Ethernet	1
DDR3	1GB/ 16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	MIPI CSI	1 (2lane)
E2PROM	64Kbit	LED/KEY	2 LEDs/ 2 KEYS
JTAG	1	2*40P Expansion Ports	64 Single-end / 32 Differential Pairs
UART	1	Form Factors	3.54 inch x 2.36 inch (90 mm x 60 mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



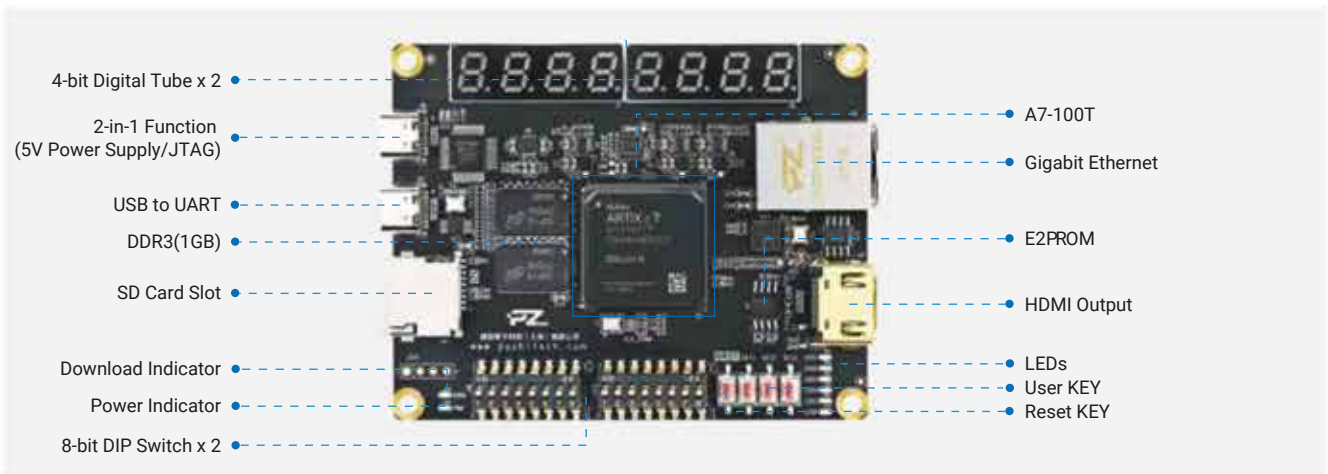
PA35T-EDU

SPECIFICATION			
FPGA Chip	XC7A35T-2FGG484I	Gigabit Ethernet	1
DDR3	1GB/ 16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	Crystal Oscillator	200MHz Differential Crystal for FPGA
E2PROM	64Kbit	LED/KEY	6 LEDs/ 4 KEYS
JTAG	1	40P 0.5mm FPC Interface	5V 3.3A power supply and 33 IOs Included
UART	1	Form Factors	3.54 inch x 2.76 inch (90 mm x 70 mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



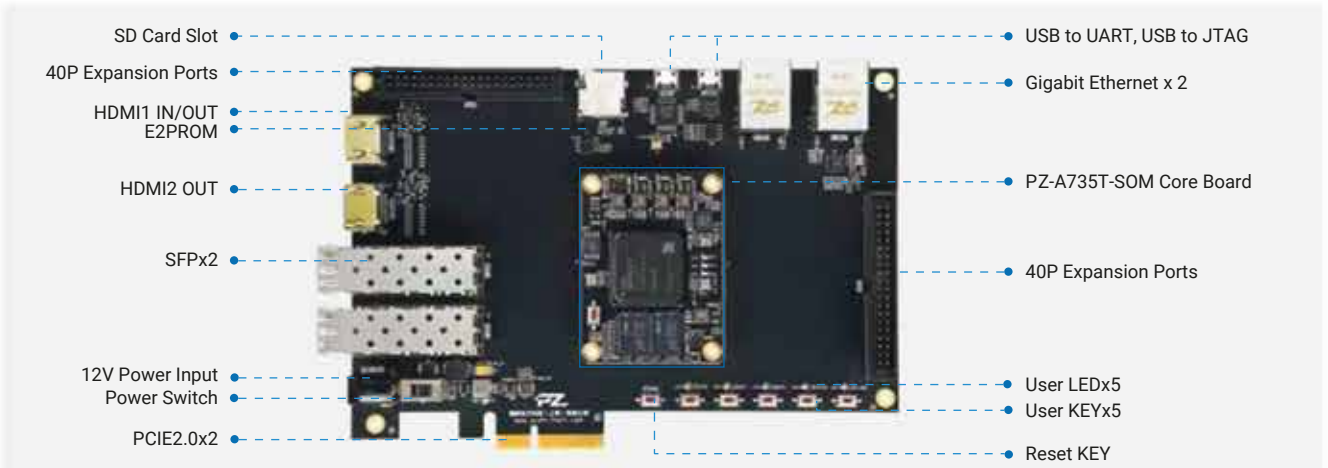
PA100T-EDU

SPECIFICATION			
FPGA Chip	XC7A100T-2FGG484I	Gigabit Ethernet	1
DDR3	1GB/16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	Crystal Oscillator	200MHz Differential Crystal for FPGA
E2PROM	64Kbit	LED/ KEY	6 LEDs/ 4 KEYS
JTAG	1	40P 0.5mm FPC Interface	5V 3.3A power supply and 33 IOs Included
UART	1	Form Factors	3.54 inch x 2.76 inch (90 mm x 70 mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



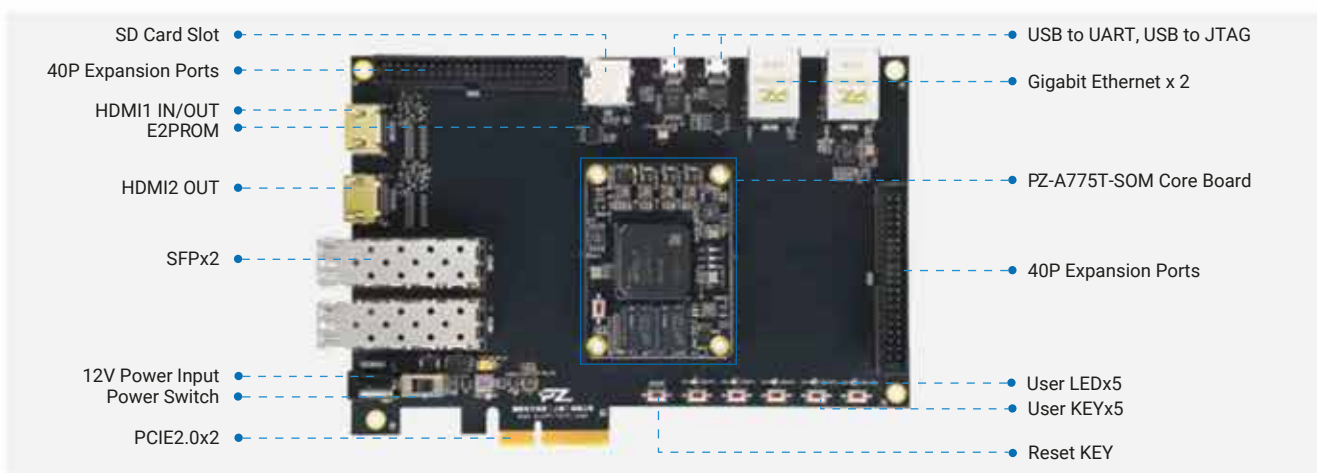
PZ-A735T-KFB

SPECIFICATION			
FPGA Chip	XC7A35T-2FGG484I	User LED/ User KEY	5 LEDs/ 5 KEYS
DDR3	1GB/16bit	HDMI	1 HDMI Input/Output, 1 HDMI Output
QSPI FLASH	256Mb	SFP	2
E2PROM	64Kbit	PCIE2.0	x 2
JTAG/UART	1/ 1	2x40P Expansion Ports	64 Single-end / 32 Differential Pairs
SD Card Slot Interface	1	Voltage/ Current	12V/1A
Gigabit Ethernet	2	Board Form Factors	6.77 inch x 4.57 inch (172mm x 116mm)



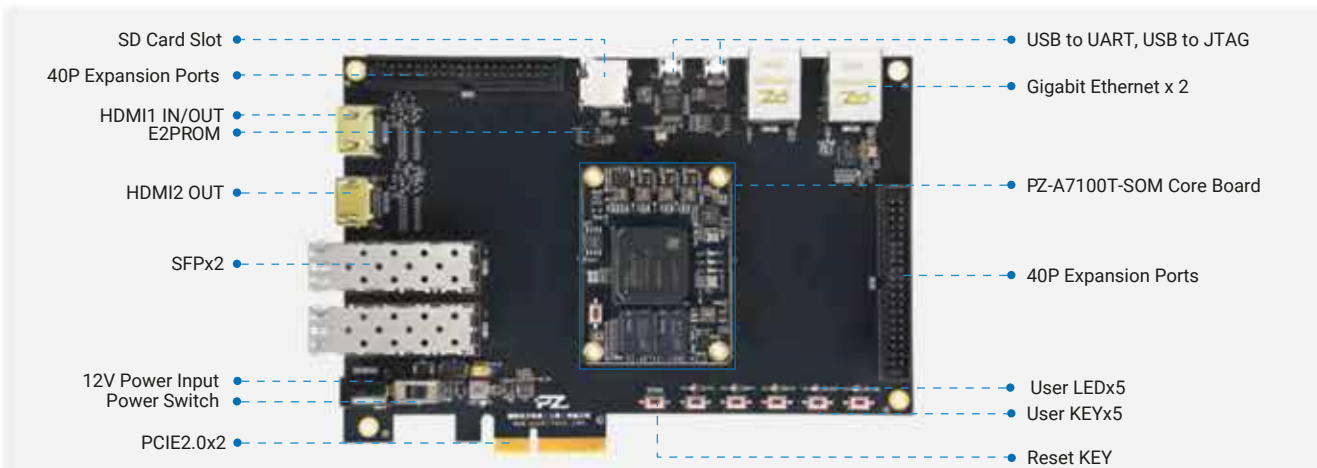
PZ-A775T-KFB

SPECIFICATION			
FPGA Chip	XC7A75T-2FGG484I	User LED/ User KEY	5 LEDs/ 5 KEYS
DDR3	1GB/16bit	HDMI Interface	1 HDMI Input/Output, 1 HDMI Output
QSPI FLASH	256MB	SFP Interface	2
E2PROM	64Kbit	PCIE2.0	x 2
JTAG/UART	1/ 1	2x40P Expansion Ports	64 Single-end / 32 Differential Pairs
SD Card Slot Interface	1	Voltage/ Current	12V/1A
Gigabit Ethernet	2	Board Form Factors	6.77 inch x 4.57 inch (172mm x 116mm)



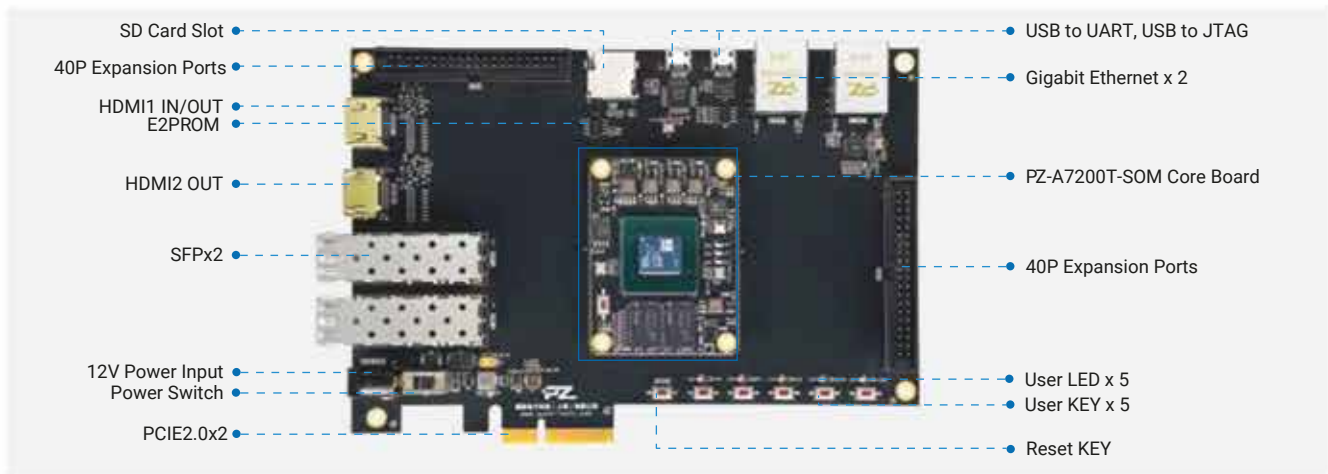
PZ-A7100T-KFB

SPECIFICATION			
FPGA Chip	XC7A100T-2FGG484I	User LED/ User KEY	5 LEDs/ 5 KEYS
DDR3	1GB/16bit	HDMI Interface	1 HDMI Input/Output, 1 HDMI Output
QSPI FLASH	256Mb	SFP Interface	2
E2PROM	64Kbit	PCIE2.0	x 2
JTAG/UART	1/ 1	2x40P Expansion Ports	64 Single-end / 32 Differential Pairs
SD Card Slot Interface	1	Voltage/ Current	12V/1A
Gigabit Ethernet	2	Board Form Factors	6.77 inch x 4.57 inch (172mm x 116mm)



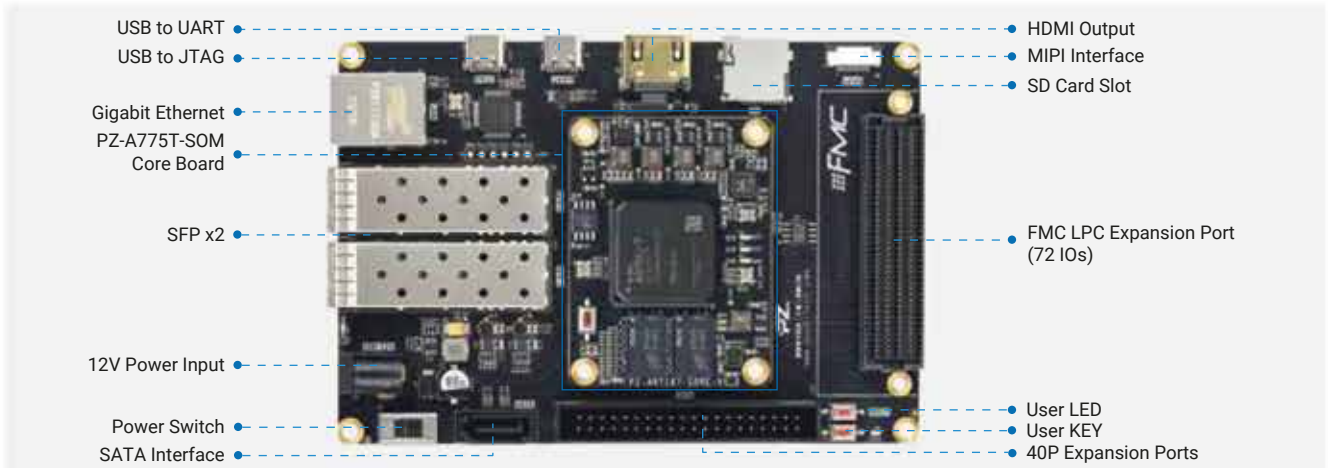
PZ-A7200T-KFB

SPECIFICATION			
FPGA Chip	XC7A200T-2FBG484I	User LED/ User KEY	5 LEDs/ 5 KEYS
DDR3	1GB/16bit	HDMI Interface	1 HDMI Input/Output, 1 HDMI Output
QSPI FLASH	256Mb	SFP Interface	2
E2PROM	64Kbit	PCIE 2.0	x 2
JTAG/UART	1/ 1	2 x 40P Expansion Ports	64 Single-end / 32 Differential Pairs
SD Card Slot Interface	1	Voltage/ Current	12V/1A
Gigabit Ethernet	2	Board Form Factors	6.77 inch x 4.57 inch (172mm x 116mm)



PA75T-FL-KFB

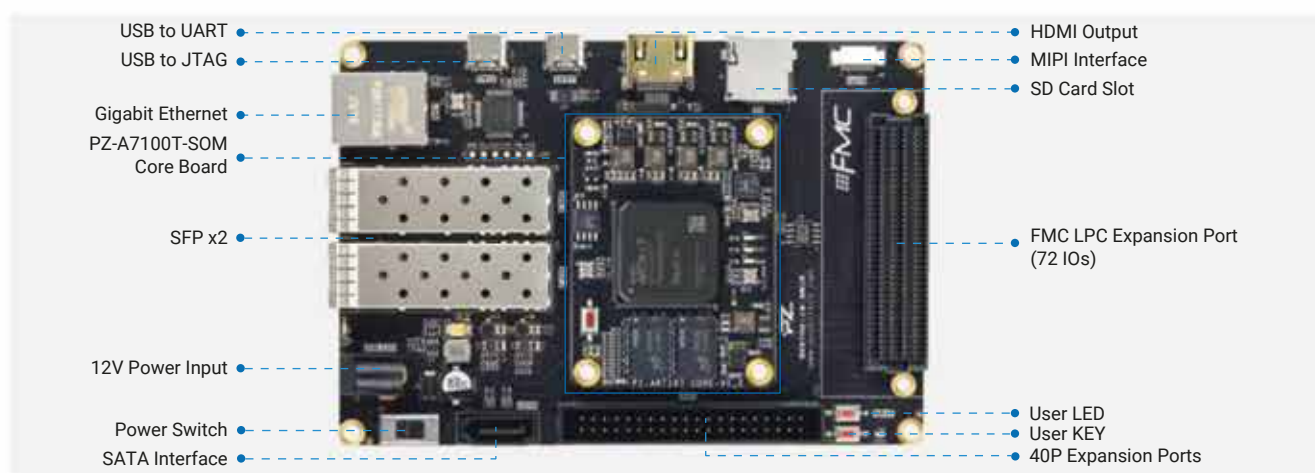
SPECIFICATION			
FPGA Chip	XC7A75T-2FGG484I	Gigabit Ethernet	1
DDR3	1GB/16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	MIPI CSI	1 (2lane)
GTP	4 Pairs of TX/RX	LED/KEY	1 LED/ 1 KEY
JTAG	1	40P Expansion Ports	32 Single-end / 16 Differential Pairs
UART	1	Board Form Factors	5.12 inch x 3.54 inch (130 mm x 90mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



PA100T-FL-KFB

SPECIFICATION

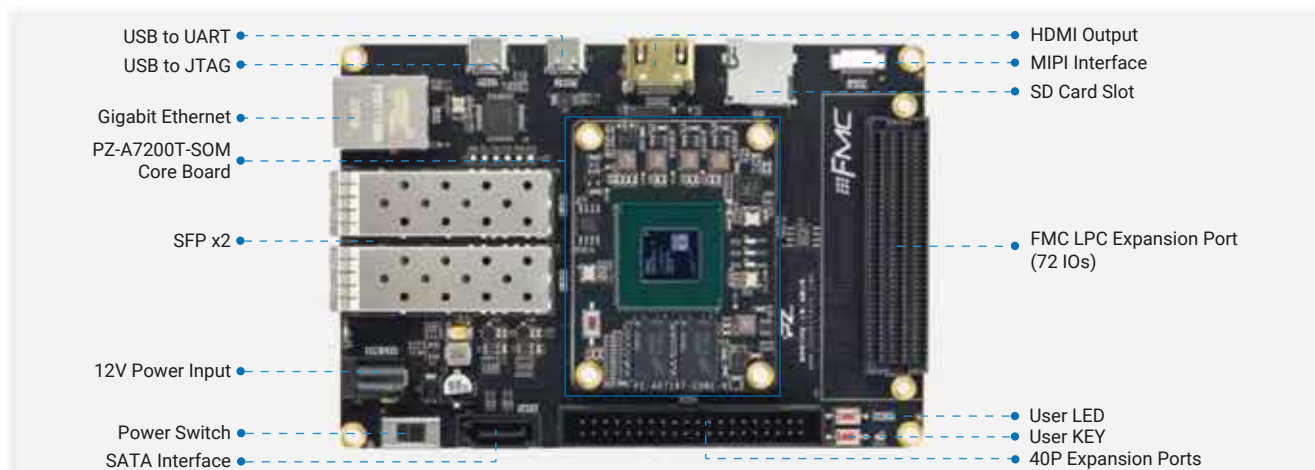
FPGA Chip	XC7A100T-2FGG484I	Gigabit Ethernet	1
DDR3	1GB/16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	MIPI CSI	1 (2lane)
GTP	4 Pairs of TX/RX	LED/KEY	1 LED/ 1 KEY
JTAG	1	40P Expansion Ports	32 Single-end / 16 Differential Pairs
UART	1	Board Form Factors	5.12 inch x 3.54 inch (130 mm x 90mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



PA200T-FL-KFB

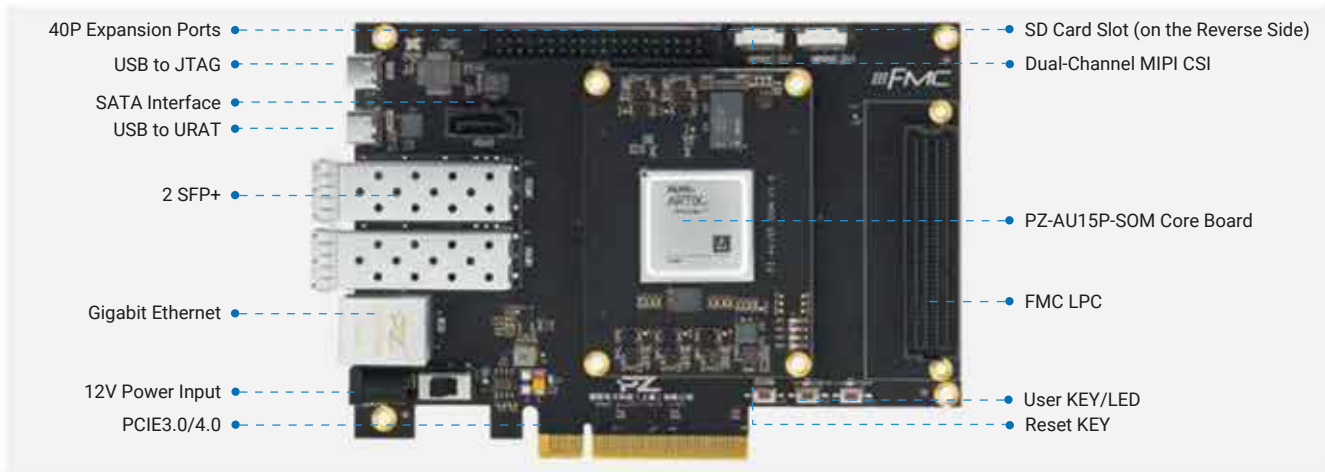
SPECIFICATION

FPGA Chip	XC7A200T-2FBG484I	Gigabit Ethernet	1
DDR3	1GB/16bit	HDMI	1 HDMI Output
QSPI FLASH	256Mb	MIPI CSI	1 (2lane)
GTP	4 Pairs of TX/RX	LED/KEY	1 LED/ 1 KEY
JTAG	1	40P Expansion Ports	32 Single-end / 16 Differential Pairs
UART	1	Board Form Factors	5.12 inch x 3.54 inch (130 mm x 90mm)
SD Card Slot Interface	1	Technology	Black Matte, Immersion Gold Process



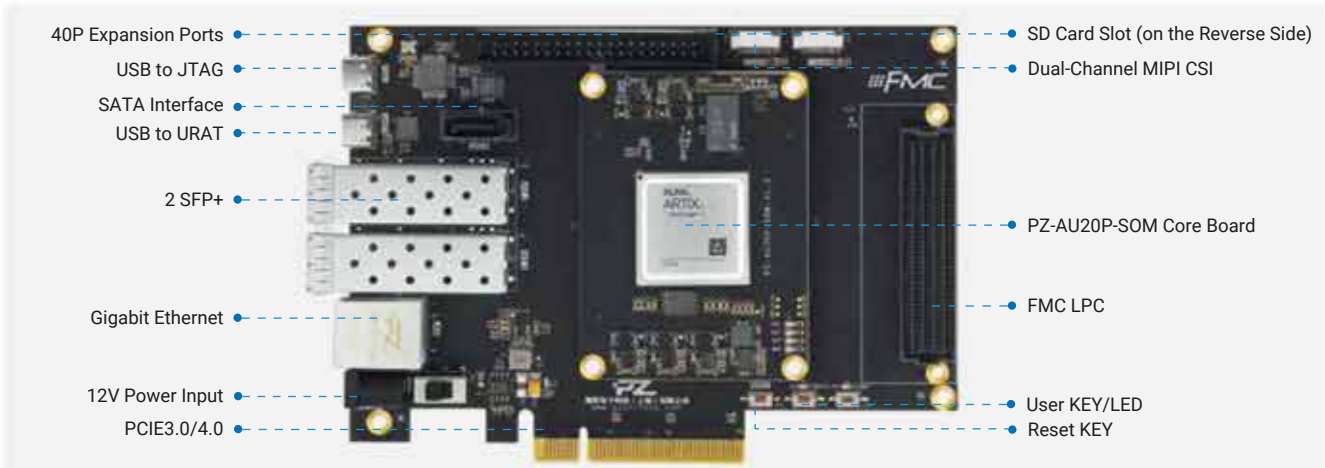
PZ-AU15P-KFB

SPECIFICATION			
FPGA Chip	XCAU15P-2FFVB676I	LED/ KEY	2LEDs/ 2 KEYS
DDR4	1GB/16bit	SFP Interface	2
QSPI FLASH	256Mb	PCIE3.0/4.0	x 8/ x 4
E2PROM	64Kbit	40P Expansion Ports	32 Single-ended/ 16 Differential Pairs
JTAG/UART	1 / 1	Voltage/ Current	12V/3A
SD Card Slot Interface	2	SOM Form Factors	2.95 inch x 2.17 inch (75 mm x 55 mm)
Gigabit Ethernet	1	Board Form Factors	5.83 inch x 4.21 inch (148mm x 107mm)



PZ-AU20P-KFB

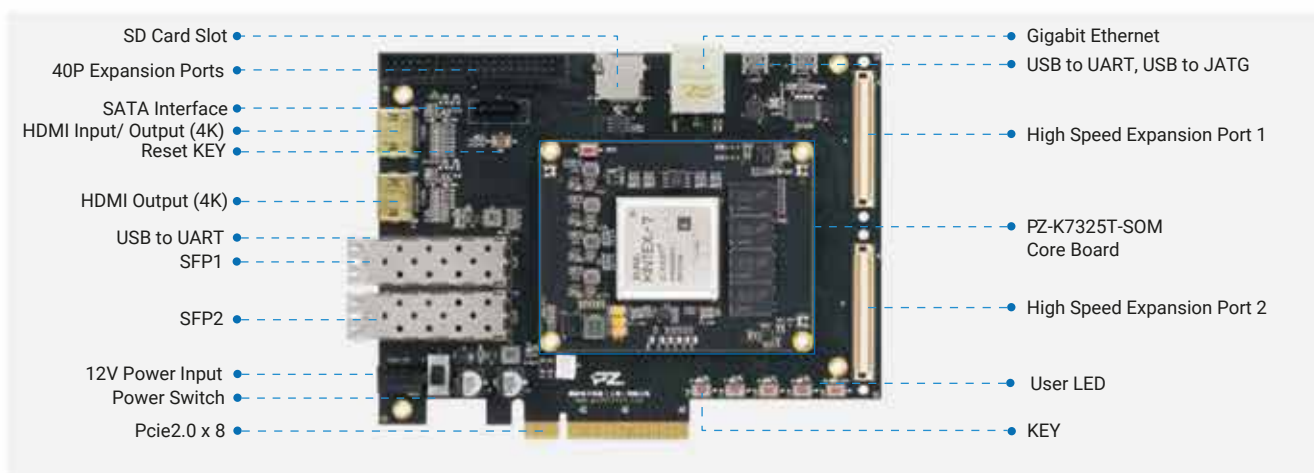
SPECIFICATION			
FPGA Chip	XCAU20P-2FFVB676I	LED/ KEY	2LEDs/ 2 KEYS
DDR4	1GB/16bit	SFP Interface	2
QSPI FLASH	256Mb	PCIE3.0/4.0	x 8/ x 4
E2PROM	64Kbit	40P Expansion Ports	32 Single-ended/ 16 Differential Pairs
JTAG/UART	1 / 1	Voltage/ Current	12V/3A
SD Card Slot Interface	2	SOM Form Factors	2.95 inch x 2.17 inch (75 mm x 55 mm)
Gigabit Ethernet	1	Board Form Factors	5.83 inch x 4.21 inch (148mm x 107mm)



PZ-K7325T-KFB

SPECIFICATION

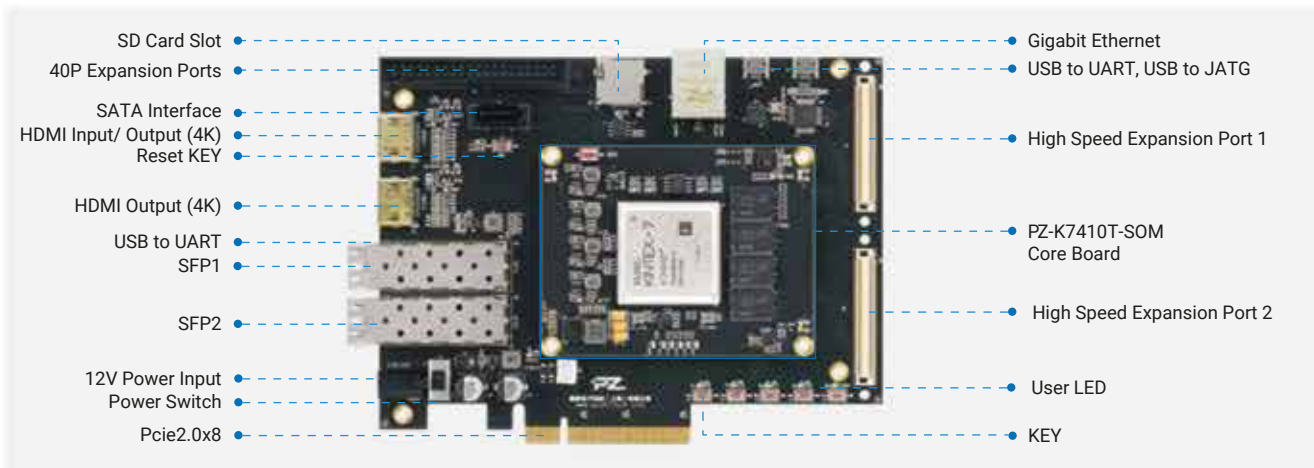
FPGA Chip	XC7K325T-2FFG900I	LED	5 LEDs
DDR3	2GB /64bit	KEY	5 KEYS
QSPI FLASH	256Mb	HDMI Interface	1 HDMI Input Output/ 1 HDMI Output
E2PROM	64Kbit	SFP/ SATA	2/ 1
JTAG/UART	1/ 1	PCIE2.0	x 8
SD Card Slot Interface	1	40P Expansion Ports	32 Single-ended/16 Differential Pairs
Gigabit Ethernet	1	120P Expansion Ports	96 Single-ended/ 48 Differential Pairs



PZ-K7410T-KFB

SPECIFICATION

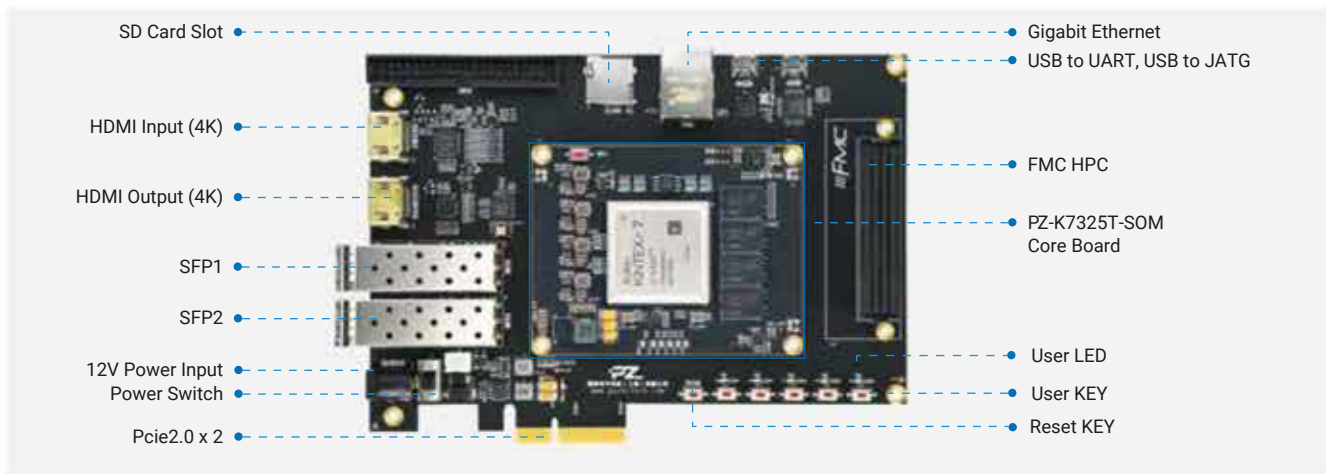
FPGA Chip	XC7K410T-2FFG900I	LED	5 LEDs
DDR3	2GB /64bit	KEY	5 KEYS
QSPI FLASH	256Mb	HDMI Interface	1 HDMI Input Output/ 1 HDMI Output
E2PROM	64Kbit	SFP/ SATA	2/ 1
JTAG/UART	1/ 1	PCIE2.0	x 8
SD Card Slot Interface	1	40P Expansion Ports	32 Single-ended/16 Differential Pairs
Gigabit Ethernet	1	120P Expansion Ports	96 Single-ended/ 48 Differential Pairs



PZ-K7325T-FH-KFB

SPECIFICATION

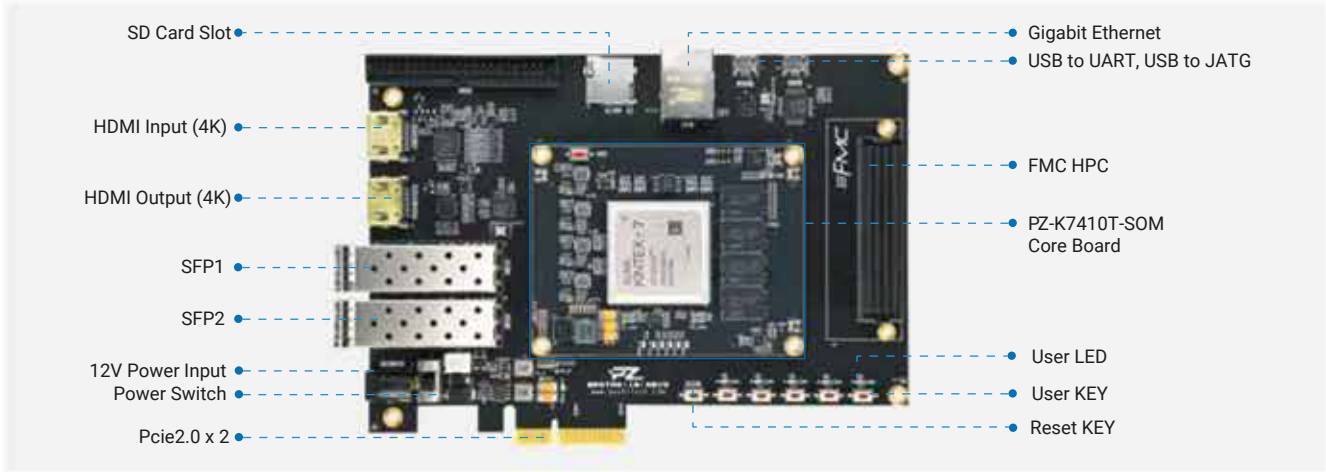
FPGA Chip	XC7K325T-2FFG900I	LED	5 LEDs
DDR3	2GB /64bit	KEY	5 KEYS
QSPI FLASH	256Mb	HDMI Interface (4K)	1 HDMI Input/ 1 HDMI Output
E2PROM	64Kbit	SFP Interface	2
JTAG/UART	1 / 1	PCIE 2.0	x 2
SD Card Slot Interface	1	40P Expansion Ports	32 Single-ended/16 Differential Pairs
Gigabit Ethernet	1	FMC Interface	FMC HPC, IO All Connected



PZ-K7410T-FH-KFB

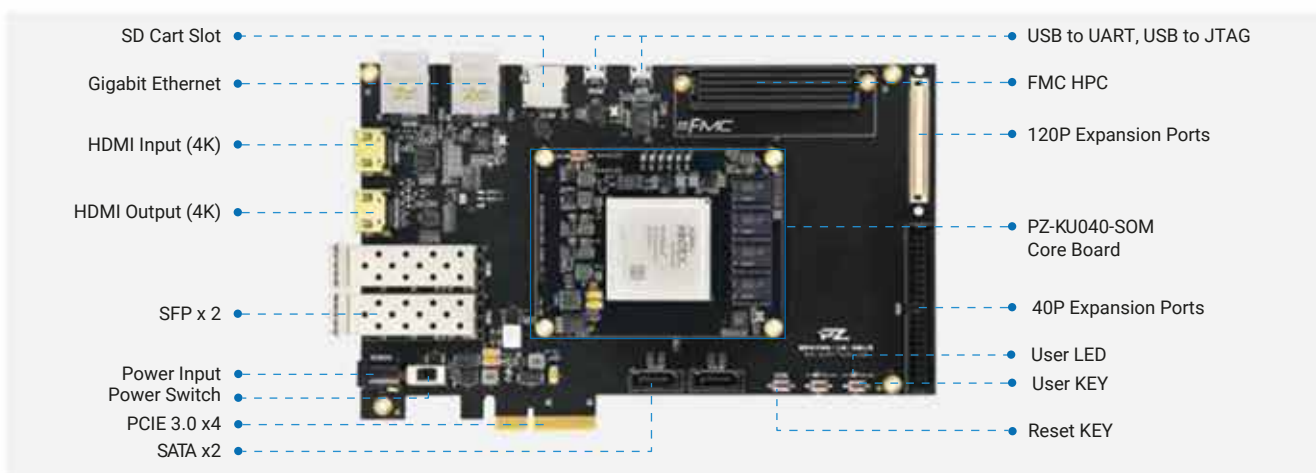
SPECIFICATION

FPGA Chip	XC7K410T-2FFG900I	LED	5 LEDs
DDR3	2GB /64bit	KEY	5 KEYS
QSPI FLASH	256Mb	HDMI Interface (4K)	1 HDMI Input/ 1 HDMI Output
E2PROM	64Kbit	SFP Interface	2
JTAG/UART	1/ 1	PCIE 2.0	x 2
SD Card Slot Interface	1	40P Expansion Ports	32 Single-ended/16 Differential Pairs
Gigabit Ethernet	1	FMC Interface	FMC HPC, IO All Connected



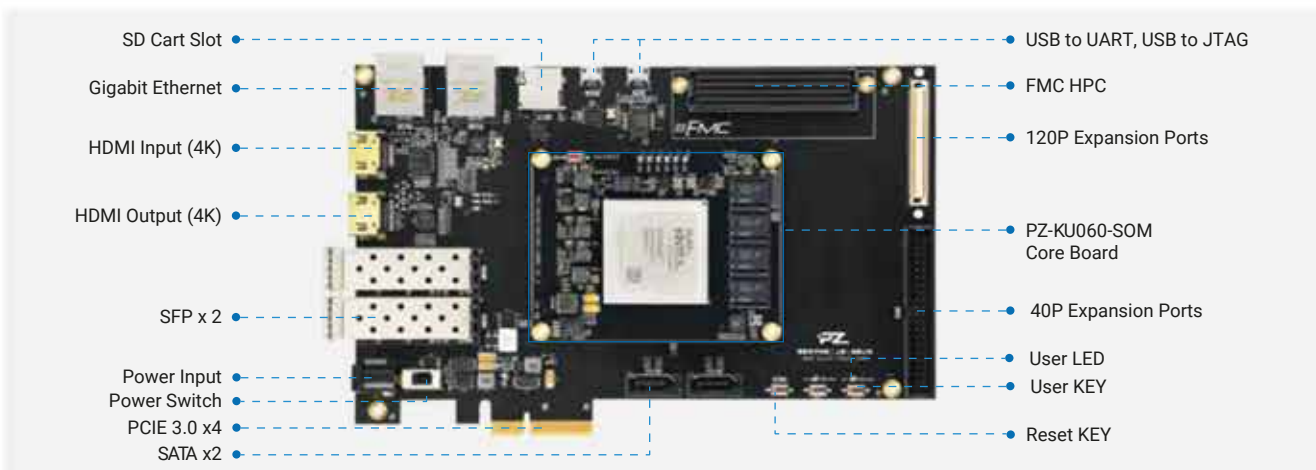
PZ-KU040-KFB

SPECIFICATION			
FPGA Chip	XCKU040-2FFVA1156I	HDMI (4K)	1 Input / Output, Support 4K/60 fps
DDR4	4GB/64bit	SFP Interface	2
QSPI FLASH	256MB + 256MB	PCIE 3.0	x 4
E2PROM	64Kb	40P Expansion Ports	32 Single-ended/16 Differential Pairs
JTAG/Uart/SD Cart Slot	1/ 1/ 1	120P Expansion Ports	48 Single-ended/24 Differential Pairs
Gigabit Ethernet	2	FMC Interface	FMC HPC, IO All Connected
LED/KEY	2/ 2	Board Form Factors	6.14 inch x 4.76 inch (156mm x 121mm)



PZ-KU060-KFB

SPECIFICATION			
FPGA Chip	XCKU060-2FFVA1156I	HDMI (4K)	1 Input / Output, Support 4K/60 fps
DDR4	4GB/64bit	SFP Interface	2
QSPI FLASH	256MB + 256MB	PCIE3.0	x 4
E2PROM	64Kb	40P Expansion Ports	32 Single-ended/16 Differential Pairs
JTAG/Uart/SD Cart Slot	1/ 1/ 1	120P Expansion Ports	48 Single-ended/24 Differential Pairs
Gigabit Ethernet	2	FMC Interface	FMC HPC, IO All Connected
LED/KEY	2/ 2	Board Form Factors	6.14 inch x 4.76 inch (156mm x 121mm)



PZ-KU040-FHT-KFB

SPECIFICATION			
FPGA Chip	XCKU040-2FFVA1156I	PCIE3.0	x 8 Interface
QSPI FLASH	256MB + 256MB	FMC Interface	1 FMC HPC, 2 FMC LPC
E2PROM	64Kb	Voltage/ Current	12V/3A
Start-up Mode	JTAG/QSPI, Default Boot Mode is QSPI	SOM Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
UART	1	Board Form Factors	6.50 inch x 4.25 inch (165mm x 108mm)
JTAG Downloader	1	Technology	Black Matte, Immersion Gold Process
Gigabit Ethernet	1		



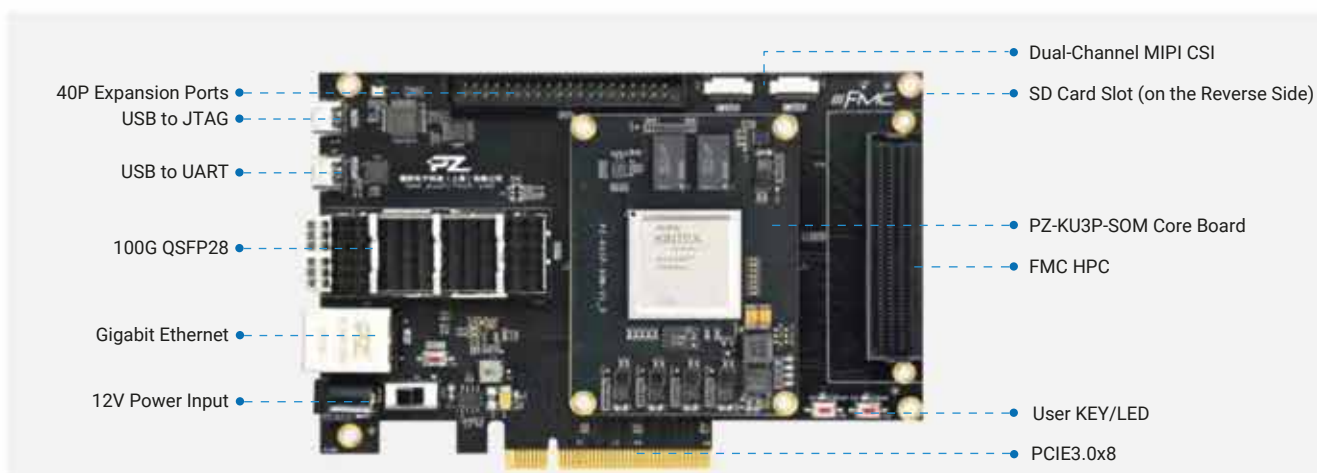
PZ-KU060-FHT-KFB

SPECIFICATION			
FPGA Chip	XCKU060-2FFVA1156I	PCIE3.0	x 8 Interface
QSPI FLASH	256MB + 256MB	FMC Interface	1 FMC HPC, 2 FMC LPC
E2PROM	64Kb	Voltage/ Current	12V/3A
Start-up Mode	JTAG/QSPI, Default Boot Mode is QSPI	SOM Form Factors	3.30 inch x 2.55 inch (83.8mm x 64.8mm)
UART	1	Board Form Factors	6.50 inch x 4.25 inch (165mm x 108mm)
JTAG Downloader	1	Technology	Black Matte, Immersion Gold Process
Gigabit Ethernet	1		



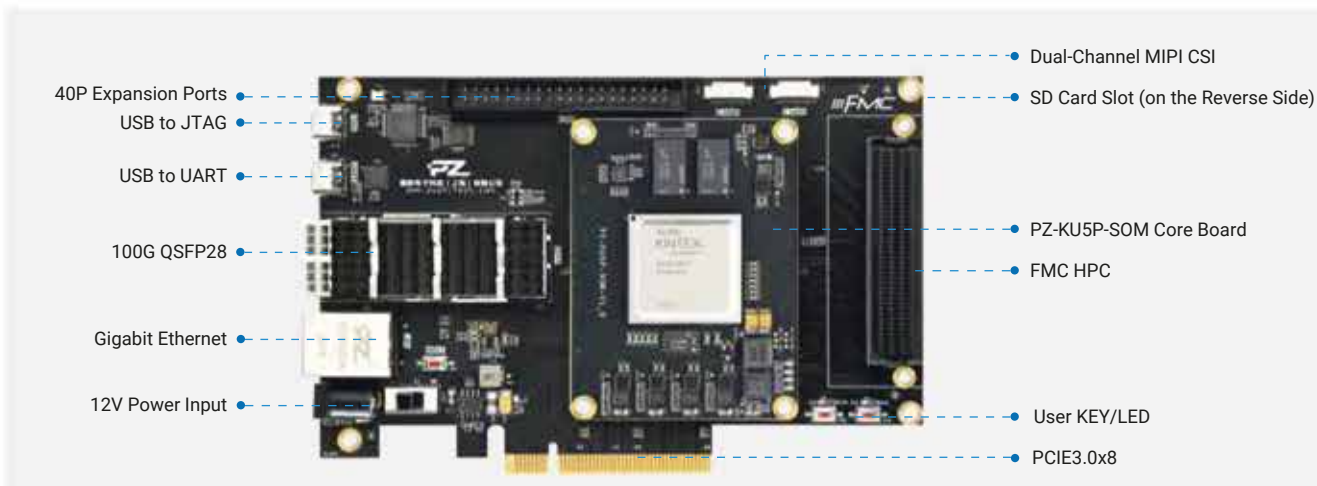
PZ- KU3P-KFB

SPECIFICATION			
FPGA Chip	XCKU3P-2FFVB676I	LED	2 LEDs
DDR4	2GB/32bit	KEY	2 KEYS
QSPI FLASH	256Mb	PCIE 3.0	x 8
E2PROM	64Kbit	QSFP28	1 (100G)
UART	1	40P Expansion Ports	1, 32 Single-ended/ 16 Differential Pairs
SD Card Slot Interface	1	FMC Interface	1 FMC HPC
Gigabit Ethernet	1	MIPI CSI	2



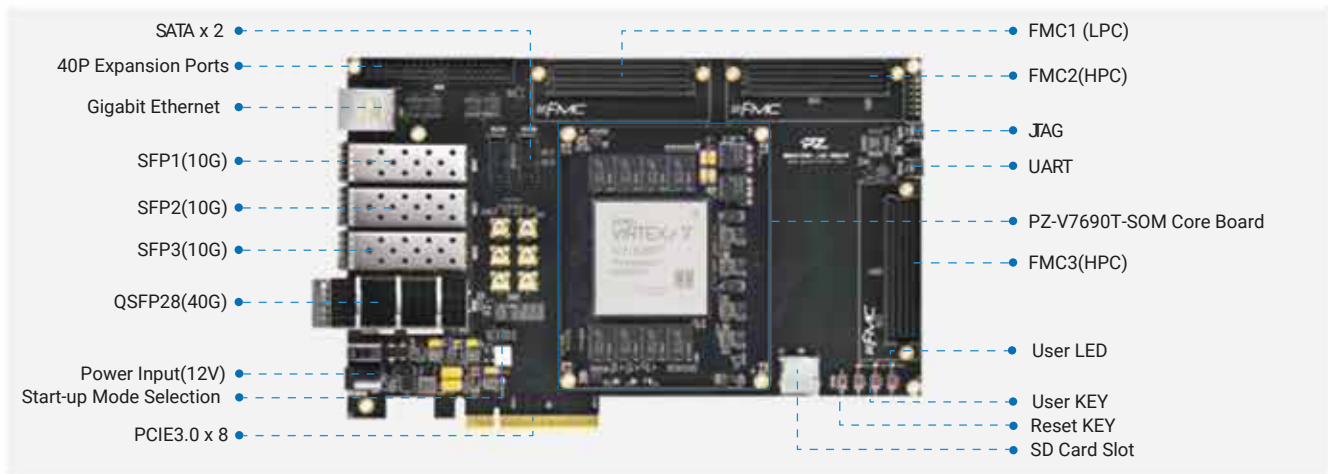
PZ- KU5P-KFB

SPECIFICATION			
FPGA Chip	XCKU5P-2FFVB676I	LED	2 LEDs
DDR4	2GB/32bit	KEY	2 KEYS
QSPI FLASH	256Mb	PCIE 3.0	x 8
E2PROM	64Kbit	QSFP 28	1 (100G)
UART	1	40P Expansion Ports	1, 32 Single-ended/ 16 Differential Pairs
SD Card Slot Interface	1	FMC Interface	1 FMC HPC
Gigabit Ethernet	1	MIPI CSI	2



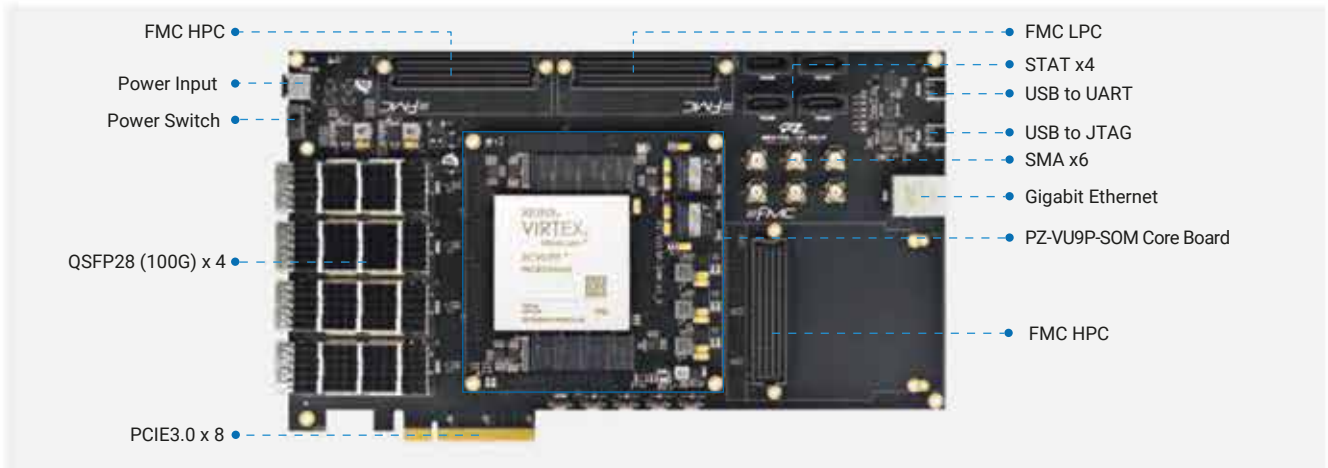
PZ-V7690T-KFB

SPECIFICATION			
FPGA Chip	XC7V690T-2FFG1761I	LED	2
DDR3	2GB/64bit + 2GB/64bit	KEY	2
QSPI FLASH/BPI FLASH	1Gbit/1Gbit	SFP Interface	3
E2PROM	64Kb	QSFP 28	1(40G)
FMC	1 LPC, 2 HPC	PCIE3.0	x 8
SD Card Slot Interface	1	40P Expansion Ports	32 Single-ended/16 Differential Pairs
Gigabit Ethernet	1	Board Form Factors	8.66 inch x 5.12 inch (220mm x 130mm)



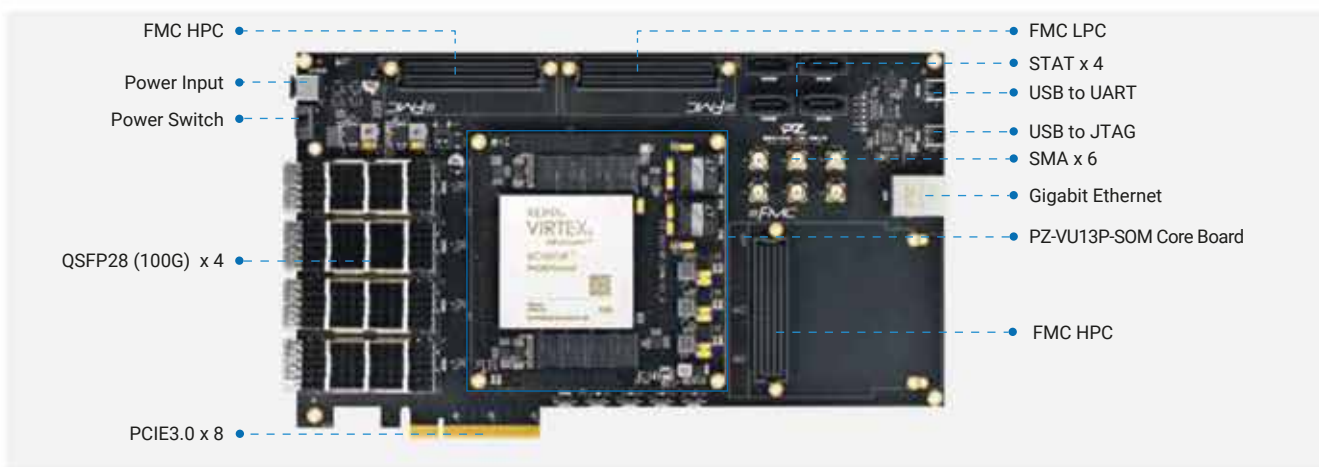
PZ-VU9P-KFB

SPECIFICATION			
FPGA Chip	XC7VU9P-2FLGB2104I	FMC Interface	2 HPC(8 pairs of GT, 168 IOs) LPC(1 pair of GT, 72 IOs)
DDR4	8GB DDR4 + 8GB DDR4	Voltage/ Current	12V/20A
JTAG Downloader	1	SOM Form Factors	3.94 inch x 3.94 inch (100mm x 100mm)
UART	1	Board Form Factors	10.08 inch x 5.51 inch (256mm x 140mm)
Gigabit Ethernet	1	Technology	Black Matte, Immersion Gold Process
PCIE3.0	x 8		
QSFP28	4 x100G		



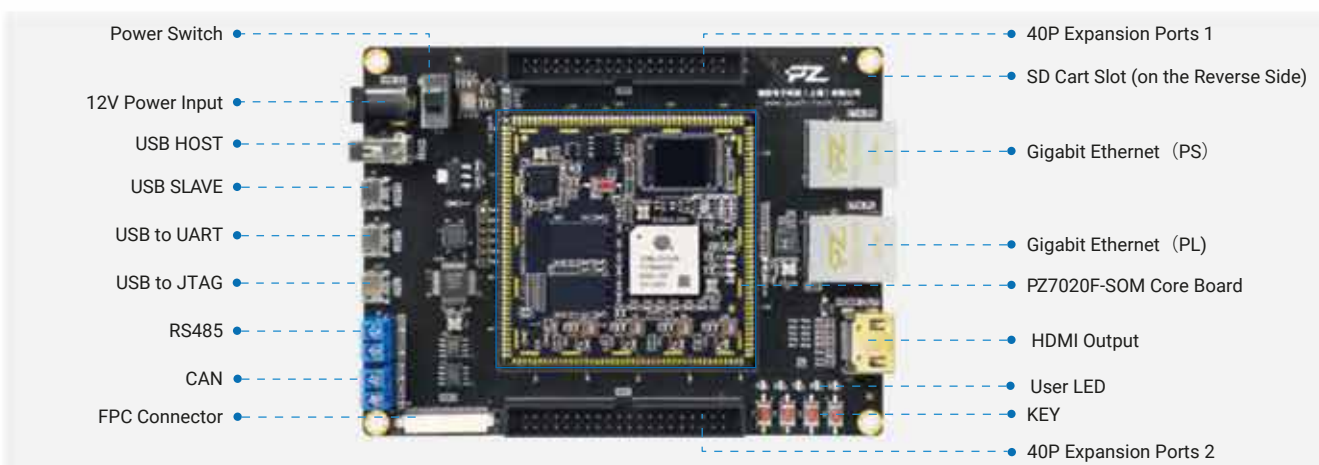
PZ-VU13P-KFB

SPECIFICATION			
FPGA Chip	XCVU13P-2FHGB2104I	FMC Interface	2 HPC(8 pairs of GT, 168 I/Os) LPC(1 pair of GT, 72 I/Os)
DDR4	8GB DDR4 + 8GB DDR4	Voltage/ Current	12V/ 20A
JTAG Downloader	1	SOM Form Factors	3.94 inch x 3.94 inch (100mm x 100mm)
UART	1	Board Form Factors	10.08 inch x 5.51 inch (256mm x 140mm)
Gigabit Ethernet	1	Technology	Black Matte, Immersion Gold Process
PCIE3.0	x8 Interface		
QSFP 28	4 x 100G		



PZ7020F-KFB

SPECIFICATION			
FPGA Chip	JFMQL20S400	Gigabit Ethernet	1 on PS Side/ 1 on PL Side
ARM Processor Core	Four Cortex-A7, Main Frequency 800Mhz	LED/KEY	5/ 4
DDR3/DD3L	1GB	HDMI	1
EMMC/QSPI FLASH	8G/128Mb	40P FPC Expansion Ports	24 Single-ended / 12 Differential Pairs
JTAG/UART/RS485/CAN	1/ 1/1/ 1	2 x 40P Expansion Ports	56 Single-ended / 27 Differential Pairs
SD Card Slot Interface	1	SOM Form Factors	2.40 inch x 2.40 inch (61mm x 61mm)
USB2.0 Host/ Slave	1/ 1	Board Form Factors	5.04 inch x 3.78 inch (128mm x 96mm)



P201Mini

SPECIFICATION			
RF Chip	AD9361	DDR3/QSPI FLASH	1GB /256Mb
Frequency Range	70M-6Ghz	Gigabit Ethernet	1
Signal Bandwidths	200K-56Mhz	Power Supply	TypeC/XH2.54 Power Supply Options
Power Amplifier	10-15dbm	UART/JTAG/SD	1/ 1/1
FPGA Chip	XC7Z020-2CLG400I	10M/PPS	1 Input
Processor Core	Dual Cortex-A9, 766Mhz	Expansion Port IOs	8 x 3.3V IOs, 2 x 1.8V IOs
Logic Cells	85K	Form Factors	3.78 inch x 2.17 inch x 0.91 inch (256mm x 140mm x 23mm)



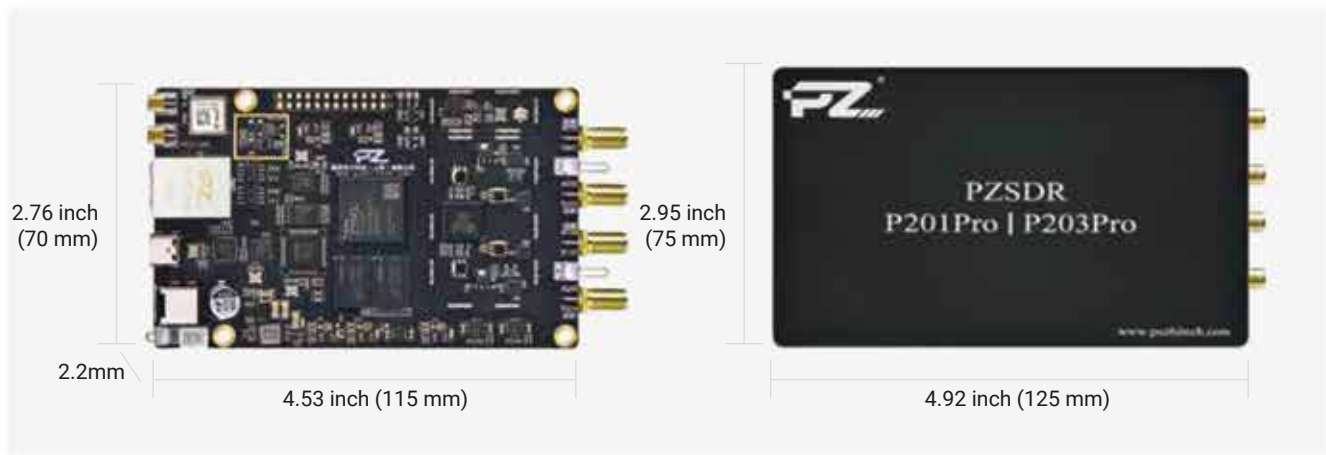
P203Mini

SPECIFICATION			
RF Chip	AD9363	DDR3/QSPI FLASH	1GB / 256Mb
Frequency Range	325M-3.8Ghz	Gigabit Ethernet	1
Signal Bandwidths	200K-20Mhz	Power Supply	TypeC/XH2.54 Power Supply Options
Power Amplifier	10-15dbm	UART/JTAG/SD	1/ 1/1
FPGA Chip	XC7Z020-2CLG400I	10M/PPS	1 Input
Processor Core	Dual Cortex-A9, 766Mhz	Expansion Port IOs	8 x 3.3V IOs, 2 x 1.8V IOs
Logic Cells	85K	Form Factors	3.78 inch x 2.17 inch x 0.91 inch (256mm x 140mm x 23mm)



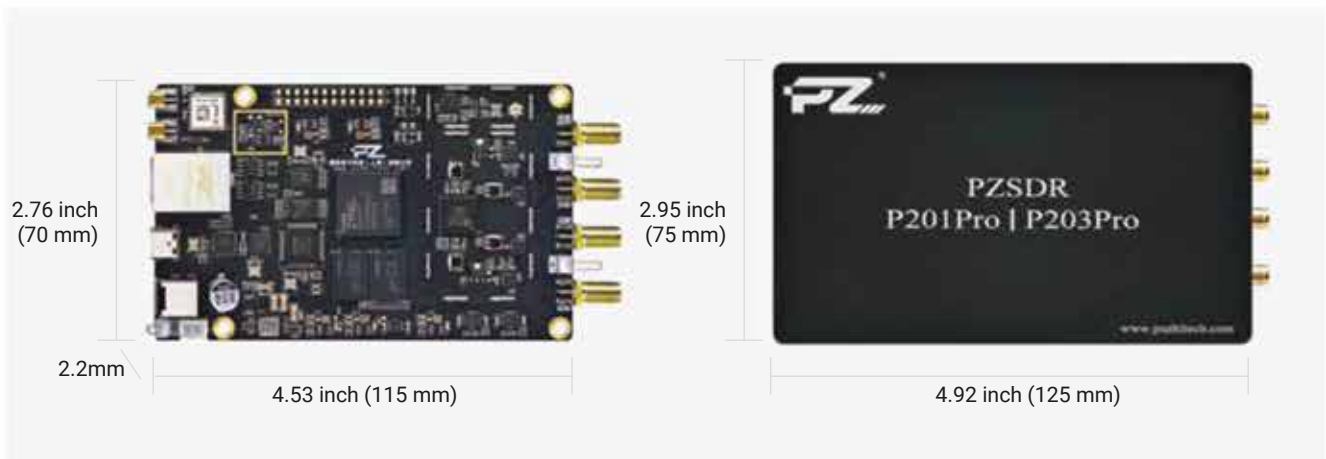
P201Pro

SPECIFICATION			
RF Chip	AD9361	RF Clock	40M VCTCXO(0.5PPM)/ipex Input External Input(Default is 40M VCTCXO)
Frequency Range	70M-6Ghz	Clock Input /Output	1 Input/ 1 Output
Signal Bandwidths	200K-56Mhz	JTAG/UART/USB OTG/SD	1/ 1/1/ 1
Power Amplifier	2TX	Gigabit Ethernet/ GPS	1/ 1
FPGA Chip	XC7Z020-2CLG400I	Expansion Port IOs	16 x3.3V IOs, 4 x 1.8V IOs
Processor Core	Dual Core Cortex-A9, 766Mhz	Power Supply	8-36V Ultra-wide Voltage Supply (Limit 50V)
DDR3/QSPI FLASH/E2PROM	1GB / 256Mb / 256Kb		



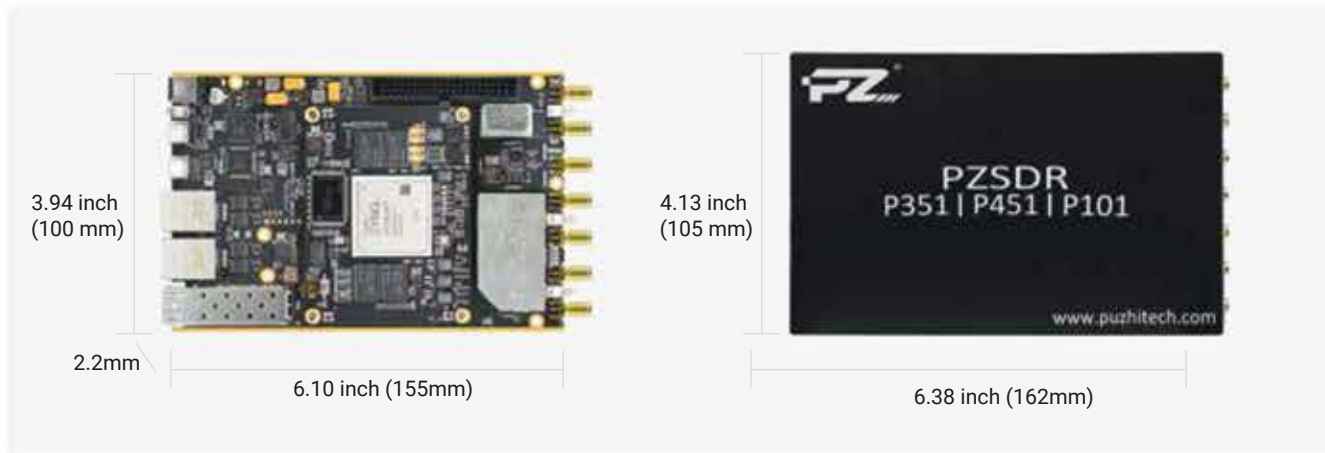
P203Pro

SPECIFICATION			
RF Chip	AD9363	RF Clock	40M VCTCXO(0.5PPM)/ipex Input External Input(Default is 40M VCTCXO)
Frequency Range	325M-3.8Ghz	Clock Input /Output	1 Input/ 1 Output
Signal Bandwidths	200K-20Mhz	JTAG/UART/USB/ SD	1/ 1/1/ 1
Power Amplifier	2TX	Gigabit Ethernet	1
FPGA Chip	XC7Z020-2CLG400I	GPS	1
Processor Core	Dual Core Cortex-A9,766Mhz	Power Supply	8-36V Ultra-wide Voltage Supply (Limit 50V)
DDR3/QSPI FLASH/E2PROM	1GB / 256Mb / 256Kb		



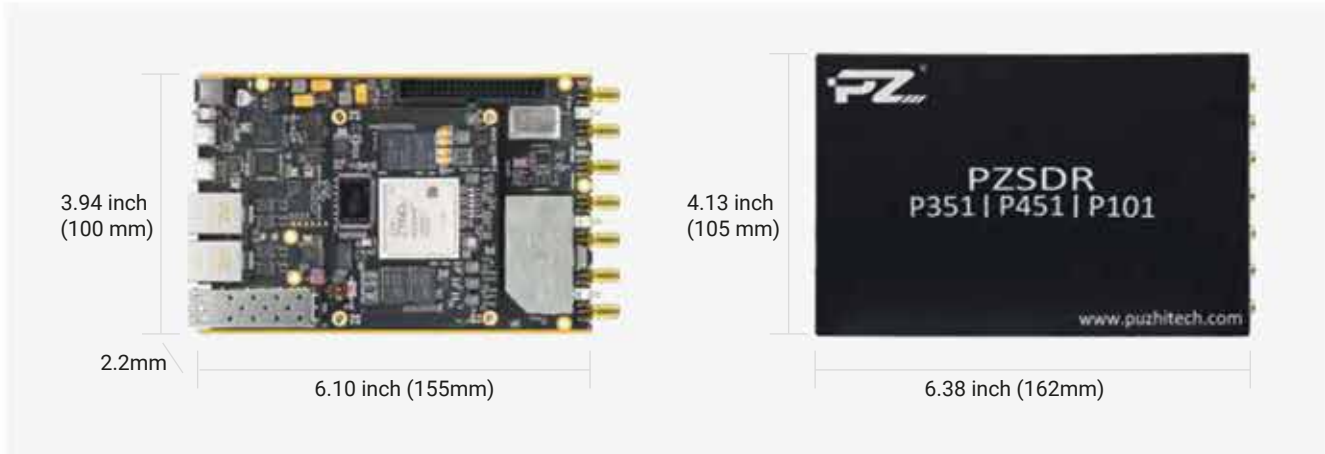
P351

SPECIFICATION			
FPGA Chip	XC7Z035-2FFG900I	EMMC	8GB
Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz	RF Clock	40M VCTCXO(0.5PPM)/ ipex Input/ External Input (Default 40M VCTCXO, OCXO available as an option)
RF Chip	AD9361	Clock Input/ Gigabit Ethernet	1 Clock on PS Side/ 1 Ethernet on PL Side
Frequency/ Signal	70M-6Ghz / 200K-56Mhz	USB 3.0/ USB 2.0 OTG	1 / 1
Power Amplifier	10-15dbm	UART/JTAG/SD Cart Slot/GPS	1/ 1/1/ 1
DDR3/DD3L	1GB on PS Side, 1GB on PL Side GB	Power Supply	12-36V
QSPI FLASH	256Mb		



P451

SPECIFICATION			
FPGA Chip	XC7Z045-2FFG900I	EMMC	8GB
Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz	RF Clock	40M VCTCXO(0.5PPM)/ ipex Input/ External Input (Default 40M VCTCXO, OCXO available as an option)
RF Chip	AD9361	Clock Input/ Gigabit Ethernet	1 Clock on PS Side/ 1 Ethernet on PL Side
Frequency/ Signal	70M-6Ghz / 200K-56Mhz	USB 3.0/ USB 2.0 OTG	1 / 1
Power Amplifier	10-15dbm	UART/JTAG/SD Cart Slot/GPS	1/ 1/1/ 1
DDR3/DD3L	1GB on PS Side, 1GB on PL Side GB	Power Supply	12-36V
QSPI FLASH	256Mb		



P101

SPECIFICATION			
FPGA Chip	XC7Z100-2FFG900I	EMMC	8GB
Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz	RF Clock	40M VCTCXO(0.5PPM)/ ipex Input/ External Input (Default 40M VCTCXO, 0CXO available as an option)
RF Chip	AD9361	Clock Input/ Gigabit Ethernet	1 Clock on PS Side/ 1 Ethernet on PL Side
Frequency/ Signal	70M-6Ghz / 200K-56Mhz	USB 3.0/ USB 2.0 OTG	1 /1
Power Amplifier	10-15dbm	UART/JTAG/SD Cart Slot/GPS	1/ 1/1/ 1
DDR3/DD3L	1GB on PS Side, 1GB on PL Side GB	Power Supply	12-36V
QSPI FLASH	256Mb		





**МАКРО
ГРУПП**

www.macrogroupp.ru

Официальный дистрибьютор

e-mail: fpga@macrogroupp.ru

тел.: +7 (800) 333-0605

