



Features

- RoHS compliant
- Compatible with 155 Mbps ATM and SONET OC-3 SDH STM-1
- Industry standard 1×9 footprint
- SC connector
- Single power supply 3.3 V
- Differential PECL inputs and outputs
- Compatible with solder and aqueous wash processes
- Class 1 laser product complies with EN 60825-1

Ordering Information

PART NUMBER	ТХ	RX	VOLTAGE	TEMPERATURE	LD TYPE
LSB2-A3L-PC-N5-57	1570 nm	1510 nm	3.3 V	0° C to 70° C	DFB
LSB2-A3L-PI-N5-57	1570 nm	1510 nm	3.3 V	-40 °C to 85 °C	DFB

Absolute Maximum Ratings

Absolute Maximum Raungs							
SYMBOL	MIN	MAX	UNITS	NOTE			
T_S	-40	85	°C				
Vcc	-0.5	4.0	V				
V_{IN}	-0.5	Vcc	V				
I_o		50	mA				
I_{OP}		400	mA				
T_{SOLD}		260	°C	10 seconds on leads			
	T _S Vcc V _{IN} I _o I _{OP}	$\begin{array}{c c} T_{S} & -40 \\ \hline Vcc & -0.5 \\ \hline V_{IN} & -0.5 \\ \hline I_{o} & \\ \hline I_{OP} & \end{array}$	T_S -40 85 Vcc -0.5 4.0 V_{IN} -0.5 Vcc I_o 50 I_{OP} 400	T_S -40 85 $^{\circ}$ C Vcc -0.5 4.0 V V_{IN} -0.5 Vcc V I_o 50 mA I_{OP} 400 mA			

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Operating Environment

PARAMETER	SYMBOL	MIN	MAX	UNITS	NOTE
Case Operating Temperature	T. T. T. T.		70	°c —	
Case Operating Temperature	T_C –	-40	85	с –	
Supply Voltage	Vcc	3.1	3.5	V	
Supply Current	$I_{TX} + I_{RX}$		300	mA	

Transmitter Electro-optical Characteristics

 $V cc = 3.1 \text{ V to } 3.5 \text{ V}, T_{\text{C}} = 0^{\circ} \text{C to } 70^{\circ} \text{C}(-40^{\circ} \text{C to } 85^{\circ} \text{C})$

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Data Rate	В	50	155	200	Mb/s	
Output Optical Power	Pout	-5		+2	dBm	Average
9/125 μm fiber	Тош	-5		+2	uDIII	Average
Extinction Ratio	ER	10			dB	
Center Wavelength	λ_C	1560	1570	1580	nm	
Spectral Width (-20dB)	$\Delta\lambda$			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Rise/Fall Time (10–90%)	$T_{r,f}$		1	2	ns	
Output Eye	Compliant v	vith Telcordi	ia GR-253-C	ORE Issue 3	and ITU-T re	commendation G-957
Data Input Current-Low	I_{IL}	-350			μA	
Data Input Current-High	I_{IH}			350	μA	
Transmitter Data Input Voltage-High	$V_{IH} - V_{CC}$	-1.1		-0.74	V	Note 1
Transmitter Data Input Voltage-Low	$V_{IL} - V_{CC}$	-2.0		-1.58	V	Note 1
Transmitter Data Input Differential Voltage	V _{DIFF}	0.3		1.6	V	Note 1

Note 1: These inputs are compatible with 10K, 10KH and 100K ECL and PECL input.

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Receiver Electro-optical Characteristics

$V cc = 3.1 \text{ V to } 3.5 \text{ V}, T_{\text{C}} = 0$	°C to 70	°C(-40	°C to 85 °C	C)
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PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Data Rate	В	50	155	200	Mb/s	
Optical Input Power-maximum	P_{IN}	0			dBm	Note 1
Optical Input Power-minimum (Sensitivity)	P _{IN}			-33	dBm	Note 1
Operating Center Wavelength	λ_C	1500		1520	nm	
Return Loss	RL			-14	dB	λ=1560~1580nm
Signal Detect-Asserted	P_A			-33	dBm	Average
Signal Detect-Deasserted	P_D	-45			dBm	Average
Signal Detect-Hysteresis	$P_A - P_D$	1.0			dB	
Signal Detect Output voltage-High	$V_{OH} - V_{CC}$	-1.1		-0.74	V	Note 2
Signal Detect Output voltage-Low	$V_{OL} - V_{CC}$	-2.0		-1.58	V	Note 2
Crosstalk	CRT			-45	dB	
Data Output Rise, Fall Time (10–90%)	$T_{r,f}$		1	2	ns	
Data Output Voltage-High	$V_{OH} - V_{CC}$	-1.1		-0.74	V	Note 2
Data Output Voltage-Low	$V_{OL} - V_{CC}$	-2.0		-1.58	V	Note 2

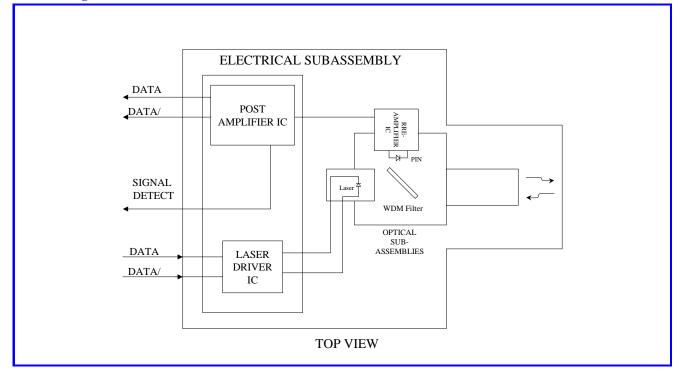
Note 1: The input data is at 155.52 Mbps, $2^{23} - 1$ PRBS data pattern with 72 "1"s and 72 "0"s inserted per the ITU-T recommendation G.958 Appendix 1. The receiver is guaranteed to provide output data with Bit Error Rate (BER) better than or equal to 1×10^{-10} .

Note 2: These outputs are compatible with 10K, 10KH and 100K ECL and PECL input.

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Block Diagram of Transceiver



Transmitter and Receiver Optical Sub-assembly Section

A 1570 nm InGaAsP laser and an InGaAs PIN photodiode integrate with an WDM filter to form a bi-directional single fiber optical subassembly (OSA). The laser of OSA is driven by a LD driver IC which converts differential input LVPECL logic signals into an analog laser driving current. And, The photodiode of OSA is connected to a circuit providing post-amplification quantization, and optical signal detection.

Receiver Signal Detect

Signal Detect is a basic fiber failure indicator. This is a single-ended LVPECL output. As the input optical power is decreased, Signal Detect will switch from high to low (deassert point) somewhere between sensitivity and the no light input level. As the input optical power is increased from very low levels, Signal Detect will switch back from low to high (assert point). The assert level will be at least 1.0 dB higher than the deassert level.

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Connection Diagram

Pin-Out			
	1. RX GND 2. RD+ 3. RD- 4. SD		
	5. VCCR 6. VCCT	TOP VIEW	
	7. TD- 8. TD+	O ^{N/C}	
	9. TX GND	Ŭ	

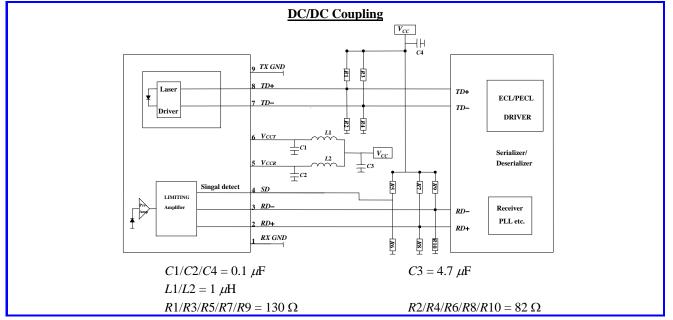
PIN	SYMBOL	DESCRIPTION
1	RX GND	Receiver Signal Ground, Directly connect this pin to the receiver ground plane
2	RD+	<i>RD</i> + is an open-emitter output circuit. Terminate this high-speed differential LVPECL output with standard LVPECL techniques at the follow-on device input pin. (See recommended circuit schematic)
3	RD-	<i>RD</i> – is an open-emitter output circuit. Terminate this high-speed differential LVPECL output with standard LVPECL techniques at the follow-on device input pin. (See recommended circuit schematic)
4	SD	Signal Detect. Normal optical input levels to the receiver result in a logic "1" output, V_{OH} , asserted. Low input optical levels to the receiver result in a fault condition indicated by a logic "0" output V_{OL} , deasserted Signal Detect is a single-ended LVPECL output. <i>SD</i> can be terminated with LVPECL techniques via 50 Ω to $V_{CCR} - 2$ V. Alternatively, <i>SD</i> can be loaded with a 180 Ω resistor to <i>RX GND</i> to conserve electrical power with small compromise to signal quality. If Signal Detect output is not used, leave it open-circuited. This Signal Detect output can be used to drive a LVPECL input on an upstream circuit, such as, Signal Detect input or Loss of Signal-bar.
5	V _{CCR}	Receiver Power Supply Provide +3.3 Vdc via the recommended receiver power supply filter circuit. Locate the power supply filter circuit as close as possible to the V_{CCR} pin.
6	V _{CCT}	Transmitter Power Supply Provide +3.3 Vdc via the recommended transmitter power supply filter circuit. Locate the power supply filter circuit as close as possible to the V_{CCT} pin.
7	TD-	Transmitter Data In-Bar Terminate this high-speed differential LVPECL input with standard LVPECL techniques at the transmitter input pin. (See recommended circuit schematic)
8	TD+	Transmitter Data In Terminate this high-speed differential LVPECL input with standard LVPECL techniques at the transmitter input pin. (See recommended circuit schematic)
9	TX GND	Transmitter Signal Ground Directly connect this pin to the transmitter signal ground plane. Directly connect this pin to the transmitter ground plane.

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RoHS compliant TX-1570/RX-1510 nm Single-mode, Single-Fiber (Bi-directional) Transceiver 1×9, SC Simplex Connector, 3.3 V (100km) 155 Mbps ATM/SONET OC-3/SDH STM-1/Fast Ethernet

Recommended Circuit Schematic



In order to get proper functionality, a recommended circuit is provided in above recommended circuit schematic. When designing the circuit interface, there are a few fundamental guidelines to follow.

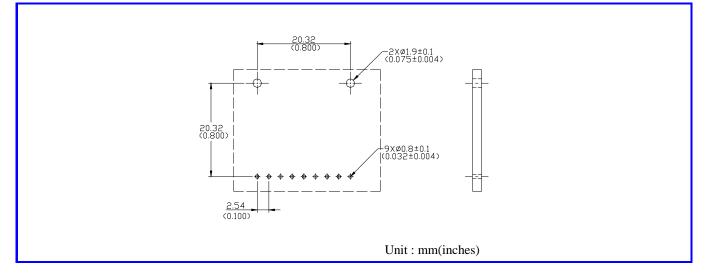
- (1) The differential data lines should be treated as 50 Ω Micro strip or strip line transmission lines. This will help to minimize the parasitic inductance and capacitance effects. Locate termination at the received signal end of the transmission line. The length of these lines should be kept short and of equal length.
- (2) For the high speed signal lines, differential signals should be used, not single-ended signals, and these differential signals need to be loaded symmetrically to prevent unbalanced currents which will cause distortion in the signal.
- (3) Multi layer plane PCB is best for distribution of V_{CC} , returning ground currents, forming transmission lines and shielding, Also, it is important to suppress noise from influencing the fiber-optic transceiver performance, especially the receiver circuit.
- (4) A separate proper power supply filter circuits shown in Figure for the transmitter and receiver sections. These filter circuits suppress V_{cc} noise over a broad frequency range, this prevents receiver sensitivity degradation due to V_{CC} noise.
- (5) Surface-mount components are recommended. Use ceramic bypass capacitors for the 0.1 μ F capacitors and a surface-mount coil inductor for 1 μ H inductor. Ferrite beads can be used to replace the coil inductors when using quieter V_{CC} supplies, but a coil inductor is recommended over a ferrite bead. All power supply components need to be placed physically next to the V_{CC} pins of the receiver and transmitter.
- (6) Use a good, uniform ground plane with a minimum number of holes to provide a low-inductance ground current return for the power supply currents.

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Recommended Board Layout Hole Pattern



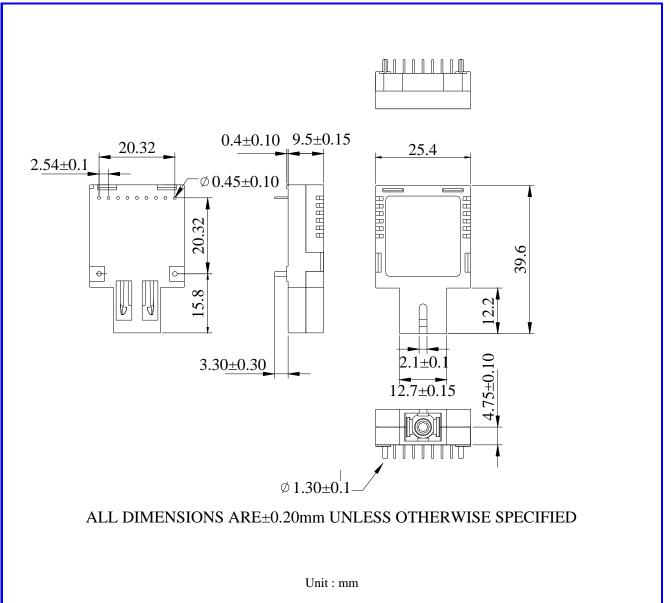
This transceiver is compatible with industry standard wave or hand solder processes. After wash process, all moisture must be completely remove from the module. The transceiver is supplied with a process plug to prevent contamination during wave solder and aqueous rinse as well as during handling, shipping or storage.

Solder fluxes should be water-soluble, organic solder fluxes. Recommended cleaning and degreasing chemicals for these transceivers are alcohol's (methyl, isopropyl, isobutyl), aliphatics (hexane, heptane) and other chemicals, such as soap solution or naphtha. Do not use partially halogenated hydrocarbons for cleaning/degreasing.

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Drawing Dimensions

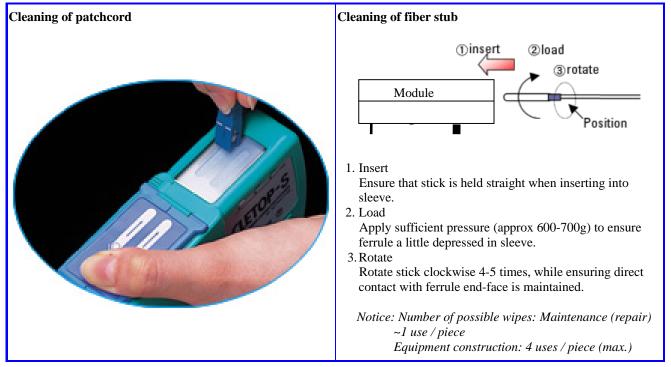


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Optical Receptacle Cleaning Recommendations

All fiber stubs inside the receptacle portions were cleaned before shipment. In the event of contamination of the optical ports, the recommended cleaning process is the use of forced nitrogen. If contamination is thought to have remained, the optical ports can be cleaned using a NTT international Cletop® stick type and HFE7100 cleaning fluid. Before the mating of patchcord, the fiber end should be cleaned up by using Cletop® cleaning cassette.



Note: The pictures were extracted from NTT-ME website. And the Cletop® is a trademark registered by NTT-ME

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Eye Safety Mark

The LSB series Single mode transceiver is a class 1 laser product. It complies with EN 60825-1 and FDA 21 CFR 1040.10 and 1040.11. In order to meet laser safety requirements the transceiver shall be operated within the Absolute Maximum Ratings.

Caution

All adjustments have been done at the factory before the shipment of the devices. No maintenance and user serviceable part is required. Tampering with and modifying the performance of the device will result in voided product warranty.

Required Mark

Class 1 Laser Product Complies with 21 CFR 1040.10 and 1040.11

Note : All information contained in this document is subject to change without notice.

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