Shenzhen Leadtek Electronics Co.,Ltd

PRODUCT SPECIFICATION TFT-LCD MODULE

Module No: LTK050WVNCT05-V0

- ☑ Preliminary Specification
- ☐ Approval Specification

Designed by	Checked by	Approved by
jona	Tom	lan

Final Approval by Customer

Approved by	Comment

**The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

Revision Record

REV NO.	REV DATE	CONTENTS	Note
V0	2023-2-16	NEW ISSUE	

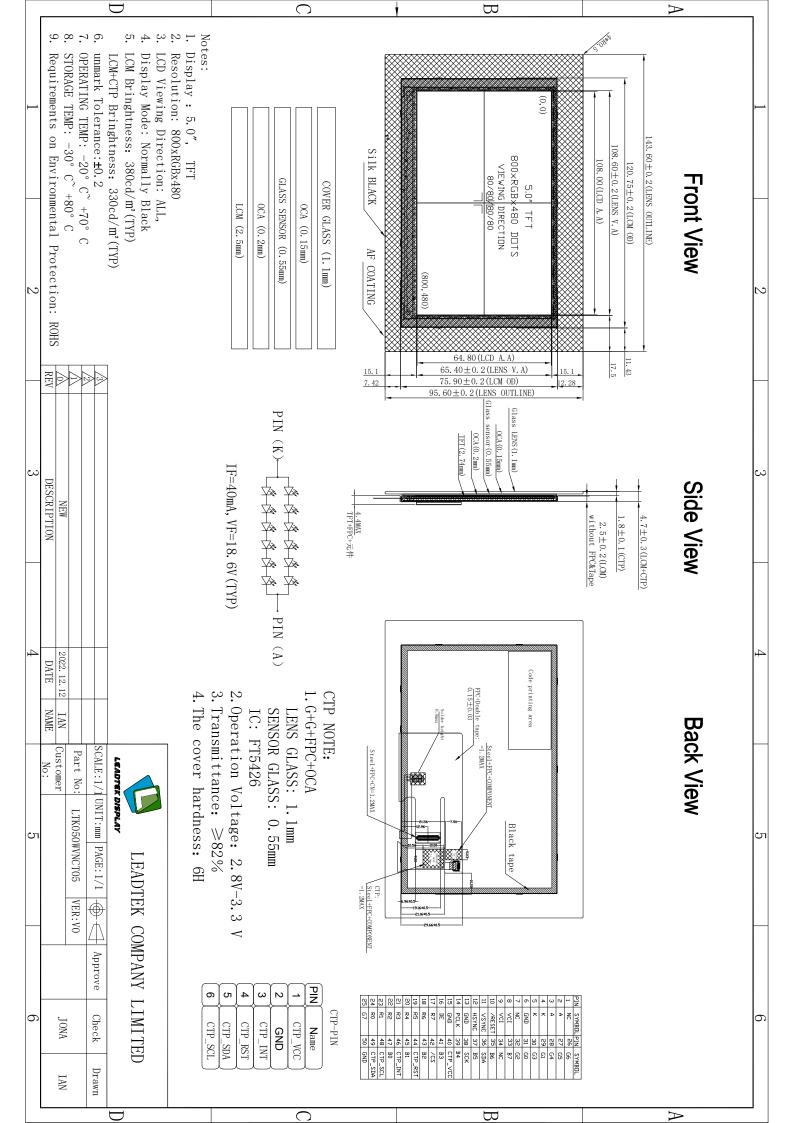
1. Numbering System

2. TFT General Information

ITEM	STANDARD VALUES	UNITS
LCD type	5.0"TFT	
Dot arrangement	800(RGB)×480	dots
Color filter array	RGB vertical stripe	
Display mode	IPS / Transmissive / Normally Black	
Viewing Direction	80/80/80	
LCM+CTP Outline Dimension	143.60(W)×95.60(H)×4.70(T)	mm
Active area	108.00(W)×64.80(H)	mm
Dot pitch	0.08625(H) x 0.08625(V)	mm
Interface	3line SPI + 24bit RGB	

3. External Dimensions







4. Interface Description

PIN NO.	PIN NAME	DESCRIPTION
1	NC	NC
2	А	Power supply for backlight anode input terminal.
3	А	Power supply for backlight anode input terminal.
4	K	Power supply for backlight cathode input terminals.
5	K	Power supply for backlight cathode input terminals.
6	GND	Ground
7	NC	NC
8	VCI	TFT and CTP power supply input.
9	VCI	TFT and CTP power supply input.
10	/RESET	Reset signal input terminal, active at 'L'.
11	VSYNC	Vertical Sync Input
12	HSYNC	Horizontal Sync Input
13	GND	Ground
14	PCLK	Dot Data Clock
15	GND	Ground
16	DE	Data Enable Input
17	R7	
18	R6	
19	R5	
20	R4	Dod data hus
21	R3	Red data bus.
22	R2	
23	R1	
24	R0	
25	G7	
26	G6	
27	G5	
28	G4	Croop data hua
29	G1	Green data bus.
30	G3	
31	G0	
32	G2	



33	В7	Blue data bus.
34	NC	NC
35	B6	Blue data bus.
36	SDA	SPI Interface Data.
37	B5	Blue data bus.
38	SCL	SPI Interface Data Clock.
39	B4	Blue data bus.
40	NC	NC
41	В3	Blue data bus.
42	/CS	Chip select signal, Active "L"
43	B2	Blue data bus.
44	CTP_RST	CTP reset line.
45	B1	Blue data bus.
46	CTP_INT	CTP interrupt line.
47	В0	Blue data bus.
48	CTP_SCL	CTP I2C clock line.
49	CTP_SDA	CTP I2C data line.
50	GND	Ground

5. Absolute Maximum Ratings

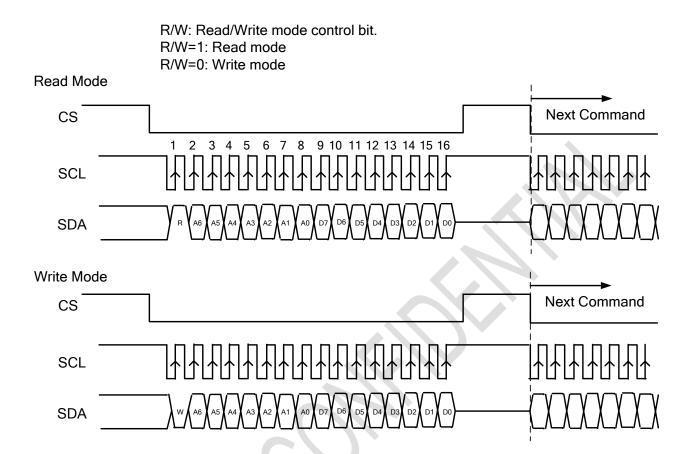
Item	Symbol	Min.	Max.	Unit
Analog Supply Voltage	VCI	-0.3	3.6	V
Input Voltage	Vin	-0.3	VCI+0.5	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	20	90	%RH

6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Analog Supply Voltage	VCI	2.5	2.8	3.3	٧	-
Input High Voltage	V _{IH}	0.7VCI	-	VCI	٧	Digital input pins
Input Low Voltage	V _{IL}	GND	-	0.3VCI	٧	Digital input pins
Output High Voltage	V _{OH}	0.8VCI	-	VCI	٧	Digital output pins
Output Low Voltage	V _{OL}	GND	-	0.2VCI	٧	Digital output pins
I/O Leak Current	ILI	-1.0	-	1.0	uA	-

7. Timing Characteristics

7.1 3-wire Serial Interface



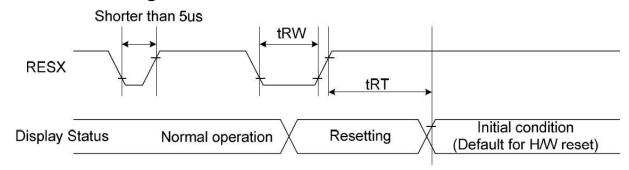
- **a.** Each serial command consists of 16 bits of data which is loaded one bit a time at the rising edge of serial clock SCL.
- **b.** Command loading operation starts from the falling edge of CS and is completed at the next rising edge of CS.
- c. The serial control block is operational after power on reset, but commands are established by the VSYNC signal. If command is transferred multiple times for the same register, the last command before the VSYNC signal is valid.
- d. If less than 16 bits of SCL are input while CS is low, the transferred data is ignored.
- **e.** If 16 bits or more of SCL are input while CS is low, the previous 16 bits of transferred data before then rising edge of CS pulse are valid data.
- f. Serial block operates with the SCL clock.
- **g.** Serial data can be accepted in the power save mode.
- h. After power on reset or GRB reset, it is required 100ms delay to begin SPI communication.

7.2. Parallel 24-bit RGB Input Timing Table

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C).

Parallel 24-bit RGB Interface Timing Table							
	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLk	Frequency	Fclk	23	25	27	MHz	
	Period Time	Th	808	816	848	DCLK	
	Display Period	Thdisp		800		DCLK	
HSYNC	Back Porch	Thbp	4	8	24	DCLK	
	Front Porch	Thfp	4	8	24	DCLK	
	Pulse Width	Thw	2	4	8	DCLK	
	Period Time	Tv	496	512	528	HSYNC	
	Display Period	Tvdisp		480		HSYNC	
VSYNC	Back Porch	Tvbp	8	16	24	HSYNC	
	Front Porch	Tvfp	8	16	24	HSYNC	
	Pulse Width	Tvw	2	4	8	HSYNC	

7.3 Reset Timing Characteristics



Signal	Symbol	Parameter	Min	Max	Unit
	tRW	Reset pulse duration	10		uS
RESX	tRT Reset cancel	Deset sensel		5 (note 1,5)	mS
		Reset cancer		120 (note 1,6,7)	mS

8. Backlight Charasterics

IF=40mA, VF=18.6V (TYP)

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	-	18.6	-	V	If=40mA
Supply Current	If	-	40	-	mA	1
Luminous Intensity	-	-	330	-	Cd/m ²	If=40mA
Uniformity for LCM	-	80	-	-	%	If=40mA
Life Time	-	30000	-	-	Hr	If=40mA
Backlight Color				White		

9. Optical Characteristics

<Table 1. Optical Specifications>

 $[Ta=25\pm2^{\circ}C]$

Para	meter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		70	80	-	Deg.	
Viewing	TIONZONIAI	Θ_9	CR > 10	70	80	ı	Deg.	Note 4.1
Angle Range	Vertical	Θ ₁₂	CK > 10	70	80	ı	Deg.	Note 4.1
	vertical	Θ_6		70	80	1	Deg.	
Contra	st Ratio	CR		1000	1200	-		w/o APF
Cell Trar	nsmittance	Tr	Θ = 0°	4.15	4.9	1	%	@C Light Note 4.2/4.3
		Rx		0.595	0.625	0.655		
		Ry		0.283	0.313	0.343		
		Gx		0.282	0.312	0.342		
Repro	duction	Gy	Θ = 0°	0.534	0.564	0.594		With
of (color	Bx	9-0	0.111	0.141	0.171		@C Light Note 4.4
		Ву		0.113	0.143	0.173		
				0.278	0.308	0.338		
		Wy		0.309	0.339	0.369		
Color Gamut			Θ = 0°	50	55	-	%	
Respor	nse Time	Tr+Tf	Ta= 25° C Θ = 0°	-	30	40	ms	Note 4.5

Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIG.2).
- 2. Contrast measurements shall be made at viewing angle of Θ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIG. 2) Luminance Contrast Ratio (CR) is defined mathematically. CR = White Luminance (ON) / Black Luminance (OFF)
- 3. Transmittance is the value with DBEF Polarizer.
- 4. The color chromaticity coordinates specified in Table1 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the C/F. Measurement condition is C light source & Halogen Lamp
- 5. The electro-optical response time measurements shall be made as FIG.3 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr , and 90% to 10% is Tf.

Figure 1. The definition of Vth & Vsat

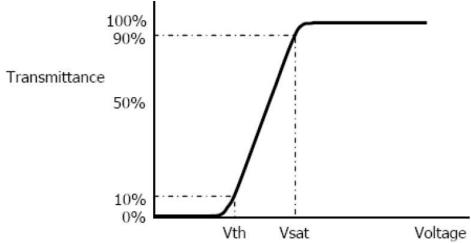


Figure 2. Measurement Set Up

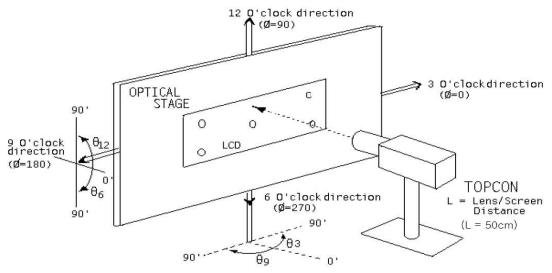
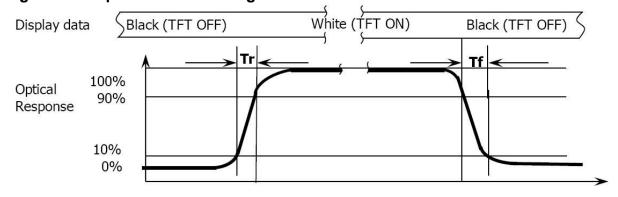


Figure 3. Response Time Testing



10. Reliability Test Conditions And Methods

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST		
1	High Temperature Storage	80℃±2℃×96Hours			
2	Low Temperature Storage	-30℃±2℃×96Hours			
3	High Temperature Operating	70℃±2℃×96Hours	Inspection after 2~4hours		
4	Low Temperature Operating	-20℃±2℃×96Hours	storage at room temperature, the samples should be free from defects: 1, Air bublle in the LCD. 2, Sealleak. 3, Non-display. 4, Missing segments.		
(5)	Temperature Cycle(Storage)	$ \begin{array}{c} -10^{\circ} C \iff 25^{\circ} C \iff 60^{\circ} C \\ (30min) & (5min) & (30min) \\ \hline & 1 \text{cycle} \\ \hline & Total 10 \text{cycle} \end{array} $			
6	Damp Proof Test (Storage)	50℃±5℃×90%RH×96Hours	5,Glass crack. 6,Current IDD is twice higher than initial value.		
7	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5M X,Y,Z direction for total 3hours (Packing Condition)	7,The surface shall be free from damage. 8,The electric charateristic requirements shall be		
8	Drooping Test	Drop to the ground from 1M height one time every side of carton. (Packing Condition)	satisfied.		
9	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times			

REMARK:

- 1,The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3,For Damp Proof Test,Pure water(Resistance $> 10M\Omega$)should be used.
- 4,In case of malfunction defect caused by ESD damage,if it would be recovered to normal state after resetting,it would be judge as a good part.
- 5,EL evaluation should be excepted from reliability test with humidity and temperature:Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11. Inspection Standard

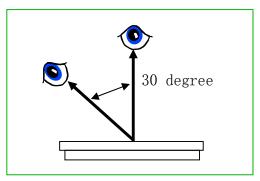
This standard apply to C-STN/TFT module

1. Spot check plan:

According to spot check level II, MIL-STD-105D Level II, the rank of accept or reject is below:

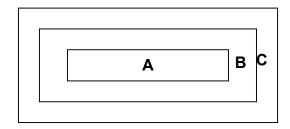
A 级: major non-conformance: AQL 0.65 minor non-conformance: AQL 1.

2. Inspection condition:



Under daylight lamp 20 $\sim\!40W_{\odot}$ product distance inspector'eye 30cm,incline degree 30° $_{\circ}$

3. LCD area define:



Area A: display area

Area B: VA area

Area C: out of VA area, not in sight after assemby

Remark :non-conformance at area C,but is OK that isn't influence raliability of product & assembly by customer.

4. Inspection standard

4.1 Major non-conformance

NO.	Item Inspection standard		Rate	
4.1.1	Function non-confor mance	 No display, display abnormaly Miss line, short B/L no function or function abnormaly TP no function 	major	
4.1.2	miss	No matter miss what component		
4.1.3	Out of size	Module dimension out of spec		

4.2 Appearance non-conformance

NO.	Item	Inspection standard R						Rate	
	Black or white spot (power on)	dot non-conformance define Φ $\Phi = \frac{(x+y)}{2}$							
		A grade							
		area size (mm)		Most approve q'ty					
				Α		В	С		
4.2.1		Ф≤0.10			ignore				
		0.10<Φ≤0.15		3					
		0.15<Φ≤	0.15<Φ≤0.20 2 ignore		e				
		0.20<Φ≤0.25			1				
		0.25<Ф		0					
		Most approve 4 damages, dot to dot ≥10mm							
	Black or white line (power on)	A grade							
		Size	Size(mm)		Most approve q			q'ty	
		L(length)	W(w	ridth)	Α		В	С	
		ignore	W≤0.03		ignore		ignore	Minor	
4.2.2		L≤5.0	0.03< W≤0.05		2				
		L≤3.0	0.05< W≤0.07		1				
			0.07 <w< td=""><td colspan="2">Treat with dot non-conformance</td></w<>		Treat with dot non-conformance				
		Most approve 3 damages, line to line ≥10mm							
4.2.3	Polarizer position					Minor			



4.2.4	LCD non-conf ormance	X ≤3. Crash di (ii) commonly sur X ≤2.0 (iii) crack Disallow exten	sallow extend to ITO face scathe Y <pre></pre>	Z ignore	Minor		
4.2.5	Contrast		rack		Minor		
	voltage arp	VOP/VIcd voltage o	VOP/VIcd voltage of confirmed sample±0.15V				
4.2.6	color	Color & luminance	Color & luminance of module scope reference spec				
4.2.7	Cross talk	Reference confirmed limit sample			Minor		

12. Handling Precautions

12.1 Mounting method

The LCD panel of LTK LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl), Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

12.4 packing

- Module employ LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.



12.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it.
 And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

13. Precaution For Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to LTK LCD, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method

TBD