RoHS compliant
TX-1550/RX-1310 nm Multi-mode Bi-directional (550m)
SFP LC Simplex Connector, with Diagnostic Monitoring
1.0625Gbd Fiber Channel/1.25 Gigabit Ethernet

Features

- Industry standard small form pluggable (SFP) package
- Simplex LC connector
- Differential LVPECL inputs and outputs
- Single power supply 3.3V
- TTL signal detect indicator
- Hot Pluggable
- Class 1 laser product complies with EN 60825-1

Ordering Information

PART NUMBER	TX/RX	INPUT/OUTPUT	SIGNAL DETECT	TEMPERATURE	LD Type	Distance
LM48-C3S-TC-N-D5	1550/1310	AC/AC	TTL	0°C to 70 °C	1550 FP	550m
LM48-C3S-TI-N-D5	1550/1310	AC/AC	TTL	-40°C to 85 °C	1550 FP	550m

Diagnostics

Parameter	Range	Accuracy	Unit	Calibration
Temperature	-40 to 95	± 3	°C	External
Voltage	3.0 to 3.6	± 0.1	V	
Bias Current	0 to 90	± 10%	mA	
TX Power	-10 to +2	± 3 dB	dBm	
RX Power	-18 to 0	± 3 dB	dBm	

Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNITS	NOTE
Storage Temperature	T_S	–40	85	°C	
Supply Voltage	*Vcc*	–0.5	4.0	V	
Input Voltage	V_{IN}	–0.5	*Vcc*	V	

Recommended Operating Conditions

PARAMETER	SYMBOL	MIN	MAX	UNITS	NOTE
Case Operating Temperature	T_C	0 -40	70 85	°C	
Supply Voltage	*Vcc*	3.1	3.5	V	
Supply Current	$I_{TX} + I_{RX}$	---	250	mA	

Transmitter Electro-optical Characteristics

***Vcc* = 3.1 V to 3.5 V, T_C = 0 °C to 70 °C (-40 °C to 85 °C)**

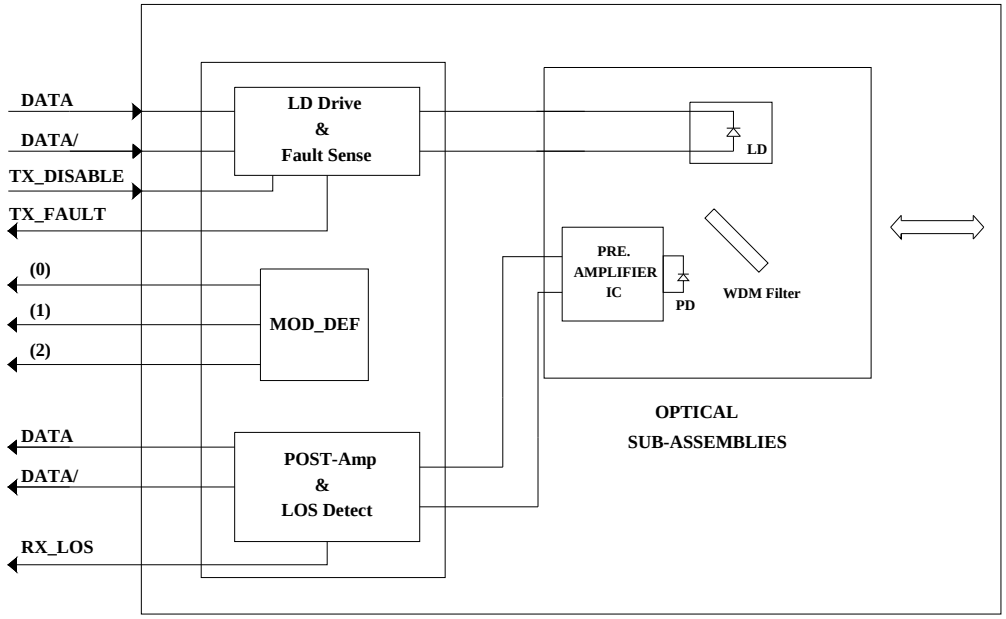
PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Output Optical Power 62.5/125 μm fiber	P_{out}	–8	---	0	dBm	Average
Extinction Ratio	*ER*	9	---	---	dB	
Center Wavelength	λ_C	1480	1530	1580	nm	
Spectral Width (RMS)	$\Delta\lambda$	---	---	4.0	nm	
Rise/Fall Time, (20–80%)	$T_{r,\,f}$	---	---	260	ps	
Total Jitter	*TJ*	---	---	227	ps	
Output Eye			Compliant with IEEE802.3z			
Max. P_{out} TX-DISABLE Asserted	P_{OFF}	---	---	–45	dBm	
Differential Input Voltage	V_{DIFF}	0.4	---	2.0	V	

Receiver Electro-optical Characteristics

Vcc = 3.1 V to 3.5 V, T_C = 0 °C to 70 °C (-40 °C to 85 °C)

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNITS	NOTE
Optical Input Power-maximum	P_{IN}	0	---	---	dBm	BER < 10^{-12}
Optical Input Power-minimum (Sensitivity)	P_{IN}	---	---	–18	dBm	BER < 10^{-12}
Operating Center Wavelength	λ_C	1260	---	1360	nm	
Optical Return Loss	ORL	14	---	---	dB	λ=1260~1360nm
Optical isolation	ISO	---	---	-40	dB	λ=1480~1580nm
Signal Detect-Asserted	P_A	---	---	–18	dBm	
Signal Detect-Deasserted	P_D	–35	---	---	dBm	
Differential Output Voltage	V_{DIFF}	0.5	---	1.2	V	
Data Output Rise, Fall Time (20–80%)	$T_{r,f}$	---	---	0.35	ns	
Receiver Loss of Signal Output Voltage-Low	RX_LOS_L	0	---	0.5	V	
Receiver Loss of Signal Output Voltage-High	RX_LOS_H	2.4	---	V_{CC}	V	

Block Diagram of Transceiver



Transmitter and Receiver Optical Sub-assembly Section

A 1550 nm InGaAsP laser and an InGaAs PIN photodiode integrate with an WDM filter to form a bi-directional single fiber optical subassembly (OSA). The laser of OSA is driven by a LD driver IC which converts differential input LVPECL logic signals into an analog laser driving current. And, The photodiode of OSA is connected to a circuit providing post-amplification quantization, and optical signal detection.

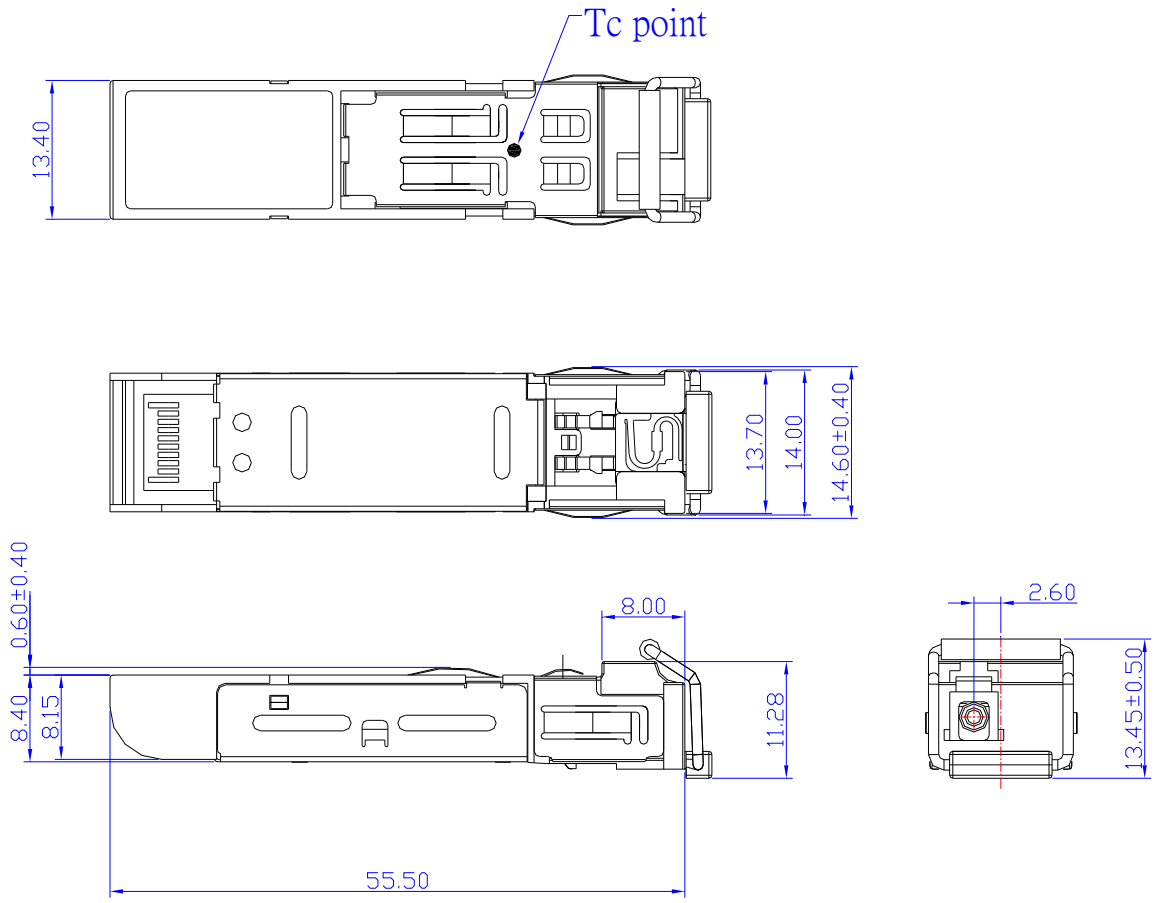
TX_DISABLE

The TX_DISABLE signal is high (TTL logic “1”) to turn off the laser output.

Receive Loss (RX_LOS)

The RX_LOS is high (logic “1”) when there is no incoming light from the companion transceiver. This signal is normally used by the system for the diagnostic purpose. The signal is operated in TTL level.

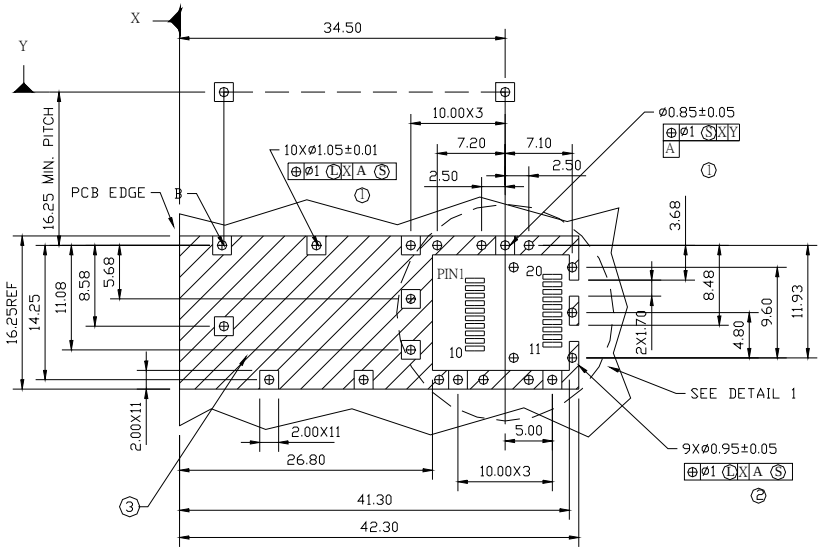
Dimensions

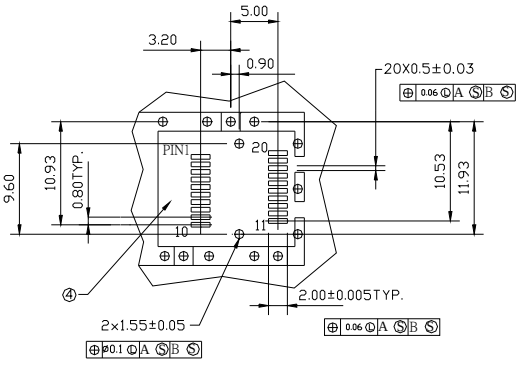


DIMENSIONS ARE IN MILLIMETERS

ALL DIMENSIONS ARE ± 0.2mm UNLESS OTHERWISE SPECIFIED

SFP host board mechanical layout





DETAIL 1

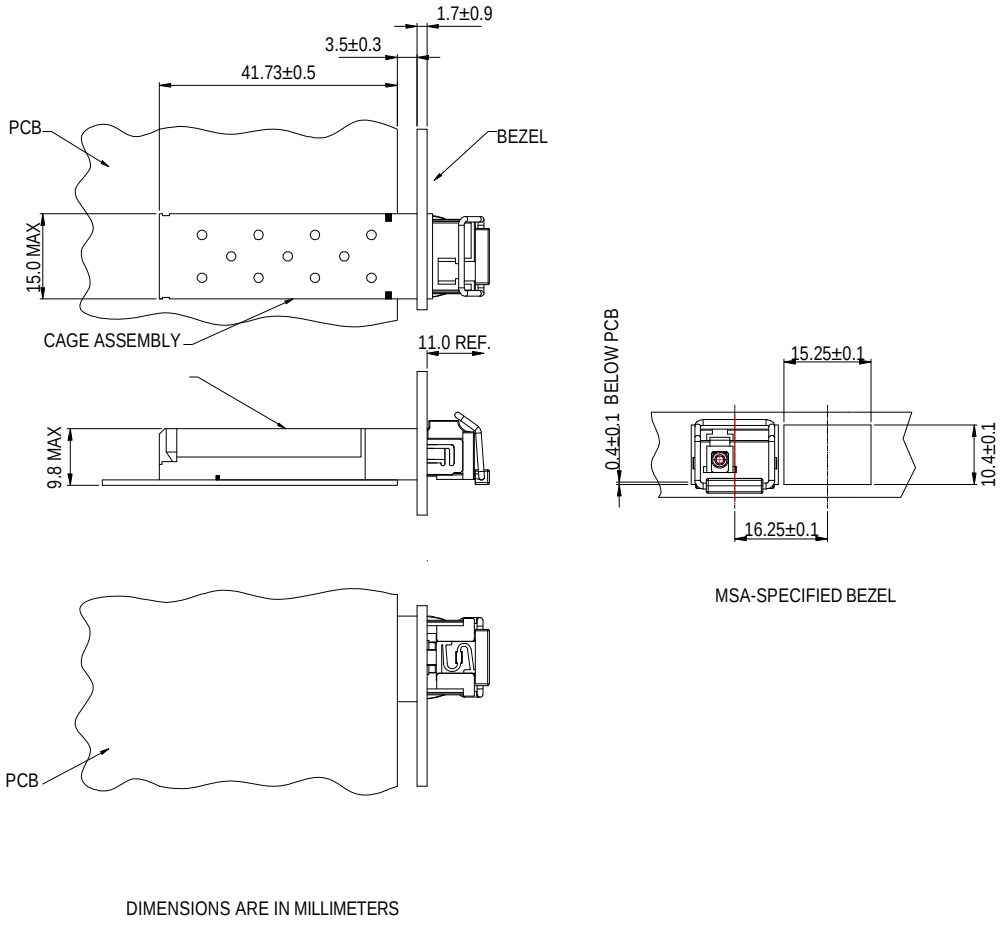
LEGEND

1.PADS AND VIAS ARE CHASSIS GROUND
2.THROUGH HOLES, PLATING OPTIONAL
3.HATCHED AREA DENOTES COMPONENT AND TRACE KEEPOUT(EXCEPT CHASSIS GROUND)
4.AREA DENOTES COMPONENT KEEPOUT (TRACES ALLOWED)

DIMENSIONS ARE IN MILLIMETERS

Unit: mm

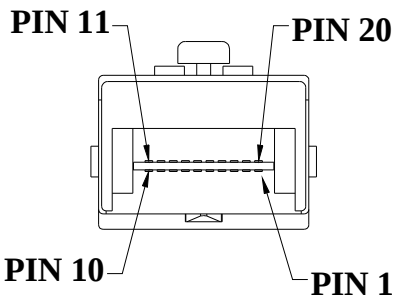
Assembly drawing



Unit: mm

Pin Assignment

Pin-Out



Pin	Signal Name	Description
1	T_{GND}	Transmit Ground
2	*TX_FAULT*	Transmit Fault
3	*TX_DISABLE*	Transmit Disable
4	*MOD_DEF (2)*	SDA Serial Data Signal
5	*MOD_DEF (1)*	SCL Serial Clock Signal
6	*MOD_DEF (0)*	TTL Low
7	*RATE SELECT*	Open Circuit
8	*RX_LOS*	Receiver Loss of Signal, TTL High, open collector
9	R_{GND}	Receiver Ground
10	R_{GND}	Receiver Ground
11	R_{GND}	Receiver Ground
12	*RX–*	Receive Data Bar, Differential PECL, ac coupled
13	*RX+*	Receive Data, Differential PECL, ac coupled
14	R_{GND}	Receiver Ground
15	V_{CCR}	Receiver Power Supply
16	V_{CCT}	Transmitter Power Supply
17	T_{GND}	Transmitter Ground
18	*TX+*	Transmit Data, Differential PCEL, ac coupled
19	*TX–*	Transmit Data Bar, Differential PCEL, ac coupled
20	T_{GND}	Transmitter Ground

Note : All information contained in this document is subject to change without notice.