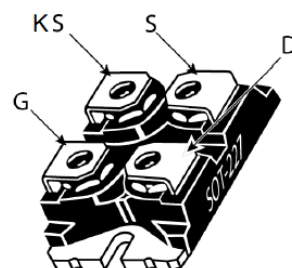


Description

Silicon Carbide (SiC) MOSFET use a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size.

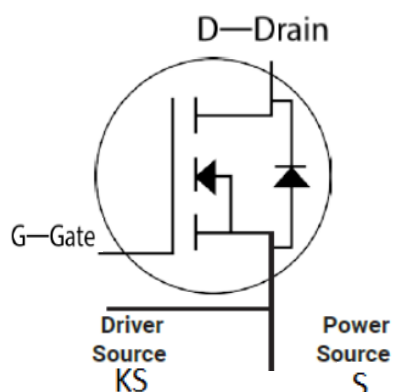
Features

- High Speed Switching with Low Capacitances
- High Blocking Voltage with Low $R_{DS(on)}$
- Fast and reliable body diode
- Easy to parallel and simple to drive
- Superior avalanche ruggedness
- ROHS Compliant, Halogen free
- Isolated voltage to 2500 V



Application

- PV inverter, converter, and industrial motor drives
- Smart grid transmission and distribution
- Induction heating and welding
- Power supply and distribution
- H/EV powertrain and EV charger



Ordering Information

Part Number	Marking	Package	Packaging
A2G100N1200MDSK	A2G100N1200MDSK	SOT227	Box

Absolute Maximum Ratings(Tc=25 °C)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage	1200	V
I _D	Drain Current(continuous)at Tc=25 °C	120	A
I _D	Drain Current(continuous)at Tc=100 °C	90	A
I _{DM}	Drain Current (pulsed)	270	A
V _{GS}	Gate-Source Voltage	-10/+22	V
P _D	Power Dissipation T _C = 25 °C	600	W
T _J , T _{stg}	Junction and Storage Temperature Range	-55 to +175	°C

Electrical Characteristics(T_J = 25 °C unless otherwise specified)
Typical Performance-Static

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DS}	Drain-source Breakdown Voltage	I _D =250uA, V _{GS} =0V	1200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =1200V, V _{GS} =0V, T _J =25 °C		5	100	uA
I _{GSS}	Gate-body Leakage Current	V _{DS} =0V ; V _{GS} =-10 to 20V		10	250	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =22mA	2	3	4	V
V _{GS(on)}	Recommended turn-on Voltage	Static		18		V
V _{GS(off)}	Recommended turn-off Voltage			-5		V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} =18V, I _D =50A		16	22	mΩ
		V _{GS} =18V, I _D =50A T _J =175 °C		28		mΩ

Typical Performance-Dynamic

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C _{iss}	Input Capacitance	V _{DS} =1000V, f=1MHz, V _{AC} =25mV		6878		pF
C _{oss}	Output Capacitance			288		pF
C _{rss}	Reverse Transfer Capacitance			13		pF
g _{fs}	Transconductance	V _{DS} =20V, I _D =50A		51		S
E _{oss}	Coss Stored Energy	V _{DS} =1000V, f=1MHz		141		μJ
E _{on}	Turn-On Energy (Body Diode)	V _{DS} =800V, V _{GS} =-5V/20V, I _D =50A, L=68μH T _J =150°C		1.88		mJ
E _{off}	Turn-Off Energy (Body Diode)			0.57		mJ
Q _g	Total Gate Charge	V _{DS} =800V, V _{GS} =-5V/20V, I _D =50A		238		nC
Q _{gs}	Gate-source Charge			76.7		nC
Q _{gd}	Gate-Drain Charge			78.3		nC
R _{G(int)}	Internal Gate Resistance	f=1MHz, V _{AC} =25mV		2.2		Ω
t _{d(on)}	Turn-on Delay Time	V _{DS} =800V, V _{GS} =-5V/20V, I _D =50A, L=68 μH R _{ext} =2.5Ω		40		ns
t _r	Rise Time			30		ns
t _{d(off)}	Turn-off Delay Time			65		ns
t _f	Fall Time			13		ns
E _{AS}	Avalanche energy, single Pulse	V _{DS} =150V, I _D =30A		3500		mJ

Typical Performance-Reverse Diode(T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{FSD}	Forward Voltage	V _{GS} =0V, I _F =37.5A, T _J =25°C		3.5	6	V
		V _{GS} =0V, I _F =37.5A, T _J =175°C		3	6	V
I _S	Continuous Diode Forward Current	V _{GS} =0V, T _C =25°C		110		A
t _{rr}	Reverse Recovery Time	V _{GS} =-5 V, I _F =50 A, V _R =800 V, di/dt=900A/μs, T _J =175°C		98		nS
Q _{rr}	Reverse Recovery Charge			613		nC
I _{rrm}	Peak Reverse Recovery Current			18		A

Thermal Characteristics

Symbol	Parameter	Value.	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case	0.25	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient	40	°C/W

The values are based on the junction-to case thermal impedance which is measured with the device mounted to a large heat sink assuming maximum junction temperature of T_J(max)=175°C

Electrical Characteristics

Fig1. Output characteristics ($T_J = 25^\circ\text{C}$)

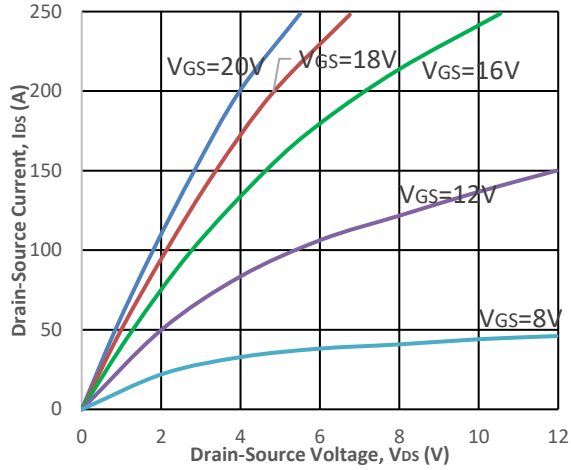


Fig2. Output characteristics ($T_J = 175^\circ\text{C}$)

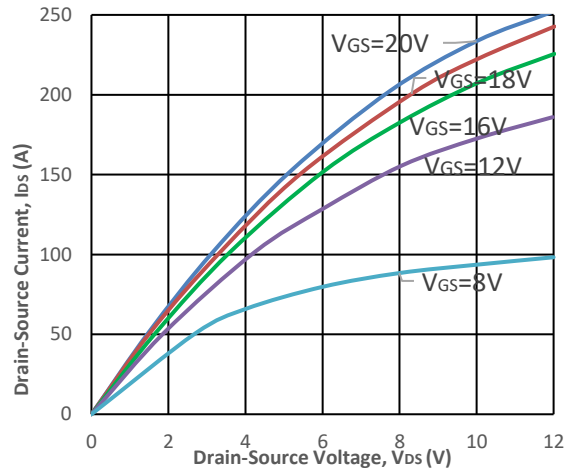


Fig3. Normalized On-Resistance vs. Temperature

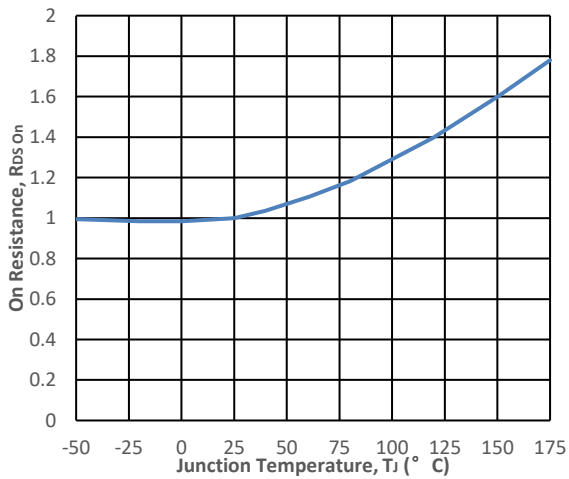


Fig4. On-Resistance vs. Temperature

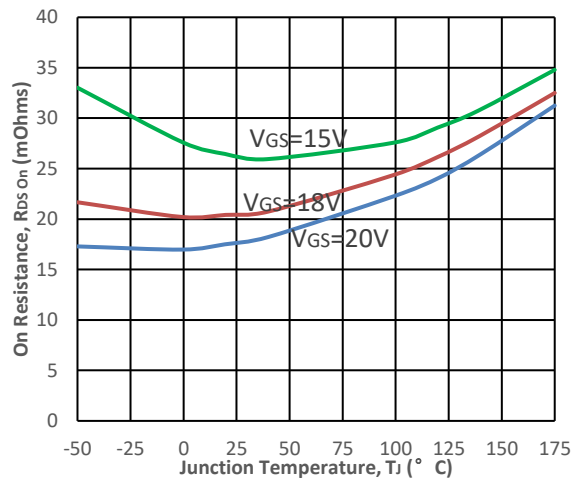


Fig5. Transfer Characteristic

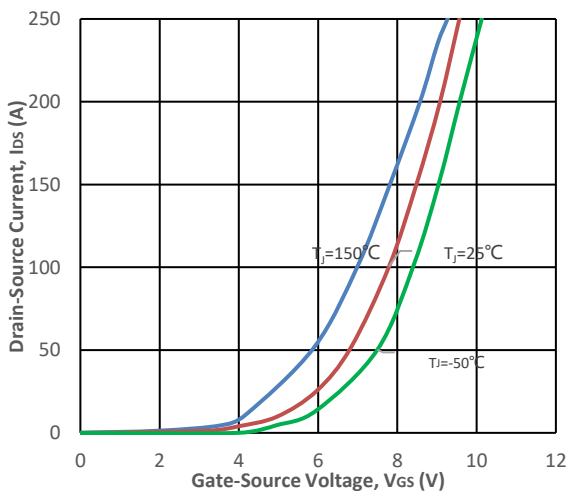


Fig6. Body Diode Characteristic at 25°C

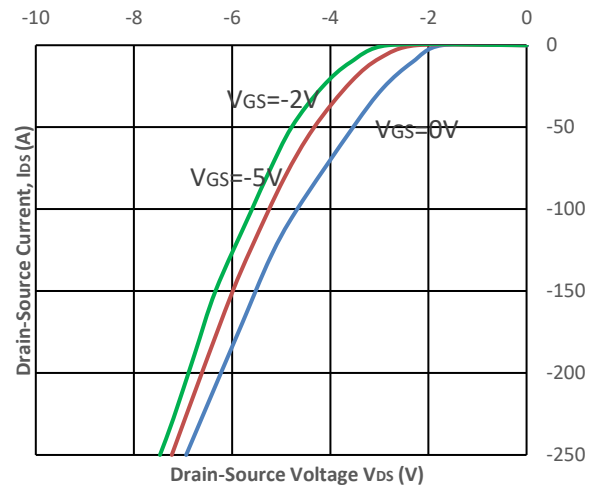


Fig7. Threshold Voltage vs. Temperature

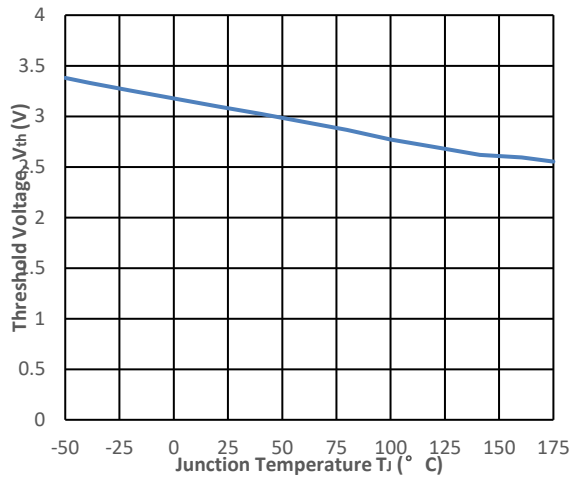


Fig8. Gate Charge Characteristics

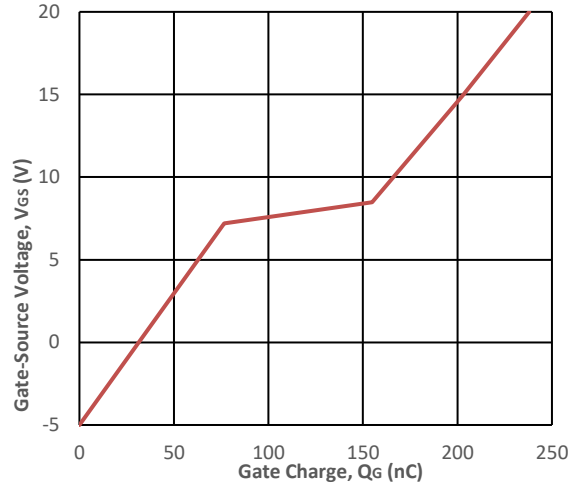


Fig9. 3rd Quadrant Characteristic at 25 $^{\circ}$ C

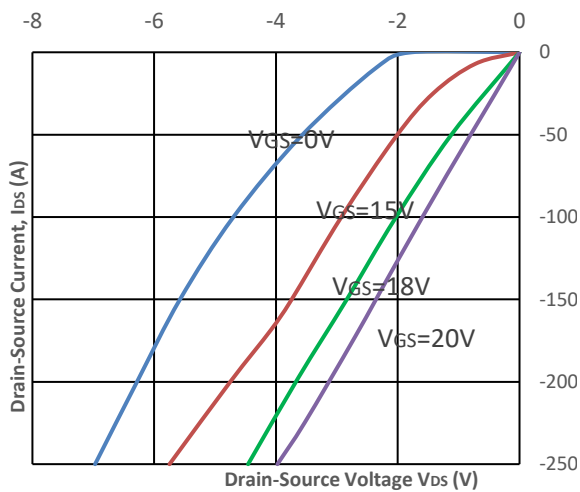


Fig10. Output Capacitor Stored Energy

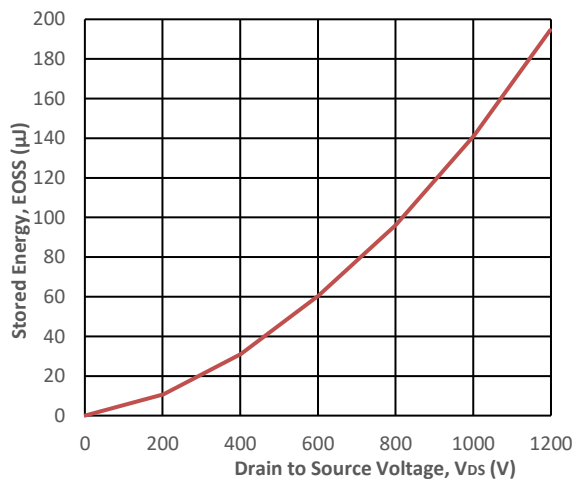


Fig11. Capacitances vs. Drain-Source

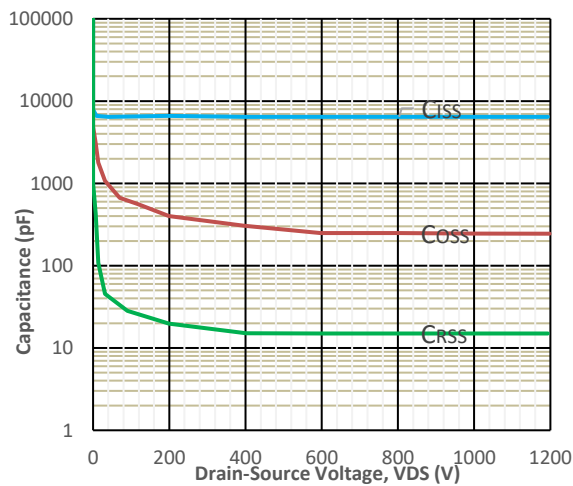


Fig12. Max Power Dissipation Derating Vs T_c

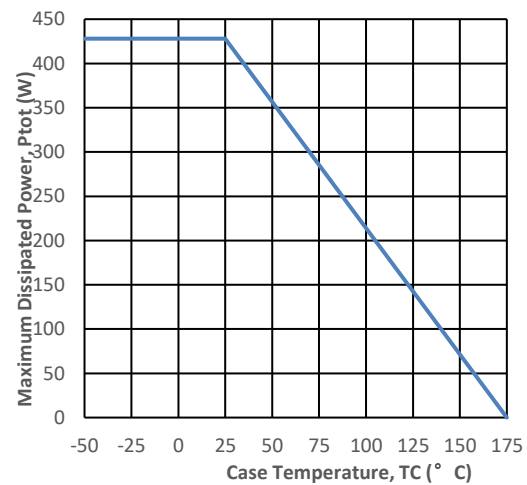


Fig13. Switching Energy vs. Drain Current

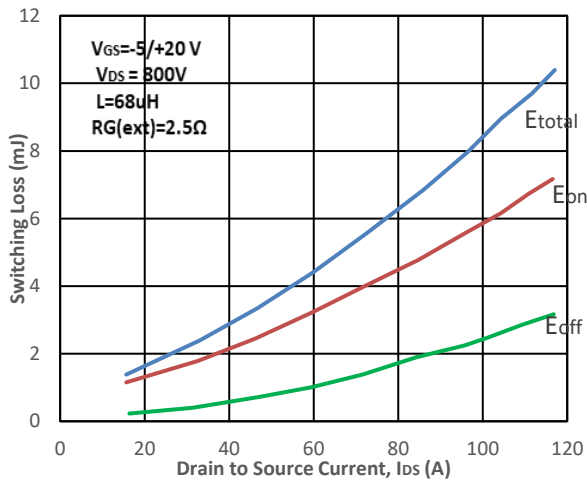


Fig14. Switching Energy vs. $R_{G(ext)}$

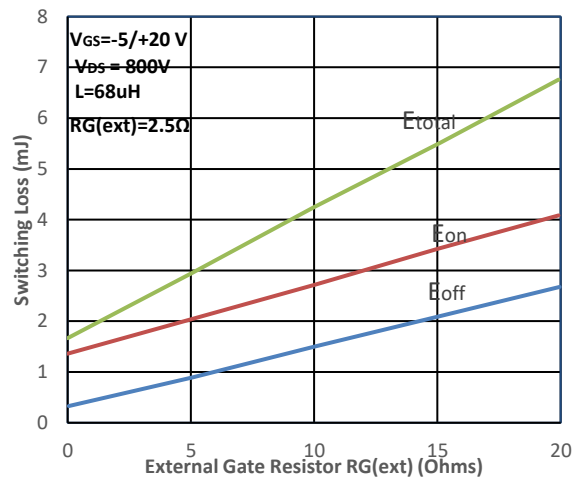


Fig15. Switching Energy vs. Temperature

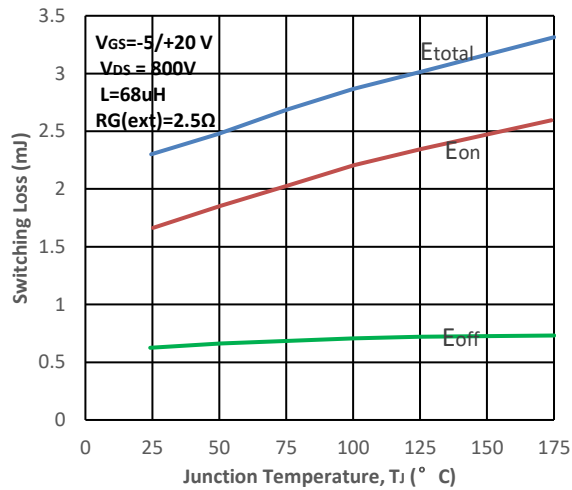


Fig16. Switching Times vs. $R_{G(ext)}$

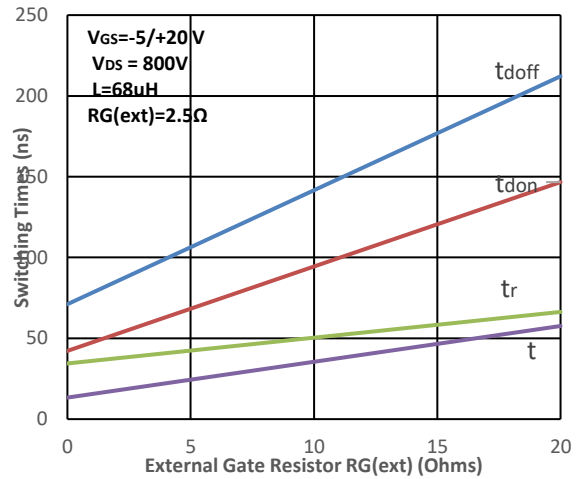


Fig17. Transient Thermal Impedance

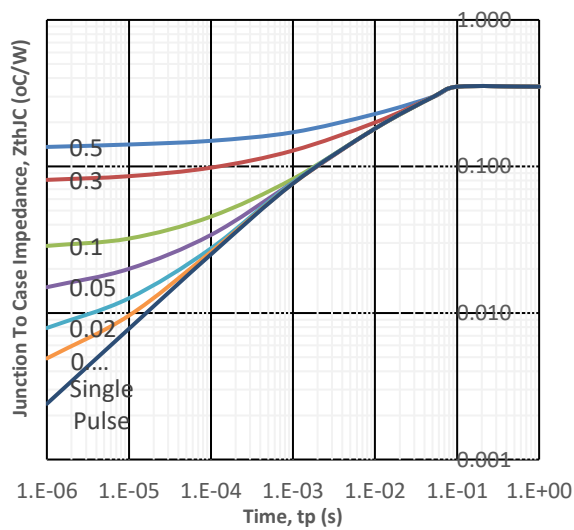
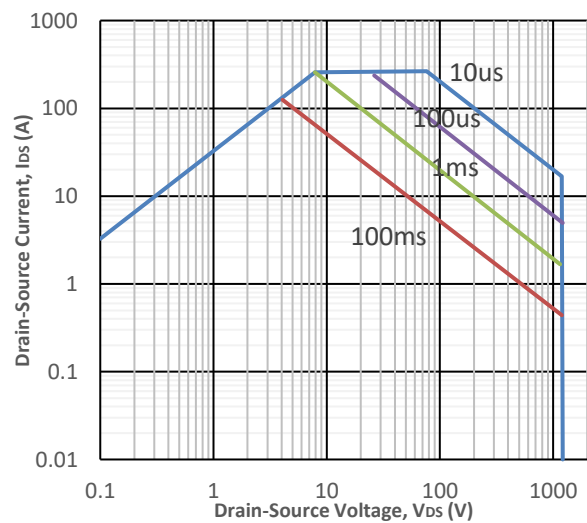


Fig18. Safe Operating Area



Package Drawing and Dimensions (UNIT: mm)

