

Shenzhen Leadtek Electronics Co.,Ltd

PRODUCT SPECIFICATION

TFT-LCD MODULE

Module No: LTK070P5084TX-06QC-V0

☒ Preliminary Specification

☐ Approval Specification

Designed by	Checked by	Approved by
<i>jona</i>	<i>Jerry</i>	<i>lan</i>

Final Approval by Customer

Approved by	Comment

※The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.



1.Document Revision History

Version	Contents	Date	Note
V0	Original	2024.01.25	



2. General Description

NO	Item	Specification	Unit	Remark
1	LCD Size	7.0	inch	-
2	Panel Type	TFT	-	-
4	Resolution	1024 x RGB x600	pixel	-
5	Display Mode	Normally Black	-	-
6	Number of Colors	16.7M	-	-
7	Viewing Direction	ALL	-	-
8	CTP+LCM Module Size	176.40 (W) x111.00(L) x5.40(T)	mm	Note
9	Panel Active Area	154.21 (W) x 85.92(L)	mm	Note
10	Pixel Pitch	0.1506 (H) x 0.1432(V)	mm	-
11	Interface	RGB-24bit	-	-

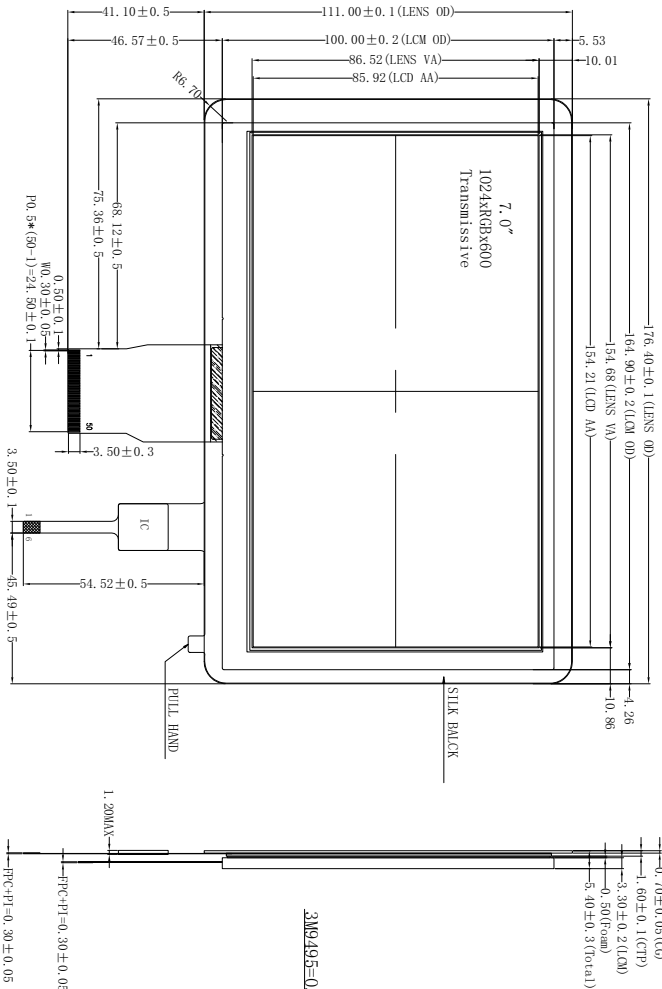
Note : Please refer to the mechanical drawing;

3. Outline Dimension

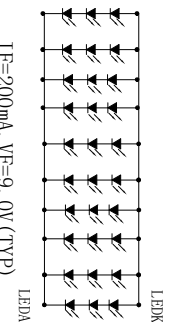
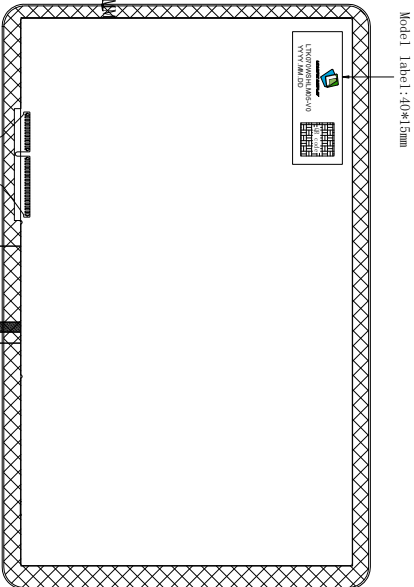
Front View

Side View

Back View



- Notes:
1. Display : 7.0", TFT
 2. Resolution: 1024xRGBx600
 3. LCD Viewing Direction: ALL
 5. Display Mode: Normally Black
 6. LCM+CTP Brightness: 450cd/m²(TYP)
 7. unmark Tolerance:±0.2
 8. OPERATING TEMP: -20° C~+60° C
 9. STORAGE TEMP: -30° C~+70° C
 10. Requirements on Environmental Protection: ROHS
- Technical requirements:
1. Structure: G+G
 2. Transmittance: ≥ 86% (550nm)
 3. All unmarked tolerances: ± 0.10mm
 4. IC: G1911, DITO, multi touch, channel count: 12 * 20
 5. Tempering degree: 110g steel ball, with a height of 40cm and no breakage when dropped, surface hardness
 - Test: ≥ 6H (pencil hardness test)
 6. Key holes and edge safety chamfers not marked in Gg ≤ 0.20mm
 7. Focus on controlling dimensions



CTP interface					
1	INT				
2	SDA				
3	SCL				
4	RST				
5	GND				
6	VDD_3.3V				

PIN NUMBER	NAME
1	LED+
2	LED+
3	LED+
4	LED+
5	GND
6	VCOM
7	VDD
8	MODE
9	DE
10	VS
11	HS
12	B7
13	B6
14	B5
15	B4
16	B3
17	B2
18	B1
19	B0
20	G7
21	G6
22	G5
23	G4
24	G3
25	G2
26	G1
27	G0
28	R7
29	R6
30	R5
31	R4
32	R3
33	R2
34	R1
35	R0
36	GND
37	PC1K
38	GND
39	L/R
40	L/D
41	VGH
42	VGL
43	AVDD
44	RES1
45	NC
46	VCOM
47	D/TIB
48	GND
49	NC
50	NC

- Notes:
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3	2	1	0	REV
NEW	DESCRIPTION	DATE	NAME	REV

Shenzhen Leadtek Electronics Co.,Ltd

SCALE:1/1	UNIT:mm	PAGE:1/1	APPROVE	CHECK	DRAWN
Part No:	LTK070P5084TX-06QC	VER:V0	Kevin	JONA	IAN
Customer No:					

4. Interface Specification

NO.	Symbol	Function	Remark
1	LED A	POWER SUPPLY- FOR BACKLIGHT CATHODE	
2	LED A	POWER SUPPLY- FOR BACKLIGHT CATHODE	
3	LED K	POWER SUPPLY+ FOR BACKLIGHT ANODE	
4	LED K	POWER SUPPLY+ FOR BACKLIGHT ANODE	
5	GND	Power ground	
6	VCOM	Common voltage	
7	DVDD	Power for Digital Circuit 3.3V	
8	MODE	DE/SYNC mode select. MODE=H:DE mode;MODE=L:SYNC mode	
9	DE	Data enable signal in RGB I/F mode 1	
10	VS	Vertical sync. Signal in RGB I/F	
11	HS	Horizontal sync. Signal in RGB I/F	
12~19	B7~B0	8 bit data bus display blue data	
20~27	G7~G0	8 bit data bus display green data	
28~35	R7~R0	8 bit data bus display red data.	
36	GND	Power ground	
37	DCLK	Pixel clock signal in RGB I/F	
38	GND	Power ground	
39	L/R	Left / right selection	
40	U/D	Up/down selection	
41	VGH	Gate ON Voltage	

42	VGL	Gate OFF Voltage	
43	AVDD	Analog supply voltage.	
44	RESET	Global reser pin	
45	NC	No connection	
46	VCOM	Common Voltage	
47	DITHB	Dithering function. Dithering function. DITHER = H:Enable internal dithering function;	
48	GND	Power ground	
49	NC	No connection	
50	NC	No connection	

5. Electrical Characteristics

5.1 TFT DC Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
TFT OperationFrame rate	Hz	--	60	--	Hz	-
Supply Voltage for(DC/DC)	VDD	3.0	3.3	3.6	V	-
TFT Gate ON Voltage	VGH	17	18	19	V	Note 2
TFT Gate OFF Voltage	VGL	-7	-6	-5	V	Note 2
TFT Common Voltage	Vcom	2.3	3.3	4.2	V	Note 1
AVDD	Avdd	9.0	9.6	10.6	V	Note 3

Note 1: VCOM value should be adjusted by different condition to optimize Flicker Value.

Note 2: VGH and VGL are the operating voltages of TFT gate.

Note 3: Adjust the contrast, make the color bigger and darker, and make the color smaller and lighter.

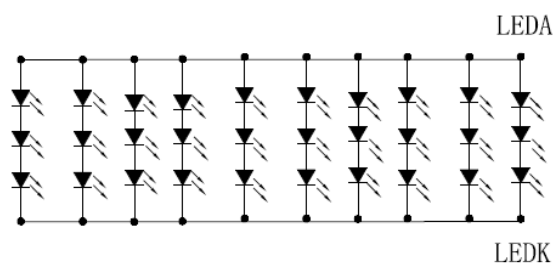
5.2.LED Backlight Specification

Item	Symbol	Min	Typ	Max	Unit	Notes
Forward voltage	Vf	-	9.0	-	V	-
Forward current	IF		200		mA	-
Luminance(LCM+CTP)	Lv		450		cd/m2	-
LED life time	N/A		30,000		Hr	

Note:The “LED life time” is defined as the module brightness decrease to 50% of original brightness at

IL=20mA/LED. The LED life time could be decreased if operating IL is larger than 25mA/LED.

LED circuit:



6.TP Features

Item	Specification
Structure	G+G
Coverlens OD	176.4 (H) *111.0 (V) *1.6 (T)
Coverlens VA	154.68 (H) *86.52 (V)
Touch Driver IC	GT911
Touch Interface	I2C

7.TP Mechanical

Item	Description	Note
Cover plate hardness test	6H Pencil Pressure:1N/45°	Hardness ≥ 6H
Operate	Finger	
Surface hardness (H)	≥6H	

8.TP Interface Specification

NO.	Name	
1	CTP_INT3.3V	Touch panel interrupt output
2	CTP_SDA3.3V	Touch panel I2C data
3	CTP_SCL3.3V	Touch panel I2C clock
4	CTP_RST3.3V	Touch panel reset
5	GND	Power ground
6	CTP_VDD3.3V	Touch panel Power supply 2.8~3.3V

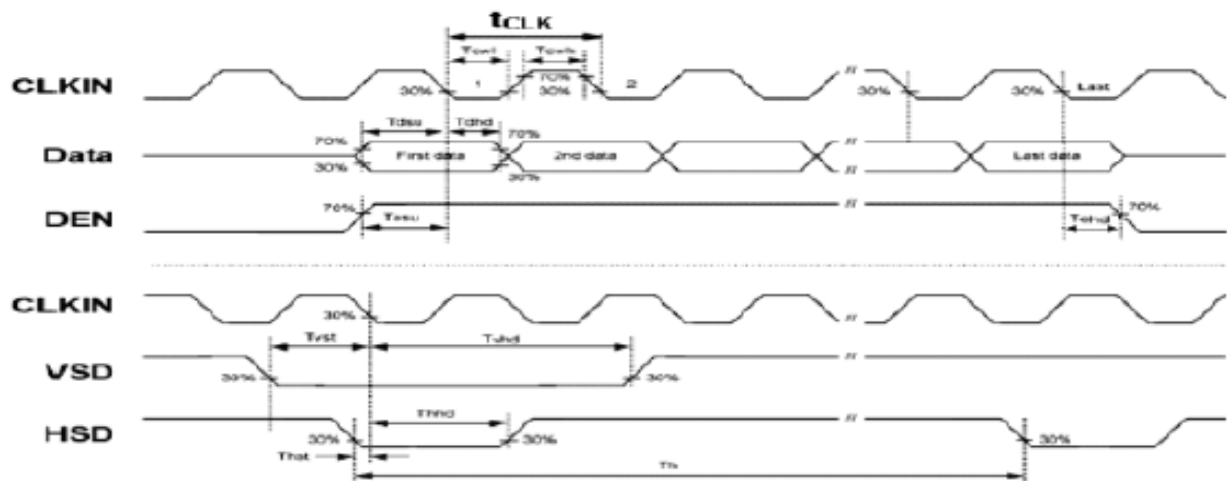
9. Timing Characteristics

9.1 Input timing table

	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Note
DE MODE	Dot Clock	1/tCLK	45	51.2	57	MHz	
	DCLK Pulse Duty	Tcwh	40	50	60	%	
	Horizontal Total Time	tH	1324	1344	1364	tCLK	
	Horizontal Effective Time	tHA		1024		tCLK	
	Horizontal Blank Time	tHB	300	320	340	tCLK	
	Vertical Total Time	tV	625	635	645	tH	
	Vertical Effective Time	tVA		600		tH	
	Vertical Blank Time	tVB	25	35	45	tH	
SYNC MODE	Horizontal Total Time	TH	1324	1344	1364	tCLK	
	Horizontal Pulse Width	Thpw		20	-	tCLK	thb + thpw = 160DCLK is fixed
	Horizontal Back Porch	Thb		140	-	tCLK	
	Horizontal Front Porch	Thfp	140	160	180	tCLK	
	Horizontal Effective Time	THA		1024		tCLK	
	Vertical Total Time	TV	625	635	645	tH	
	Vertical Pulse Width	Tvpw		3	-	th	tvpw + tvb = 23th is fixed
	Vertical Back Porch	Tvb	-	20	-	th	
	Vertical Front Porch	Tvfp	2	12	22	th	
	Vertical Valid	Tvd		600		th	

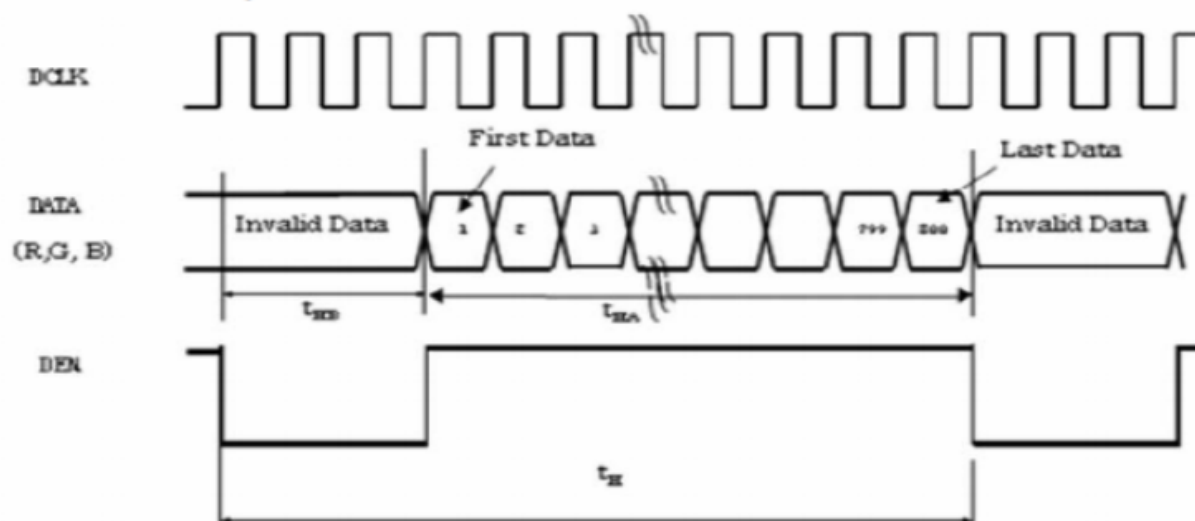
9.2 Input clock and data timing diagram

Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
DVDD Power On Slew Rate	TPOR	-	-	20	ms	From 0V to 90% DVDD
RSTB Pulse Width	TRst	50	-	-	us	DCLK=65MHz
DCLK Cycle Time	Tcph	14	-	-	ns	
DCLK Pulse Duty	Tcwh	40	50	60	%	
VSD Setup Time	Tvst	5	-	-	ns	
VSD Hold Time	Tvhd	5	-	-	ns	
HSD Setup Time	Thst	5	-	-	ns	
HSD Hold Time	Thhd	5	-	-	ns	
Data Setup Time	Tdsu	5	-	-	ns	D0[7:0], D1[7:0], D2[7:0] to DCLK
Data Hold Time	Tdhd	5	-	-	ns	D0[7:0], D1[7:0], D2[7:0] to DCLK
DEN Setup Time	Tesu	5	-	-	ns	
DEN Hold Time	Tehd	5	-	-	ns	

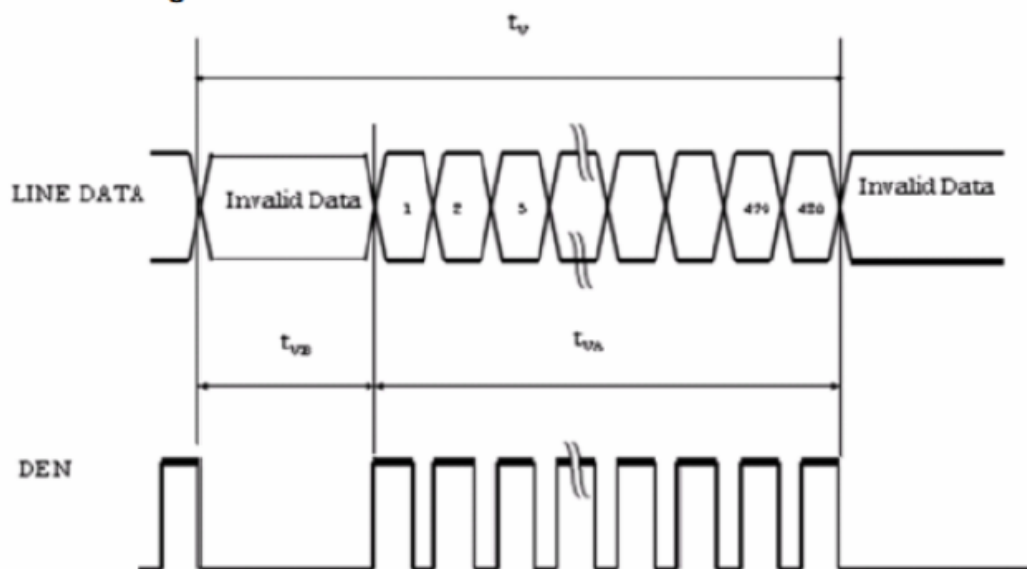


DE Mode

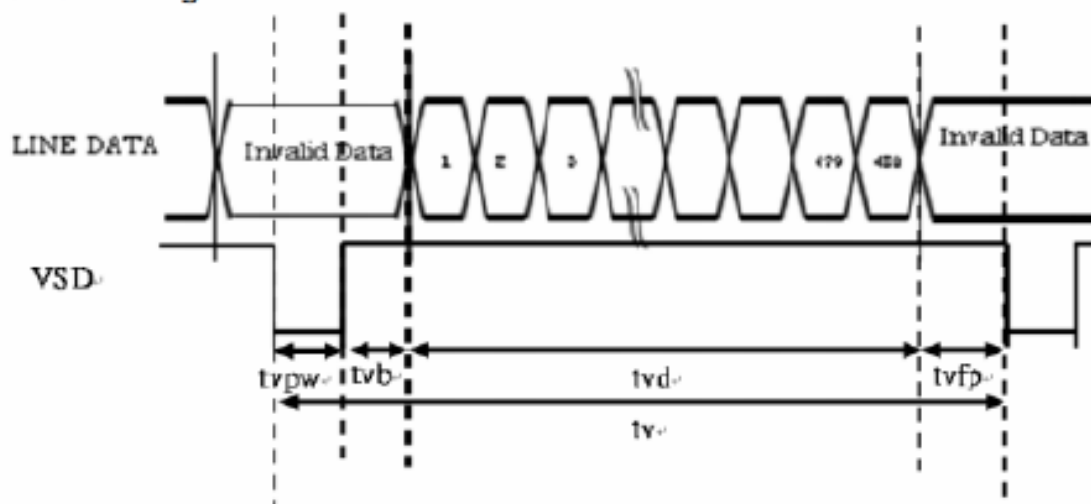
Horizontal timing :



Vertical timing :

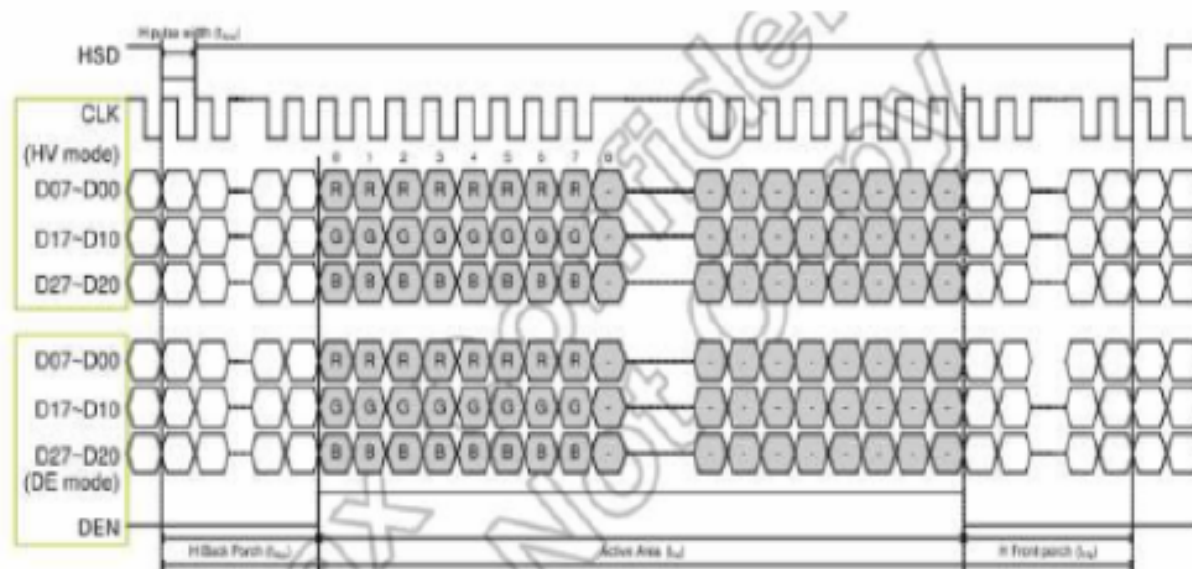


Vertical timing :

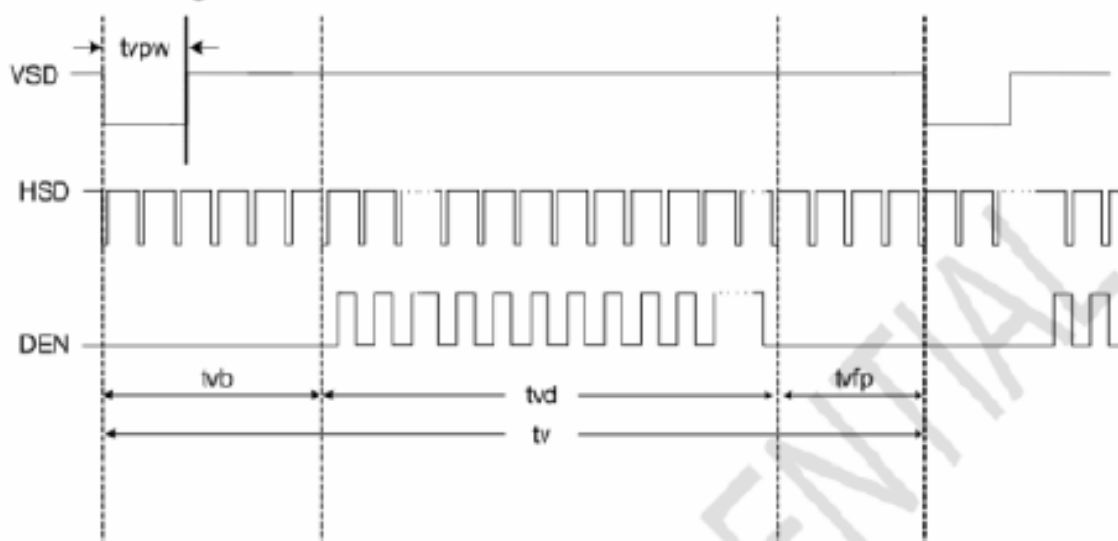


Data Input Format

Horizontal timing :



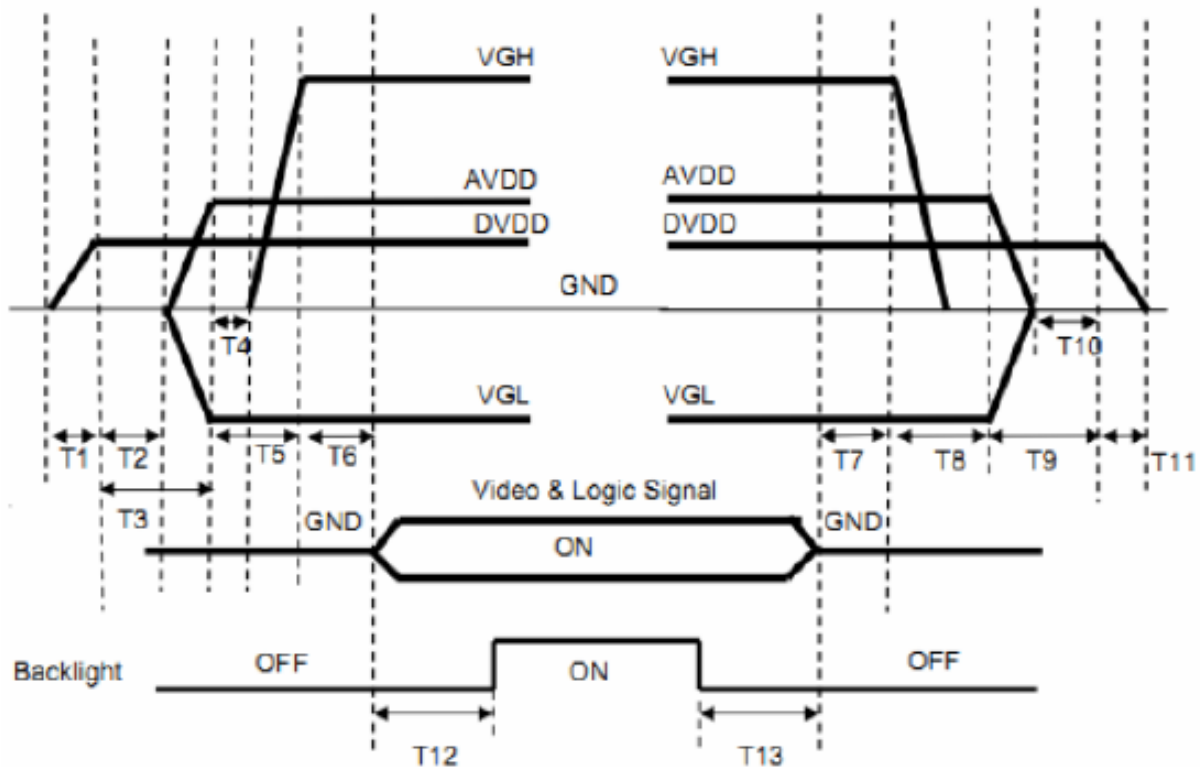
Vertical timing :



9.3.power signal sequence

High speed mode-clock channel timing

Power On : DVDD→AVDD/VGL →VGH →Video & Logic Signal→Backlight
 Power Off : Backlight→Video & Logic Signal→ VGH→AVDD/VGL→DVDD



$0 < T1 \leq 10\text{ms}$
 $T2 > 0\text{ms}$
 $T3 > 20\text{ms}$
 $T4 > 0\text{ms}$
 $T5 > 10\text{ms}$
 $0 < T6 \leq 10\text{ms}$
 $T12 \geq 200\text{ms}$

$T7 > 0\text{ms}$
 $T8 > 0\text{ms}$
 $T9 > 0\text{ms}$
 $T10 > 0\text{ms}$
 $0 < T11 \leq 10\text{ms}$
 $T13 \geq 200\text{ms}$

Reset Input Timing:

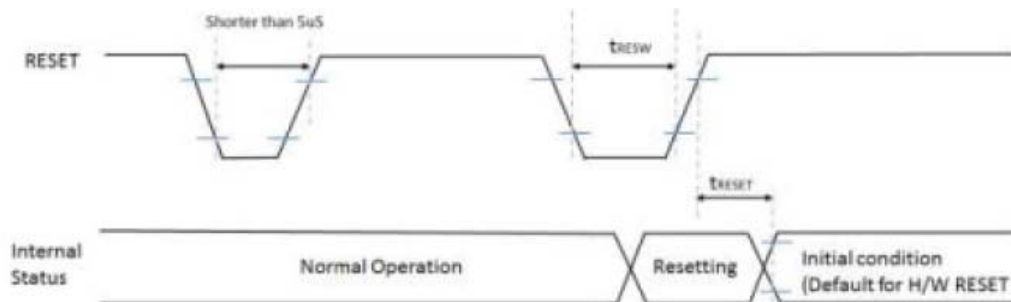


Figure 4-13 Reset Input Timing

Table 4-2 Reset Input Timing

Signal	Symbol	Parameter	Description	Specification			Unit	Notes
				MIN	TYP	MAX		
RESET	tRESW	Reset "L" pulse width		10			uS	1
	tRESET	Reset complete time	When reset applied during Sleep in mode			5	mS	2
			When reset applied during Sleep Out mode			120	mS	5

Note 1: Condition : Ta =25℃

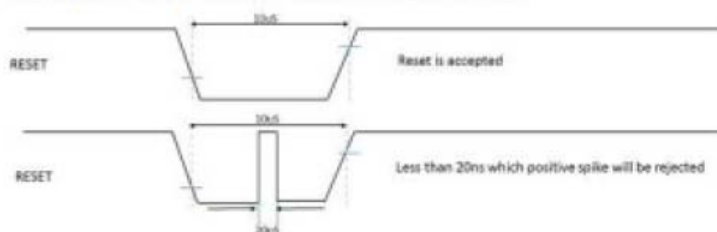
Note 2: Spike due to an electrostatic discharge on RESET line does not cause irregular system reset according to the table below.

RESET Pulse	Action
Less than 5us	Reset Rejected
More than 10uS	Reset
Between 5us and 10uS	Reset Start

Note 2: During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120ms, when Reset Starts in sleep out mode. The display remains the blank state in sleep in mode) and then return to Default condition for H/W RESET.

Note3: During Reset Complete Time, values in OTP memory will be latched to internal register during this period. This loading is done every time when there is H/W RESET complete time (tRESET) within 5ms after a rising edge of RESET.

Note 4: Spike Rejection also applies during a valid reset pulse as shown below:

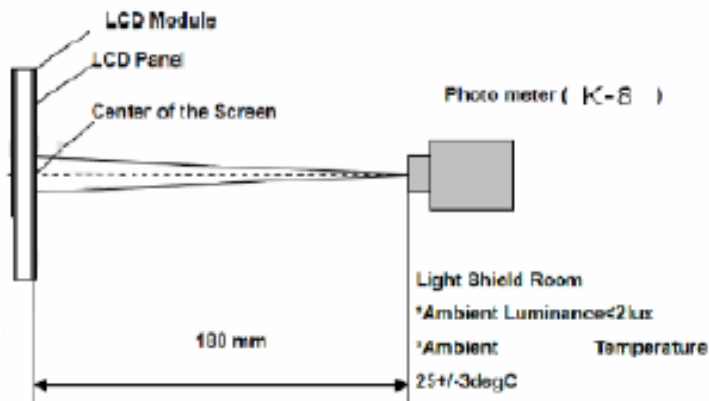


Note 5: It is necessary to wait 5ms after releasing RESET when sending commands, and Sleep Out command can not be sent

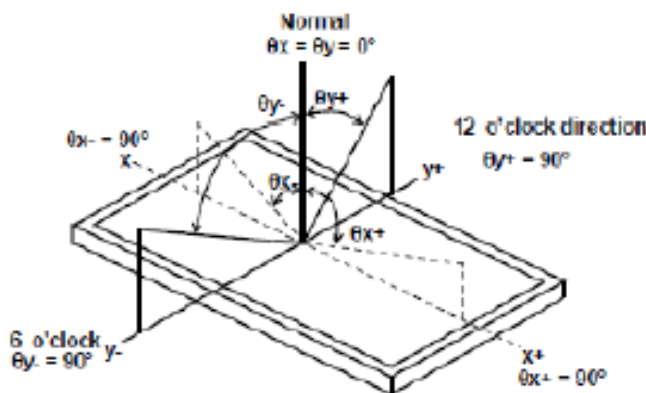
10.Optical Specification

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Response time	Tr+Tf	$\Theta=0^{\circ}$ $\varnothing=0^{\circ}$ $T_a=25^{\circ}\text{C}$		25		ms	
Contrast ratio	Cr		600	800			
Color gamut	S (%)		45	50		%	
Luminance uniformity	WHITE		80			%	
Viewing angle range	Θ_{x+}	$CR \geq 10$ $T_a=25^{\circ}\text{C}$		85		deg	
	Θ_{x-}			85		deg	
	Θ_{y+}			85		deg	
	Θ_{y-}			85		deg	
CIE(X,Y)Chromaticity	White(X)	$\Theta=0^{\circ}$ $\varnothing=0^{\circ}$ $T_a=25^{\circ}\text{C}$	0.286	0.306	0.326		
	White(Y)		0.325	0.345	0.365		

Note 1 :Measure condition : $25^{\circ} \pm 2^{\circ}$, $60 \pm 100\%RH$,under 10 Lux in the dark room. K-8,Viewing angle 2° .Measurement after lighting on 10 minus



Note2: Definition of Viewing Angle



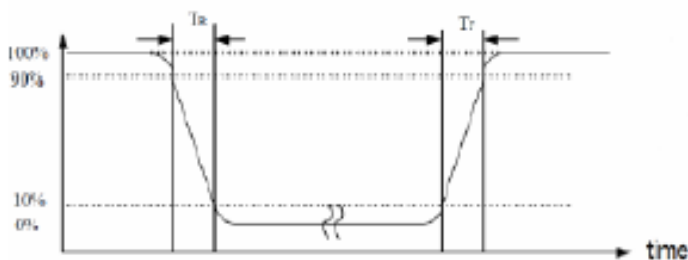
Note3: Definition of contrast ratio:

The contrast ratio can be calculated by the following expression

Contrast Ratio (CR) = L_{255} / L_0

L_{255} : Luminance of gray level 255, L_0 : Luminance of gray level 0

Note4: Definition of response time



Note (5) Definition of Transmittance(Module is without signal input)

Transmittance= Center Luminance of LCD/ Center Luminance of Back Light x100%

11. Reliability Test Items

Item	Test Condition	Criterion
High Temperature Operation	60 °C, 48 hrs	Note1, Note2
Low Temperature Operation	-20 °C, 48 hrs	
High Temperature Storage	70 °C, 48 hrs	
Low Temperature Storage	-30 °C, 48 hrs	
High Temp. & High Humidity Storage	50 °C, 90% RH, 48hrs	
Thermal Shock (Static)	-20°C, 30 min /60°C, 30 min, 2 0 cycles	

Note1: Evaluation should be tested after storage at room temperature for two hours.

Note2:

Pass: Normal display image no line defect.

Fail: No display image, or line defects.

Partial transformation of the module parts should be ignored.

12. Precautions

Please pay attentions to the followings as using the LCD module.

Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display s urface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.

- (e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.
- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.
- (j) Do not lift the FPC of Touch Panel.

Storage

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

Operation (a) When mounting or dismounting the LCD modules, turn the power off.

- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always be obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.
- (h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must

13.Packaging diagram

TBD



深圳市丽台电子有限公司

Shenzhen Leadtek Electronics Co.,Ltd

Quality Inspection Standards

品质允收标准

Model No. / 产品型号: Applies 5.5~10.0 Inch Touch Display Screen**Updated Date /生效日期:** 2022-05-20**Version / 版本:** A0**Customer confirmation :** _____

Record of Revision /修订履历

Version /版本	Revision Record /修订内容	Reviser /修订人	Revision Date /修订日期
V0	首发 / Starting	Green	2022.05.20

1.Scope of application /适用范围.

This document shall be applied to 5.5~10.0 inch touch display screen.

本文件适用于5.5~10.0 寸触摸显示屏.

2.Inspection conditions and environment /检验条件与环境.

2.1 Inspection Conditions /检验条件:

(1) Inspection Distance /检测距离: 35cm ±5cm.

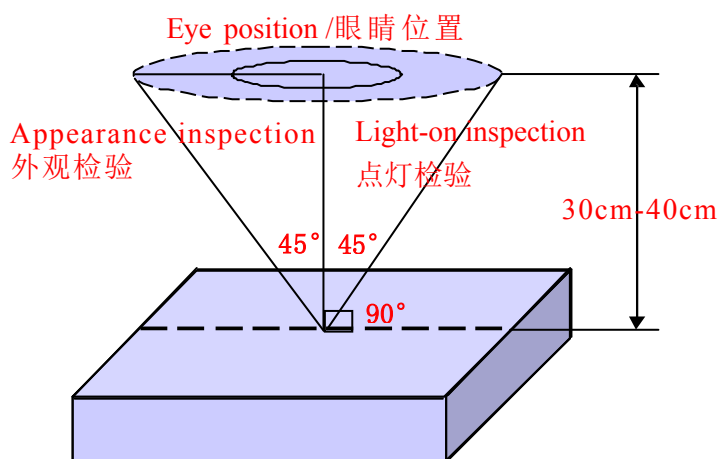
(2) Check time /检验时间:

Displays performance test /功能测试: 2~3S /Image, Cosmetic Inspection /外观检验:10~12S.

(3) Check the viewing angle /检验视角:

Light-on Inspection Angle /点灯检验角度: ±45°.

Cosmetic Inspection Angle /外观检验角度: ±45°.



(Perpendicular to LCD panel surface /垂直于LCD表面)

2.2 Inspection environment /检验环境:

Ambient Temperature 温度		25°C±5°C
Ambient Humidity 湿度		55±5%RH
Ambient Illumination 亮度	Cosmetic Inspection 外观检验	800-1000 Lux
	Functional Inspection 点灯检验	200~300Lux

2.3 Sampling Conditions /抽样条件:

(1) Quantity to be inspected /批量: Quantity of shipment lot per model /单次运送单一型号数量.

(2) Sampling method /抽样方法:

Sampling Plan /抽样计划		GB/T 2828.1- 2003
		Normal Inspection , Single Sampling 正常检验、单次抽样
		General inspection level: II 一般检验水平: 二级
AQL	Major Defect /主要缺陷	0.65
	Minor Defect /次要缺陷	1.0

(3) The classification of Major(MA) and Minor(MI) defects is shown as “3.1 Classification of defects” .
主缺(MA)及次缺(MI)定义于”3.1缺陷分类”.

3.Terms And Definitions /术语和定义

3.1 Classification of defects / 缺陷分类 :

(1) Major defects /主要缺陷:

A major defect is a defect that is likely to result in failure, or to reduce materially the usability of the product for its intended purpose .

可导致产品功能失效或减少产品可用性的缺陷.

(2) Minor defects /次要缺陷:

It will not cause the product to fail and reduce the defects in the effective use and operation of the product.

不会导致产品功能失效和减少产品的有效使用与操作的缺陷.

3.2 Point defects /点状缺陷:

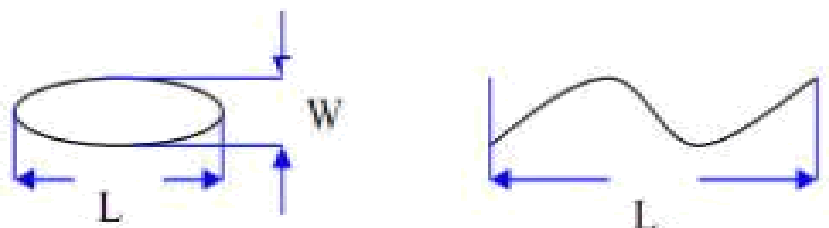
The size of the point defect is defined by the diameter D, and the average diameter of the defect is $D=1/2 (W+L)$.

点状缺陷的大小是由直径 D 定义的, 缺陷的平均直径 $D=1/2(W+L)$.

3.3 Linear defects /线状缺陷:

When defect size $L \geq 2W$, the defect count as liner type defect. Size of linear defect is defined by length (L) and the maximum width (W).

当缺陷尺寸 $L \geq 2W$ 时, 被视为线状缺陷, 线状缺陷是由长度 (L) 和最大宽度 (W) 定义的.



3.4 LCD sub-pixel dot /LCD子像素点

(1) Definition /定义 : The point defect area is greater than 50% of the LCD sub-pixel area, and is visible through ND5% filter masking .

子像素点缺陷面积大于 50% LCD子像素面积, 且透过 ND5%遮盖是可见的.

(2) The drawing of 1/2 area sub-pixel definition / 1/2 面积的子像素定义绘图:

The 1/2 area sub-pixel can be defined as below one or more of specific shapes

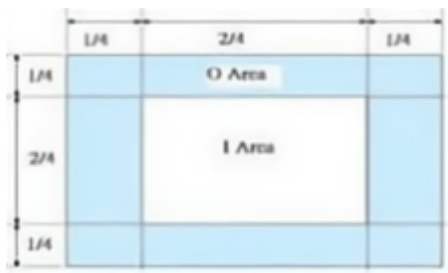
1/2 面积的子像素可以定义为如下一个或多个特定形状图:



3.5 Small bright dot /细碎亮点 :

Point defects smaller than "LCD sub-pixels" /小于“LCD子像素点”的点缺陷.

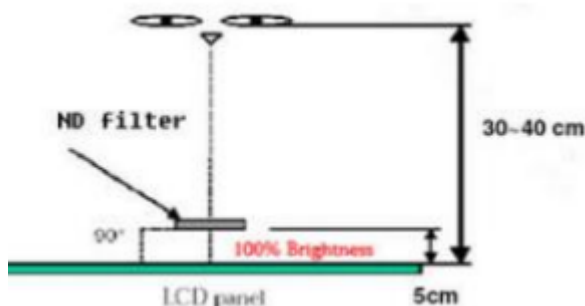
(Ratio of Zone I to Zone O /I 区与 O 区比例: 1: 2: 1)



3.6 ND filter inspection method /ND卡的检验方法:

Hold the ND filter about 5cm above the display area, with your eyes 30-40cm away from the panel, and observe for 2~3 seconds.

在显示区域上方大约 5cm 处握住 ND 卡, 眼睛距离面板 30-40cm, 观察2~3 秒.



3.7 Any FPC surface problems that do not leak copper on the surface and do not cause functional failure are acceptable.

任何 FPC 表面问题, 表面未露铜和不造成功能失效是可以接受.

3.8 Extraneous substances that can be wiped out , like Finger point,Particles are not considered as a defect .

可以被擦拭干净的表面物质不视为缺陷 (如手指印, 尘粒) .

3.9 Defects that can be covered by the material and are not visible in appearance are not considered defects.

能被物料覆盖，外观不可见的缺陷不视为缺陷。

3.10 Panel damage /面板损伤:

Glass damage outside the AA display area that does not affect the effective wiring is acceptable.

AA 显示区域以外的玻璃损伤，不影响有效线路是可以接受的。

3.11 Issues not specified or defined in this acceptance standard shall be handled through friendly negotiation between the two parties.

本允收标准中未规定或定义的问题，双方友好协商处理。

4. Inspection standards /检验标准

4.1 Structural Dimensions /结构尺寸规格

Serial Number 序号	Measurement items /测量项目		Specification /规格	Remark /备注
	名称 /Name	Unit /单位	Tolerance /公差	
1	Outside dimension: Length 尺寸: 长	mm /毫米	0.15mm~0.30mm	Please refer to the product specification for detailed dimensions and tolerances 详细的尺寸规格和公差请参考产品规格书
2	Outside dimension: Width 尺寸: 宽	mm /毫米	0.15mm~0.30mm	
3	Outside dimension: Thickness 尺寸: 高	mm /毫米	0.20mm~0.50mm	

4.2 Appearance Inspection Specification /外观检验规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass 玻璃	Wire(on Array) 线路	Can't be damaged 不能损伤	MA	
	Chipping/corner breaking 崩边/破角	Can't affect the effective lines and functions 不能影响有效线路和功能	MA	
	Edge 边缘	There must be no extensional cracks 不可有延伸性裂纹	MA	
Silicone 硅胶	Silicone coating 硅胶涂布	The height must not exceed the LCD CF surface 高度不能超过LCD CF面		MI
	Glue overflow 溢胶	Can't cover FPC, POL, etc 不能覆盖到FPC、POL等		MI

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
PCBA FPC Connector 连接器	Appearance 外观	Scratches or injuries are not allowed to cause copper exposure 划伤或损伤不允许表面出现露铜		MI
	Component 元器件	Can't be damaged and lack 不能损伤和缺少	MA	
	Goldfinger oxidation 金手指氧化	Not allowed 不允许		MI
	Connection status 连接状况	The connection must be accurate and stable 必须准确稳定连接	MA	
	Break 破裂	Not allowed 不允许	MA	
	Soldering, false soldering/tinning/tin beads 假焊/连锡/锡珠	Not allowed 不允许	MA	
POL 偏光片	Scratches 划伤	1. $W \leq 0.07\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.07\text{mm} < W \leq 0.12\text{mm}$; $L \leq 5\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.12\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许)		MI
	Dent 凹凸印	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Bubbles 气泡	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Point defects 点状不良	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Edge bubbles 边缘气泡	1. Within 1/2BM of the display area, it is not allowed 显示区往外 1/2BM 区域内, 不允许 2. The display area is 1/2 outside the BM area, and it is not controlled 显示区往外1/2BM区域以外, 不管控		MI
	Dirty/watermarked 脏污/水印	No dirt/water lines/finger marks are allowed, and must be wiped clean 不允许有脏污/水纹/手指印, 须擦拭干净方可		MI
	Warping 起翘	Not allowed 不允许		MI
	Attaching offset 贴偏	It is necessary to completely cover the display area outward, within the 1/2BM area, or without leaking POL edges after TP is attached 需完整覆盖显示区往外、1/2BM区以内或贴合TP后不会出现漏偏光片边缘		MI
	Mixture 混料	Mixing different types of POL or not using POL as required by the BOM, not allowed 不允许混贴不同型号的POL或未按BOM要求使用POL	MA	

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
TP&CG	Point defects 点状不良	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Scratches 划伤	1. $W \leq 0.07\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.07\text{mm} < W \leq 0.12\text{mm}$; $L \leq 5\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.12\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许) 4. There is a feeling scratch, Not allowable 有感划伤, 不允许		MI
	Edges and corners cracked 崩角/崩边	1. Product front / 产品正面: Edge and corner chipping is not allowed 崩角、崩边不允许 2. Product back / 产品背面: $X \leq 0.5$, $Y \leq 0.5$, $Z \leq 1/2T$; $N \leq 4$; $DS \geq 10\text{mm}$		MI
	Silk screen 丝印	The silk screen is clear, complete and correct 丝印清晰、完整、内容正确		MI
	Dirty 脏污	Non-wipeable dirt, not allowed 不可擦拭的脏污, 不允许		MI
	Broken 破损	Not allowable 不允许	MA	
	Ink color aberration 油墨色差	$\Delta E > 1$, Not allowable (不允许)		MI
	Cover pinholes 针孔	1. $D \leq 0.15\text{mm}$, $N \leq 4$, $DS \geq 10\text{mm}$, allowable 2. $D > 0.15\text{mm}$, intensive pinholes (密集型针孔), Not allowable (不允许)		MI
	IR holes IR孔	Dirt, deviation, color difference, etc. are not allowed 不允许脏污、偏位、色差等		MI
BL 背光	Backlight separation 背光分离	Not allowable 不允许		MI
	Deformation of rubber iron and rubber frame 胶铁、胶框变形	Use the plug gauge 0.3mm on the flat surface and can snap in and judge NG 在平面上使用塞规0.3mm卡翘曲位置, 能卡进判定NG		MI
	The iron frame is oxidized and not tightened 铁框氧化、卡不紧	Not allowable 不允许		MI
	Backlight sticky solder beads, glue, etc 背面粘锡珠、残胶等	Not allowable 不允许		MI
	Lnkjet coding , Barcode , QR code 喷码/条码/二维码	The lmkjet coding is clear and complete, the barcode and QR code can be scanned normally, and the content and format match 喷码清晰完整、条码和二维码可正常扫描, 内容和格式相符		MI
	Accessories(protective film, double-sided tape, insulating adhesive, etc.) 辅料(保护膜、双面 胶、绝缘胶等)	Defects such as missing pastes, sticking deviations, defects, and fractures are not allowed 不允许有漏贴、贴偏、残缺、断裂等缺陷		MI

4.3 Electrical test specifications /电性检查规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass bright spots/dark spots 玻璃亮点/暗点	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
Mura	Use ND5% filter masking, visual invisibility is OK, 200~300Lux 使用ND5%遮盖, 目视不可见即为OK, 200~300Lux		MI
Small bright dot 细碎亮点	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
Light leakage 漏光	1. Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK 2. If necessary, sign off on the sample 必要时, 签限定样		MI
Backlight black/white dots 背光黑点/白点	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
Linear foreign bodies 线状异物 (异物毛丝等)	1. $W \leq 0.07\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.07\text{mm} < W \leq 0.12\text{mm}$; $L \leq 5\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.12\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许)		MI
Black/White Print 黑印/白印	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The display is uneven 显示不均匀	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The brightness is uneven 亮度不均匀	Brightness uniformity $< 85.0\%$, Not allowable 亮度均匀性 $< 85.0\%$, 不允许		MI
Displacement of the membrane 膜材移位	Not allowable 不允许		MI
Interference pattern/Newtonian pattern 干涉纹/牛顿纹	Not allowable 不允许		MI
Display abnormal 显示异常	Not allowable 不允许	MA	
No display 无显示	Not allowable 不允许	MA	
Line/Missing Drawing 线条/缺画	Not allowable 不允许	MA	
Splash screen 闪屏	Not allowable 不允许	MA	
LCD grid LCD网格	Not allowable 不允许	MA	
Afterimage 残影	Not allowable 不允许	MA	
Wrong viewing angle 视角错误	Not allowable 不允许	MA	
No touch 无触摸	Not allowable 不允许	MA	
Touch the jump point 触摸跳点	Not allowable 不允许	MA	
Not sensitive 触摸不灵敏	Not allowable 不允许	MA	