

Shenzhen Leadtek Electronics Co.,Ltd

PRODUCT SPECIFICATION

TFT-LCD MODULE

Module No: LTK055HDHCT15-V0

☒ Preliminary Specification

☐ Approval Specification

Designed by	Checked by	Approved by
<i>jona</i>	<i>Jerry</i>	<i>lan</i>

Final Approval by Customer

Approved by	Comment

※The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.



1.Document Revision History

Version	Contents	Date	Note
V0	Initial version	2023.12.07	



2. General Description

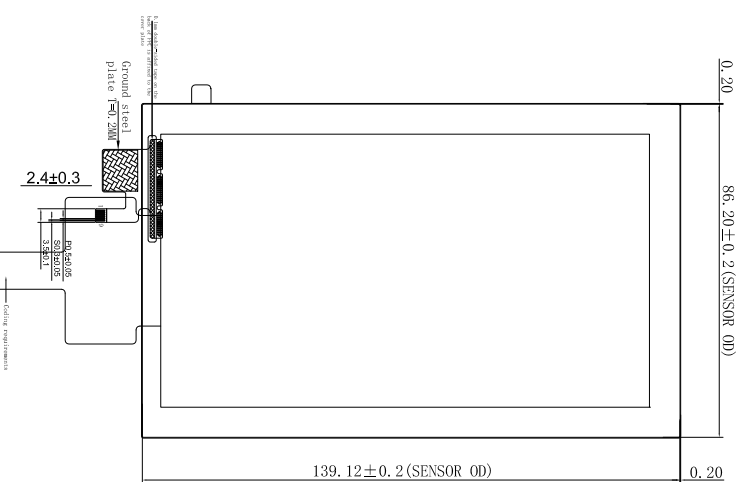
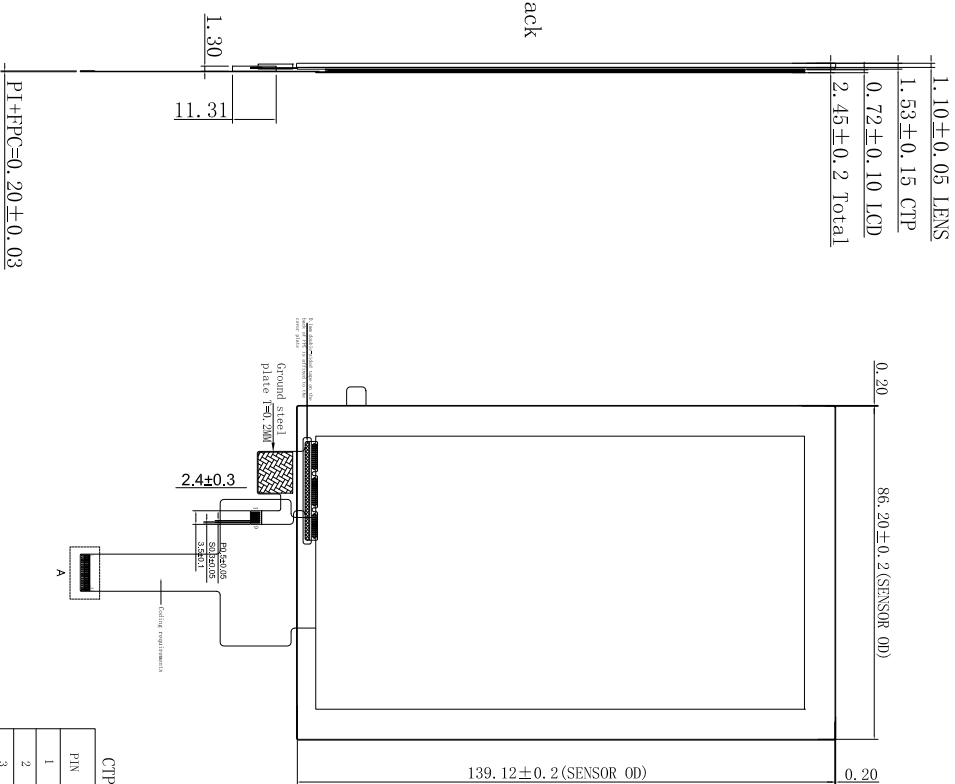
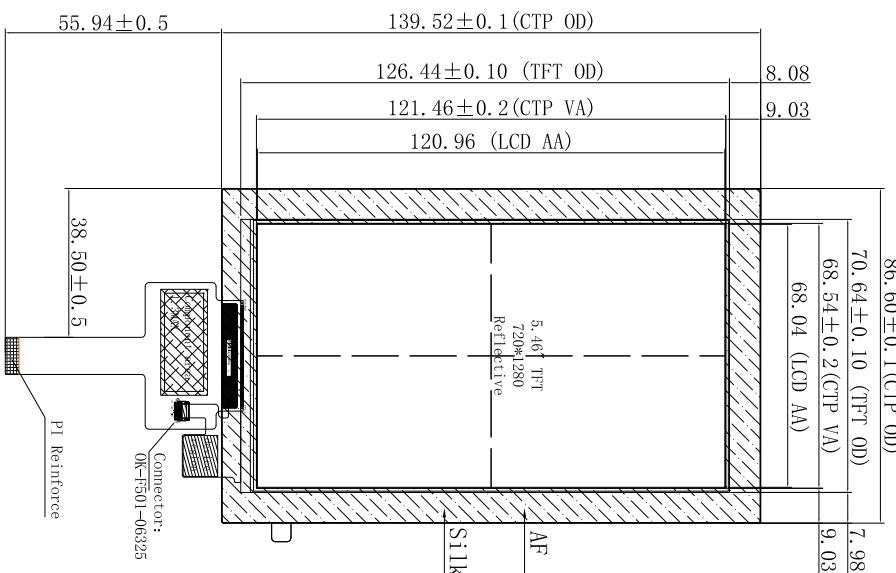
N0	Item	Specification	Unit	Remark
1	LCD Size	TFT"5.46	inch	-
2	Panel Type	TN	-	-
3	Display Resolution	720 x RGB x 1280	pixel	-
4	Display mode	Reflective / Normally White	-	-
5	Display colors	16.7M	-	-
6	Viewing Direction	60° / 60° / 60° / 60° (CR > 2)	-	-
7	Contrast Ratio	15	-	
8	CTP+LCM Module Size	86.60 (W) x139.52(L) x2.45(T)	mm	Note
9	Active Area	68.04 (W) x 120.96 (L)	mm	Note
10	Pixel Pitch	0.0945x0.0945	mm	-
11	Weight	TBD	g	-
12	Driver IC	ILI9881C	bit	-
13	Light Source	-	-	-
14	Interface	MIPI 4 Lanes	-	-

3.Mechanical Drawing

Front View

Side View

Back View



Display Pin interface

139.12±0.2 (SENSOR_OD)	
1	GND
2	TOUCH_3V3
3	GND
4	TOUCH_INT
5	TOUCH_RST
6	TOUCH_SDA
7	TOUCH_SCL
8	GND
9	LED_K
10	LED_A
11	TE
12	RSTN
13	NC
14	IOVCC_3V3
15	VCC_3V3
16	GND
17	D3P
18	D3N
19	GND
20	D2P
21	D2N
22	GND
23	CKP
24	CKN
25	GND
CTP_P1N	
P1N	NAME
1	VDD
2	GND
3	INT
4	RESET
5	SMA
6	SCL
31	GND

Technical parameters:
1. Structure: GTF; Transmittance (VA region) $\geq 86\%$;

1. Display : 5.46", TFT
2. Resolution: 720xRGBx1280
3. LCD Viewing Direction: 60/60/60/60
4. Drive IC: ILI9881C
5. Display Mode: Normally White
6. unmark Tolerance: ± 0.2
7. OPERATING TEMP: $-20^{\circ}\text{C}^{\sim}+70^{\circ}\text{C}$
8. STORAGE TEMP: $-30^{\circ}\text{C}^{\sim}+80^{\circ}\text{C}$
9. Requirements on Environmental Prot

3	
2	
1	
0	NEW
REV	DESCRIPTION

				LEADTEK COMPANY LIMITED			
LEADTEK DISPLAY							
SCALE: 1/1	UNIT: mm	PAGE: 1/1			Approve	Check	Drawn
Part No:	LTK055HDDCT15		VER: V0		Ian	Jack	kevin
Customer No:							

4. INTERFACE SIGNAL

Pin No.	Symbol	Description
1	GND	Power Ground
2	TOUCH_3V3	Power supply for CTP.
3	GND	GND pin for CTP
4	TOUCH_INT	INT pin for CTP
5	TOUCH_RST	Reset pin for TP
6	TOUCH_SDA	SDA pin for CTP
7	TOUCH_SCL	SCL pin for CTP
8	GND	Power Ground
9	NC	Not connect
10	NC	Not connect
11	TE	Tearingeffectoutput(Reserved)
12	RESET	Reset signal (LowW Active)
13	NC	Not connect
14	IOVCC_1.8V	A supply voltage to the digital circuit. (1.8V)
15	VCC_3.3V	A supply voltage to the digital circuit. (3.3V)
16	GND	Power Ground
17	D3P	MIPI DSI differential data 3 postive
18	D3N	MIPI DSI differential data 3 negative
19	GND	Power Ground
20	D2P	MIPI DSI differential data 2 postive
21	D2N	MIPI DSI differential data 2 negative
22	GND	Power Ground
23	CKP	MIPI DSI differential clock postive
24	CKN	MIPI DSI differential clock negative
25	GND	Power Ground
26	D1P	MIPI DSI differential data 1 postive
27	D1N	MIPI DSI differential data 1 negative
28	GND	Power Ground
29	D0P	MIPI DSI differential data 0 postive
30	D0N	MIPI DSI differential data 0 negative
31	GND	Power Ground

5. LCM DC CHARACTERISTICS

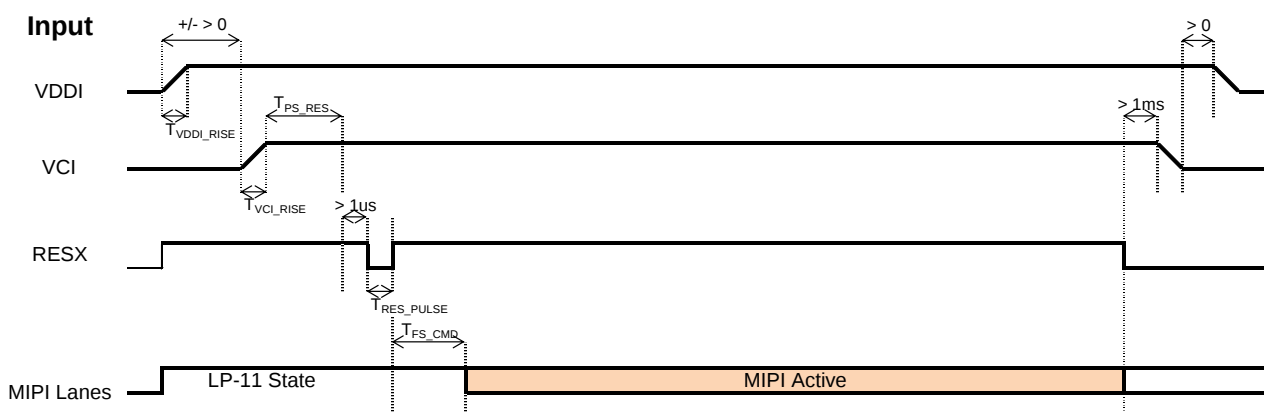
(Ta=25±2°C)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage 1	IOVCC	1.65	1.8	3.3	V	
Power Supply Voltage 2	VCI	2.5	2.8	3.6	V	

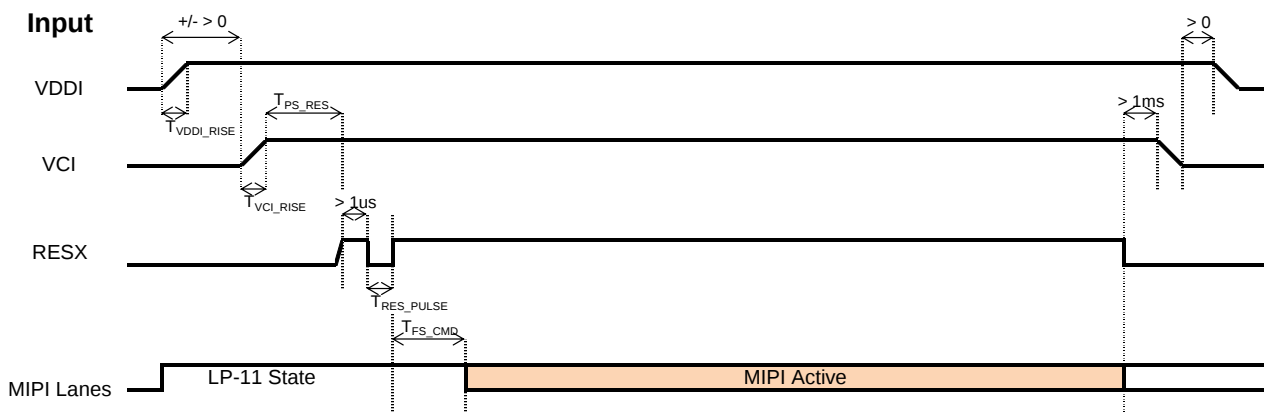
6. Power on/off Sequence

6.1.2. Power Mode 3

Case A:



Case B:



Symbol	Characteristics	Min.	Typ.	Max.	Units
T_{VDDI_RISE}	VDDI Rise time	10	-	-	us
T_{VCI_RISE}	Case A: VCI Rise time	130	-	-	us
	Case B: VCI Rise time	40			
T_{PS_RES}	VDDI/VCI on to Reset high	5	-	-	ms
T_{RES_PULSE}	Reset low pulse time	10	-	-	us
T_{FS_CMD}	Reset to first command	10	-	-	ms

Figure 93: Power on/off sequence with Power Mode 3

6.1.3. Reset Timing

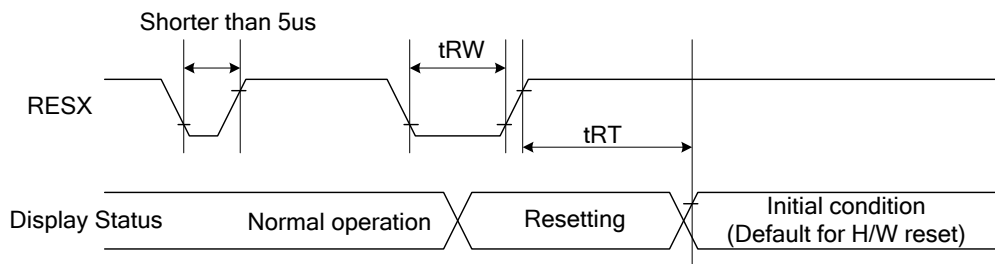


Figure 113: Reset Timing

Table 47: Reset Timing

Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5) 120 (note 1,6,7)	mS

Notes:

1. The reset cancel also includes required time for loading ID bytes, VCOM setting and other settings from EEPROM to registers. This loading is done every time when there is H/W reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the Table 48.

Table 48: Reset Descript

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 10us	Reset
Between 5us and 10us	Reset starts

3. During the Resetting period, the display will be blanked (The display enters the blanking sequence, which maximum time is 120 ms, when Reset Starts in the Sleep Out mode. The display remains the blank state in the Sleep In mode.) and then return to Default condition for Hardware Reset.
4. Spike Rejection can also be applied during a valid reset pulse, as shown below:

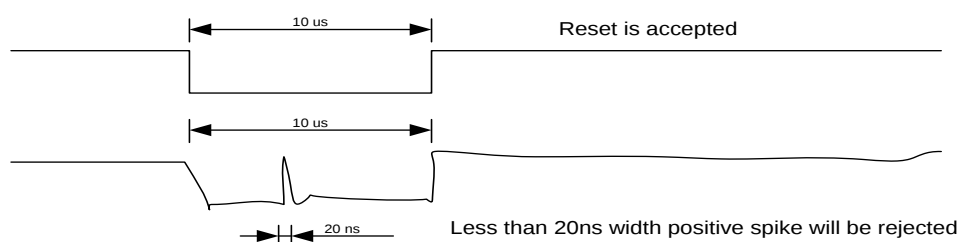


Figure 114: Positive Noise Pulse during Reset Low

5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

6.1.4.DSI DC Characteristics

The DSI uses different state codes which depend on DC voltage levels of the clock and data lanes. The meaning of the state codes is defined in the following table.

State Code	Line DC Voltage Levels	
	CLOCK_P or DATA_P	CLOCK_N or DATA_N
HS-0	Low (HS)	High (HS)
HS-1	High (HS)	Low (HS)
LP-00	Low (LP)	Low (LP)
LP-01	Low (LP)	High (LP)
LP-10	High (LP)	Low (LP)
LP-11	High (LP)	High (LP)

Note: $T_a = -30^{\circ}\text{C}$ to 70°C (to $+85^{\circ}\text{C}$ no damage)

6.1.5.DC Characteristics for DSI LP Mode

DC levels of the LP-00, LP-01, LP-10 and LP-11 are defined in the table below: DC Characteristics for the DSI LP mode when LP-RX, LP-CD or LP-TX is mentioned in the condition column. Other logical levels in the table are for MCU interface.

Parameter	Symbol	Condition	Specification			Unit
			Min.	Typ.	Max.	
Logic 1 input voltage	$V_{IHLP\text{CD}}$	LP-CD	450	-	1350	mV
Logic 0 input voltage	$V_{ILL\text{PCD}}$	LP-CD	0.0	-	200	mV
Logic 1 input voltage	$V_{IHLP\text{RX}}$	LP-RX (CLK, D0, D1, D2, D3)	880	-	1350	mV
Logic 0 input voltage	$V_{ILL\text{PRX}}$	LP-RX (CLK, D0, D1, D2, D3)	0.0	-	550	mV
Logic 0 input voltage	$V_{ILL\text{PRXULP}}$	LP-RX (CLK ULP mode)	0.0	-	300	mV
Logic 1 output voltage	$V_{OHL\text{PTX}}$	LP-TX (D0)	1.1	-	1.3	V
Logic 0 output voltage	$V_{OLL\text{PTX}}$	LP-TX (D0)	-50	-	50	mV
Logic 1 input current	I_{IH}	LP-CD, LP-RX	-	-	10	uA
Logic 0 input current	I_{IL}	LP-CD, LP-RX	-10	-	-	uA

Notes:

- $T_a = -30^{\circ}\text{C}$ to 70°C (to $+85^{\circ}\text{C}$ no damage)
- DSI High Speed mode is off.

6.1.6. DC Characteristics for DSI HS mode

Parameter	Symbol	Condition	Specification			Unit
Input Common Mode Voltage for Clock	V_{CMCLK}	CLKP/N Note 2, Note 3	70	-	330	mV
Input Common Mode Voltage for Data	V_{CMDATA}	DnP/N Note 2, Note 3, Note 5	70	-	330	mV
Common Mode Ripple for Clock Equal or Less than 450MHz	$V_{CMRCLKL450}$	CLKP/N Note 4	-50	-	50	mV
Common Mode Ripple for Data Equal or Less than 450MHz	$V_{CMRDATAL450}$	DnP/N Note 4, Note 5	-50	-	50	mV
Common Mode Ripple for Clock More than 450MHz (peak sine wave)	$V_{CMRCLKM450}$	CLKP/N	-	-	100	mV
Common Mode Ripple for Data More than 450MHz (peak sine wave)	$V_{CMRDATAM450}$	DnP/N Note 5	-	-	100	mV
Differential Input Low Level Threshold Voltage for Clock	V_{THCLK-}	CLKP/N	-70	-	-	mV
Differential Input Low Level Threshold Voltage for Data	$V_{THDATA-}$	DnP/N Note 5	-70	-	-	mV
Differential Input High Level Threshold Voltage for Clock	V_{THCLK+}	CLKP/N	-	-	70	mV
Differential Input High Level Threshold Voltage for Data	$V_{THDATA+}$	DnP/N Note 5	-	-	70	mV
Single-ended Input Low Voltage	V_{ILHS}	CLKP/N, DnP/N Note 3, Note 5	-40	-	-	mV
Single-ended Input High Voltage	V_{IHHS}	CLKP/N, DnP/N Note 3, Note 5	-	-	460	mV
Differential Termination Resistor	R_{TERM}	CLKP/N, DnP/N Note 5	80	100	125	Ω
Single-ended Threshold Voltage for Termination Enable	$V_{TERM-EN}$	CLKP/N, DnP/N Note 5	-	-	450	mV
Termination Capacitor	C_{TERM}	CLKP/N, DnP/N Note 5, Note 6	-	-	60	pF

Notes:

1. $T_a = -30^{\circ}\text{C}$ to 70°C (to $+85^{\circ}\text{C}$ no damage), $V_{CI} = 2.5\text{V}$ to 6.6V , $V_{DDI} = 1.65\text{V}$ to 3.6V
2. Includes 50mV (-50mV to 50mV) ground difference
3. Without $V_{CMRCLKM450}/V_{CMRDATAM450}$
4. Without 50mV (-50mV to 50mV) ground difference
5. $n = 0$ and 1
6. For higher bit rates, a 14pF capacitor will be needed to meet the common-mode return loss specification.

7.0 OPTICAL CHARACTERISTICS

7.1 Optical Specification (w/HSD FOG+ D65 light)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
White Reflectance (with Polarizer)		Rw (%)	$\Theta=0$ Normal viewing angle —	—	20.10	—	%	(4) Measuring with HSD polarizer , Reference Only Base on Vop=4.2V
Contrast Ratio		CR		—	15	—	—	(1)(2) Base on Vop=4.2V
Color Gamut		S%		—	7	—	%	
Response Time		ms		—	5	7		
Color Chromaticity (CIE1931)	White	W_x		—	(0.300)	—		(1)(4) Measuring with HSD polarizer , Reference Only
		W_y		—	(0.350)	—		
	Red	R_x		—	(0.413)	—		
		R_y		—	(0.429)	—		
	Green	G_x		—	(0.301)	—		
		G_y		—	(0.409)	—		
	Blue	B_x		—	(0.208)	—		
		B_y		—	(0.276)	—		
Viewing Angle	Hor.	Θ_L	CR>2	—	60	—	—	(1)(4) Measuring with HSD polarizer , Reference Only
		Θ_R		—	60	—		
	Ver.	Θ_U		—	60	—		
		Θ_D		—	60	—		

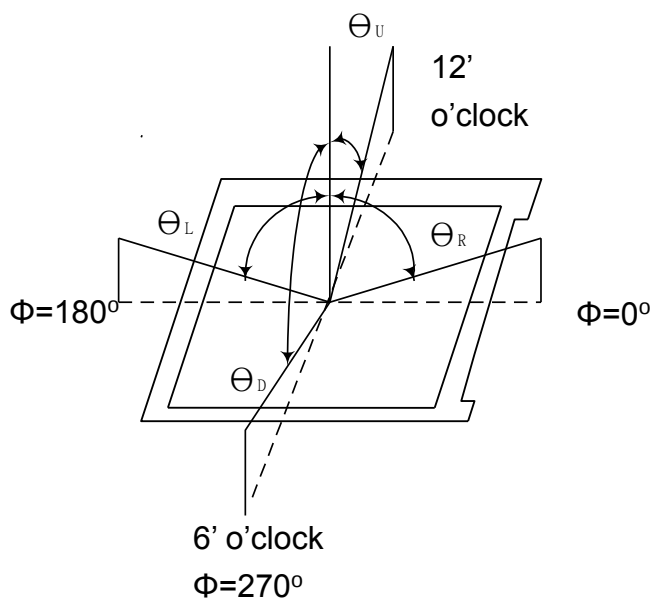
7.2 Measuring Condition

- Measuring surrounding : dark room
- Ambient temperature : $25\pm 2^{\circ}\text{C}$
- 15min. warm-up time.

7.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

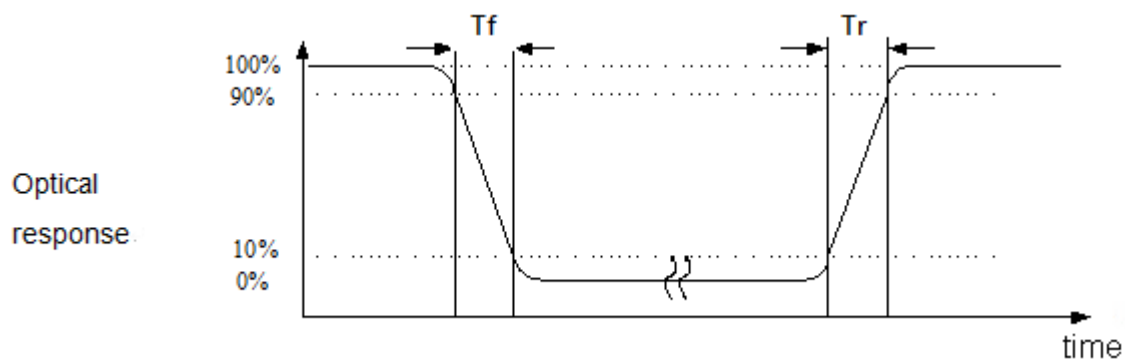
Note (1) Definition of Viewing Angle:



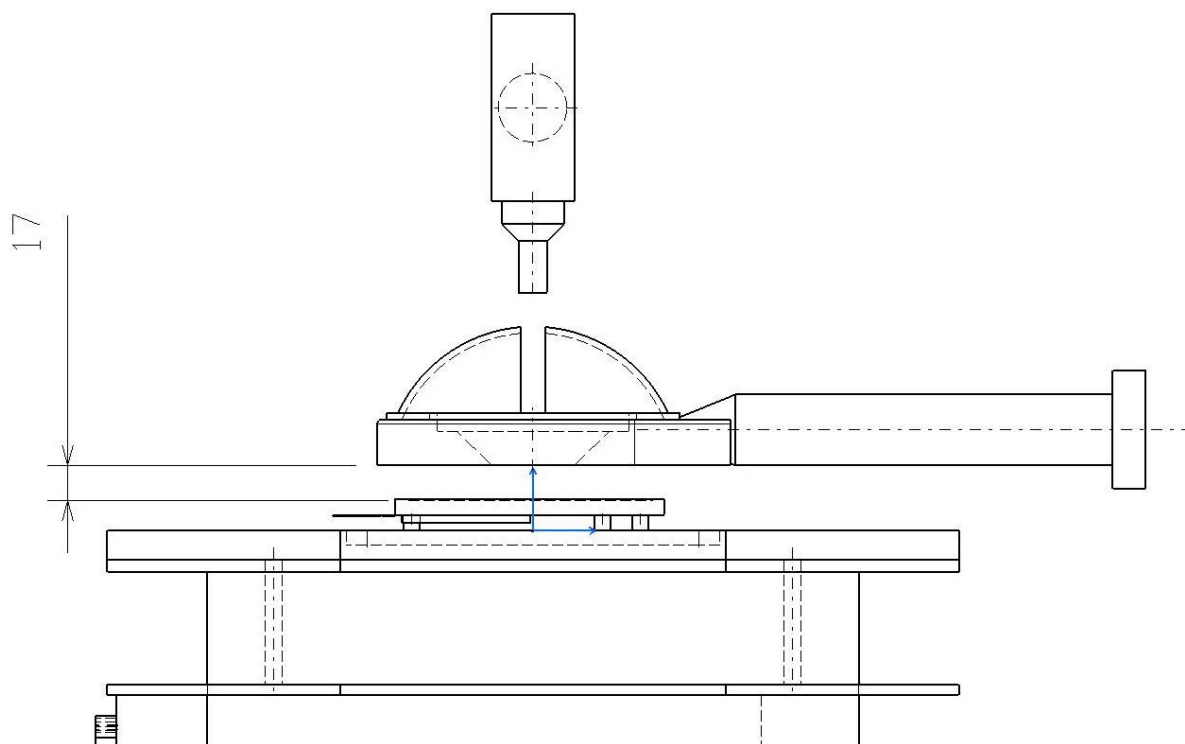
Note (2) Definition of Contrast Ratio (CR) :
measured at the center point of panel

$$\text{CR} = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3) Definition of Response Time : Sum of T_R and T_F



Note (4) Definition of optical measurement setup



8. Reliability test items

No.	Test Item	Test Condition	Notes
1	High Temp. Storage	+80°C / 48H	1. Functional test is OK. Missing Segment, short, unclear segment non-display, display abnormally and liquid crystal leakage un-allowed. 2. No low temperature bubbles, end seal loose and fall, frame rainbow.
2	Low Temp. Storage	-30°C / 48H	
3	High Temp. Operating	+70°C / 48H	
4	Low Temp. Operating	-20°C / 48H	
5	High Temperature / Humidity storage	50°C x 90%RH / 48H	
6	Thermal and cold shock	Static state, -20°C (30min) ~60°C (30min), 50 cycles	
7	ESD test	±2KV, Human Body Mode, 150pF/330Ω; ±4KV, Air Mode, 150pF/330Ω;	

Note: All tests above are practiced at module type.

There is no display function NG issue occurred, All the cosmetic specification is judged before the reliability stress.

9.0 General Precaution

9.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

9.2 Assembly Precaytton

- 1、 Please use the mounting hole on the module side in installing and do not bending or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- 2、 Please design display housing in accordance with the following guide lines.
- 3、 Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- 4、 Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. The clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- 5、 Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- 6、 Please do not press any parts on the rear side such as source IC, gate IC, and FPC during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- 7、 Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- 8、 Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change. 11.2.7 Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

9.3 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. Leadtek does not warrant the module, if customers disassemble or modify the module.

9.4 Breakage of LCD Panel

- 1、 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 2、 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 3、 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 4、 Handle carefully with chips of glass that may cause injury, when the glass is broken.

9.5 Absolute Maximum Ratings and Power Protection Circuit

- 1、 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 2、 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 3、 It's recommended employing protection circuit for power supply.

11.6 Operation

- 1 、 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 2、 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 3 、 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 4 、 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

9.6 Static Electricity

- 1 、 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 2、 Because LCD module uses CMOS-IC on TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 3 、 Persons who handle the module should be grounded through adequate methods.

9.7 Disposal

When disposing LCD module, obey the local environmental regulations.

9.8 OTHERS

- 1 、 A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight land strong UV rays.
- 2、 Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- 3、 For the packaging box, please pay attention to the followings:
- 4、 Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- 5、 Please do not pile them up more than 6 boxes. (They are not designed so.) And please do not turn over.
- 6、 Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- 7、 Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

10.0 Packing form-TBD



深圳市丽台电子有限公司

Shenzhen Leadtek Electronics Co.,Ltd

Incoming Inspection Standards

来料检验标准

Model No. / 产品型号: Applies 5.5~10.0 Inch Touch Display ScreenUpdated Date / 生效日期: 2022-05-20Version / 版本: A0

Customer confirmation : _____

Record of Revision / 修订履历

Version / 版本	Revision Record / 修订内容	Reviser / 修订人	Revision Date / 修订日期
V0	首发 / Starting	Green	2022.05.20

1.Scope of application /适用范围.

This document shall be applied to 5.5~10.0 inch touch display screen.

本文件适用于5.5~10.0 寸触摸显示屏.

2.Inspection conditions and environment /检验条件与环境.

2.1 Inspection Conditions /检验条件:

(1) Inspection Distance /检测距离: 35cm ±5cm.

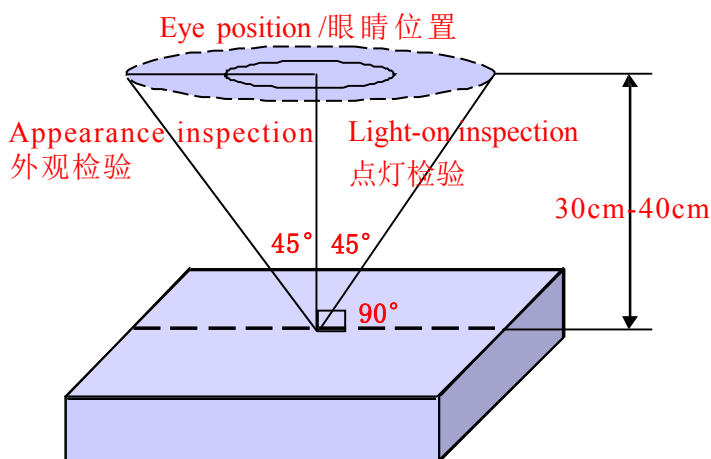
(2) Check time /检验时间:

Displays performance test /功能测试: 2~3S /Image, Cosmetic Inspection /外观检验:10~12S.

(3) Check the viewing angle /检验视角:

Light-on Inspection Angle /点灯检验角度: ±45°.

Cosmetic Inspection Angle /外观检验角度: ±45°.



(Perpendicular to LCD panel surface /垂直于LCD表面)

2.2 Inspection environment /检验环境:

Ambient Temperature 温度		25°C±5°C
Ambient Humidity 湿度		55±5%RH
Ambient Illumination 亮度	Cosmetic Inspection 外观检验	800-1000 Lux
	Functional Inspection 点灯检验	200~300Lux

2.3 Sampling Conditions /抽样条件:

(1) Quantity to be inspected /批量: Quantity of shipment lot per model /单次运送单一型号数量.

(2) Sampling method /抽样方法:

Sampling Plan /抽样计划		GB/T 2828.1- 2003
		Normal Inspection , Single Sampling 正常检验、单次抽样
		General inspection level: II 一般检验水平: 二级
AQL	Major Defect /主要缺陷	0.65
	Minor Defect /次要缺陷	1.0

(3) The classification of Major(MA) and Minor(MI) defects is shown as “3.1 Classification of defects” .
主缺(MA)及次缺(MI)定义于”3.1缺陷分类”.

3.Terms And Definitions /术语和定义

3.1 Classification of defects / 缺陷分类 :

(1) Major defects /主要缺陷:

A major defect is a defect that is likely to result in failure, or to reduce materially the usability of the product for its intended purpose .

可导致产品功能失效或减少产品可用性的缺陷.

(2) Minor defects /次要缺陷:

It will not cause the product to fail and reduce the defects in the effective use and operation of the product.

不会导致产品功能失效和减少产品的有效使用与操作的缺陷.

3.2 Point defects /点状缺陷:

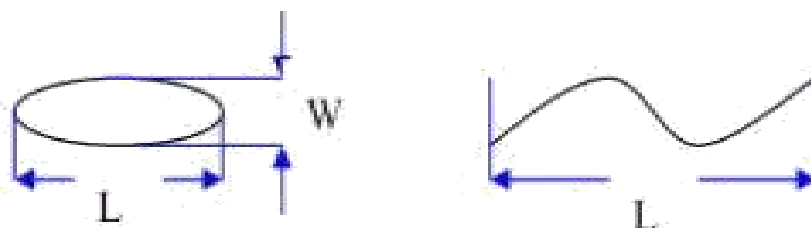
The size of the point defect is defined by the diameter D, and the average diameter of the defect is $D=1/2 (W+L)$.

点状缺陷的大小是由直径 D 定义的, 缺陷的平均直径 $D=1/2(W+L)$.

3.3 Linear defects /线状缺陷:

When defect size $L \geq 2W$, the defect count as liner type defect. Size of linear defect is defined by length (L) and the maximum width (W).

当缺陷尺寸 $L \geq 2W$ 时, 被视为线状缺陷, 线状缺陷是由长度 (L) 和最大宽度 (W) 定义的.



3.4 LCD sub-pixel dot /LCD子像素点

(1) Definition /定义 : The point defect area is greater than 50% of the LCD sub-pixel area, and is visible through ND5% filter masking .

子像素点缺陷面积大于 50% LCD子像素面积, 且透过 ND5%遮盖是可见的.

(2) The drawing of 1/2 area sub-pixel definition / 1/2 面积的子像素定义绘图:

The 1/2 area sub-pixel can be defined as below one or more of specific shapes

1/2 面积的子像素可以定义为如下一个或多个特定形状图:



3.5 Small bright dot /细碎亮点 :

Point defects smaller than "LCD sub-pixels" /小于“LCD子像素点”的点缺陷.

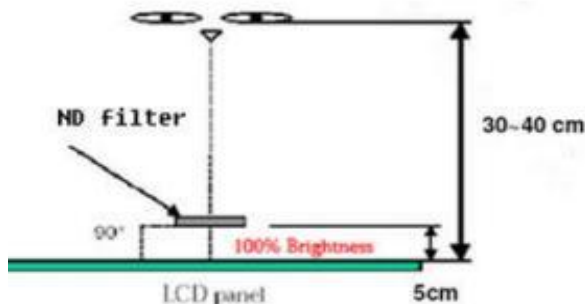
(Ratio of Zone I to Zone O / I 区与 O 区比例: 1: 2: 1)



3.6 ND filter inspection method /ND卡的检验方法:

Hold the ND filter about 5cm above the display area, with your eyes 30-40cm away from the panel, and observe for 2~3 seconds.

在显示区域上方大约 5cm 处握住 ND 卡, 眼睛距离面板 30-40cm, 观察2~3 秒.



3.7 Any FPC surface problems that do not leak copper on the surface and do not cause functional failure are acceptable.

任何 FPC 表面问题, 表面未露铜和不造成功能失效是可以接受.

3.8 Extraneous substances that can be wiped out , like Finger point,Particles are not considered as a defect .

可以被擦拭干净的表面物质不视为缺陷 (如手指印, 尘粒) .

3.9 Defects that can be covered by the material and are not visible in appearance are not considered defects.

能被物料覆盖，外观不可见的缺陷不视为缺陷。

3.10 Panel damage /面板损伤:

Glass damage outside the AA display area that does not affect the effective wiring is acceptable.

AA 显示区域以外的玻璃损伤，不影响有效线路是可以接受的。

3.11 Issues not specified or defined in this acceptance standard shall be handled through friendly negotiation between the two parties.

本允收标准中未规定或定义的问题，双方友好协商处理。

4. Inspection standards /检验标准

4.1 Structural Dimensions /结构尺寸规格

Serial Number 序号	Measurement items /测量项目		Specification /规格	Remark /备注
	名称 /Name	Unit /单位	Tolerance /公差	
1	Outside dimension: Length 尺寸: 长	mm /毫米	0.15mm~0.30mm	Please refer to the product specification for detailed dimensions and tolerances 详细的尺寸规格和公差请参考产品规格书
2	Outside dimension: Width 尺寸: 宽	mm /毫米	0.15mm~0.30mm	
3	Outside dimension: Thickness 尺寸: 高	mm /毫米	0.20mm~0.50mm	

4.2 Appearance Inspection Specification /外观检验规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass 玻璃	Wire(on Array) 线路	Can't be damaged 不能损伤	MA	
	Chipping/corner breaking 崩边/破角	Can't affect the effective lines and functions 不能影响有效线路和功能	MA	
	Edge 边缘	There must be no extensional cracks 不可有延伸性裂纹	MA	
Silicone 硅胶	Silicone coating 硅胶涂布	The height must not exceed the LCD CF surface 高度不能超过LCD CF面		MI
	Glue overflow 溢胶	Can't cover FPC, POL, etc 不能覆盖到FPC、POL等		MI

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
PCBA FPC Connector 连接器	Appearance 外观	Scratches or injuries are not allowed to cause copper exposure 划伤或损伤不允许表面出现露铜		MI
	Component 元器件	Can't be damaged and lack 不能损伤和缺少	MA	
	Goldfinger oxidation 金手指氧化	Not allowed 不允许		MI
	Connection status 连接状况	The connection must be accurate and stable 必须准确稳定连接	MA	
	Break 破裂	Not allowed 不允许	MA	
	Soldering, false soldering/tinning/tin beads 假焊/连锡/锡珠	Not allowed 不允许	MA	
POL 偏光片	Scratches 划伤	1. $W \leq 0.07\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.07\text{mm} < W \leq 0.12\text{mm}$; $L \leq 5\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.12\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许)		MI
	Dent 凹凸印	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Bubbles 气泡	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Point defects 点状不良	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Edge bubbles 边缘气泡	1. Within 1/2BM of the display area, it is not allowed 显示区往外 1/2BM 区域内, 不允许 2. The display area is 1/2 outside the BM area, and it is not controlled 显示区往外1/2BM区域以外, 不管控		MI
	Dirty/watermarked 脏污/水印	No dirt/water lines/finger marks are allowed, and must be wiped clean 不允许有脏污/水纹/手指印, 须擦拭干净方可		MI
	Warping 起翘	Not allowed 不允许		MI
	Attaching offset 贴偏	It is necessary to completely cover the display area outward, within the 1/2BM area, or without leaking POL edges after TP is attached 需完整覆盖显示区往外、1/2BM区以内或贴合TP后不会出现漏偏光片边缘		MI
	Mixture 混料	Mixing different types of POL or not using POL as required by the BOM, not allowed 不允许混贴不同型号的POL或未按BOM要求使用POL	MA	

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
TP&CG	Point defects 点状不良	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
	Scratches 划伤	1. $W \leq 0.07\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.07\text{mm} < W \leq 0.12\text{mm}$; $L \leq 5\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.12\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许) 4. There is a feeling scratch, Not allowable 有感划伤, 不允许		MI
	Edges and corners cracked 崩角/崩边	1. Product front / 产品正面: Edge and corner chipping is not allowed 崩角、崩边不允许 2. Product back / 产品背面: $X \leq 0.5$, $Y \leq 0.5$, $Z \leq 1/2T$; $N \leq 4$; $DS \geq 10\text{mm}$		MI
	Silk screen 丝印	The silk screen is clear, complete and correct 丝印清晰、完整、内容正确		MI
	Dirty 脏污	Non-wipeable dirt, not allowed 不可擦拭的脏污, 不允许		MI
	Broken 破损	Not allowable 不允许	MA	
	Ink color aberration 油墨色差	$\Delta E > 1$, Not allowable (不允许)		MI
	Cover pinholes 针孔	1. $D \leq 0.15\text{mm}$, $N \leq 4$, $DS \geq 10\text{mm}$, allowable 2. $D > 0.15\text{mm}$, intensive pinholes (密集型针孔), Not allowable (不允许)		MI
	IR holes IR孔	Dirt, deviation, color difference, etc. are not allowed 不允许脏污、偏位、色差等		MI
BL 背光	Backlight separation 背光分离	Not allowable 不允许		MI
	Deformation of rubber iron and rubber frame 胶铁、胶框变形	Use the plug gauge 0.3mm on the flat surface and can snap in and judge NG 在平面上使用塞规0.3mm卡翘曲位置, 能卡进判定NG		MI
	The iron frame is oxidized and not tightened 铁框氧化、卡不紧	Not allowable 不允许		MI
	Backlight sticky solder beads, glue, etc 背面粘锡珠、残胶等	Not allowable 不允许		MI
	Inkjet coding, Barcode, QR code 喷码/条码/二维码	The Inkjet coding is clear and complete, the barcode and QR code can be scanned normally, and the content and format match 喷码清晰完整、条码和二维码可正常扫描, 内容和格式相符		MI
	Accessories(protective film, double-sided tape, insulating adhesive, etc.) 辅料(保护膜、双面胶、绝缘胶等)	Defects such as missing pastes, sticking deviations, defects, and fractures are not allowed 不允许有漏贴、贴偏、残缺、断裂等缺陷		MI

4.3 Electrical test specifications /电性检查规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass bright spots/dark spots 玻璃亮点/暗点	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
Mura	Use ND5% filter masking, visual invisibility is OK, 200~300Lux 使用ND5%遮盖, 目视不可见即为OK, 200~300Lux		MI
Small bright dot 细碎亮点	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
Light leakage 漏光	1. Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK 2. If necessary, sign off on the sample 必要时, 签限定样		MI
Backlight black/white dots 背光黑点/白点	1. $D \leq 0.20\text{mm}$, Ignore (忽略) 2. $0.20\text{mm} < D \leq 0.40\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.40\text{mm} < D$, Not allowable (不允许)		MI
Linear foreign bodies 线状异物 (异物毛丝等)	1. $W \leq 0.07\text{mm}$; $L \leq 5\text{mm}$, Ignore (忽略) 2. $0.07\text{mm} < W \leq 0.12\text{mm}$; $L \leq 5\text{mm}$; $N \leq 4$; $DS \geq 10\text{mm}$ 3. $0.12\text{mm} < W$; $5\text{mm} < L$, Not allowable (不允许)		MI
Black/White Print 黑印/白印	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The display is uneven 显示不均匀	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The brightness is uneven 亮度不均匀	Brightness uniformity $< 85.0\%$, Not allowable 亮度均匀性 $< 85.0\%$, 不允许		MI
Displacement of the membrane 膜材移位	Not allowable 不允许		MI
Interference pattern/Newtonian pattern 干涉纹/牛顿纹	Not allowable 不允许		MI
Display abnormal 显示异常	Not allowable 不允许	MA	
No display 无显示	Not allowable 不允许	MA	
Line/Missing Drawing 线条/缺画	Not allowable 不允许	MA	
Splash screen 闪屏	Not allowable 不允许	MA	
LCD grid LCD网格	Not allowable 不允许	MA	
Afterimage 残影	Not allowable 不允许	MA	
Wrong viewing angle 视角错误	Not allowable 不允许	MA	
No touch 无触摸	Not allowable 不允许	MA	
Touch the jump point 触摸跳点	Not allowable 不允许	MA	
Not sensitive 触摸不灵敏	Not allowable 不允许	MA	